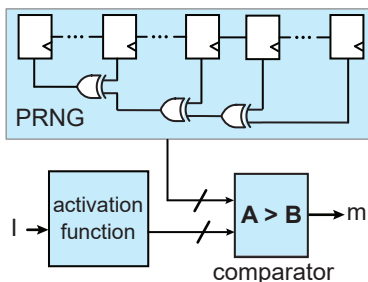


Hardware implementations of probabilistic bits

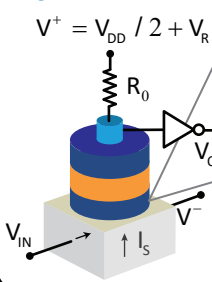
Digital

CMOS based

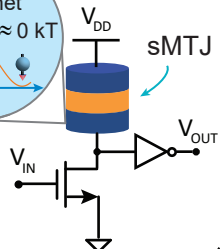


Mixed-signal

SOT based

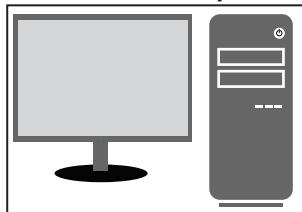


STT based



Hybrid Probabilistic - Classical Computer

Classical Computer



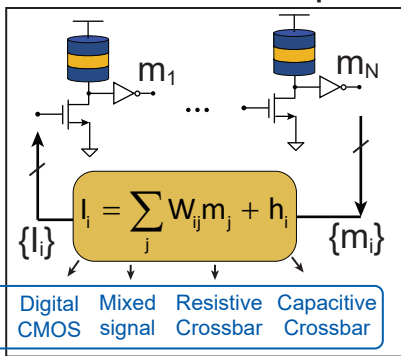
Samples



Weights



Probabilistic Computer



Options for synapse



Digital CMOS Mixed signal Resistive Crossbar Capacitive Crossbar