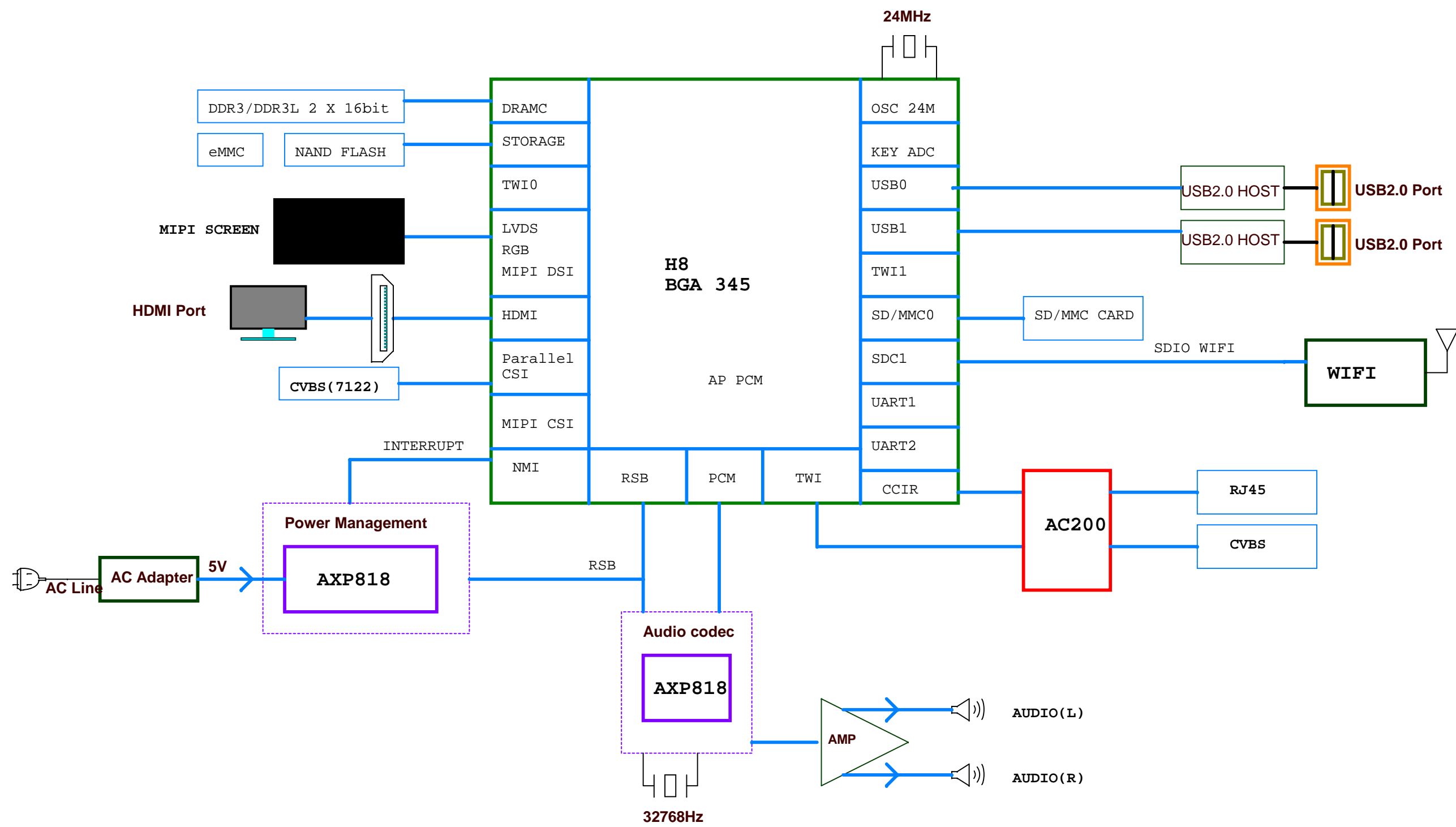


REVISION HISTORY

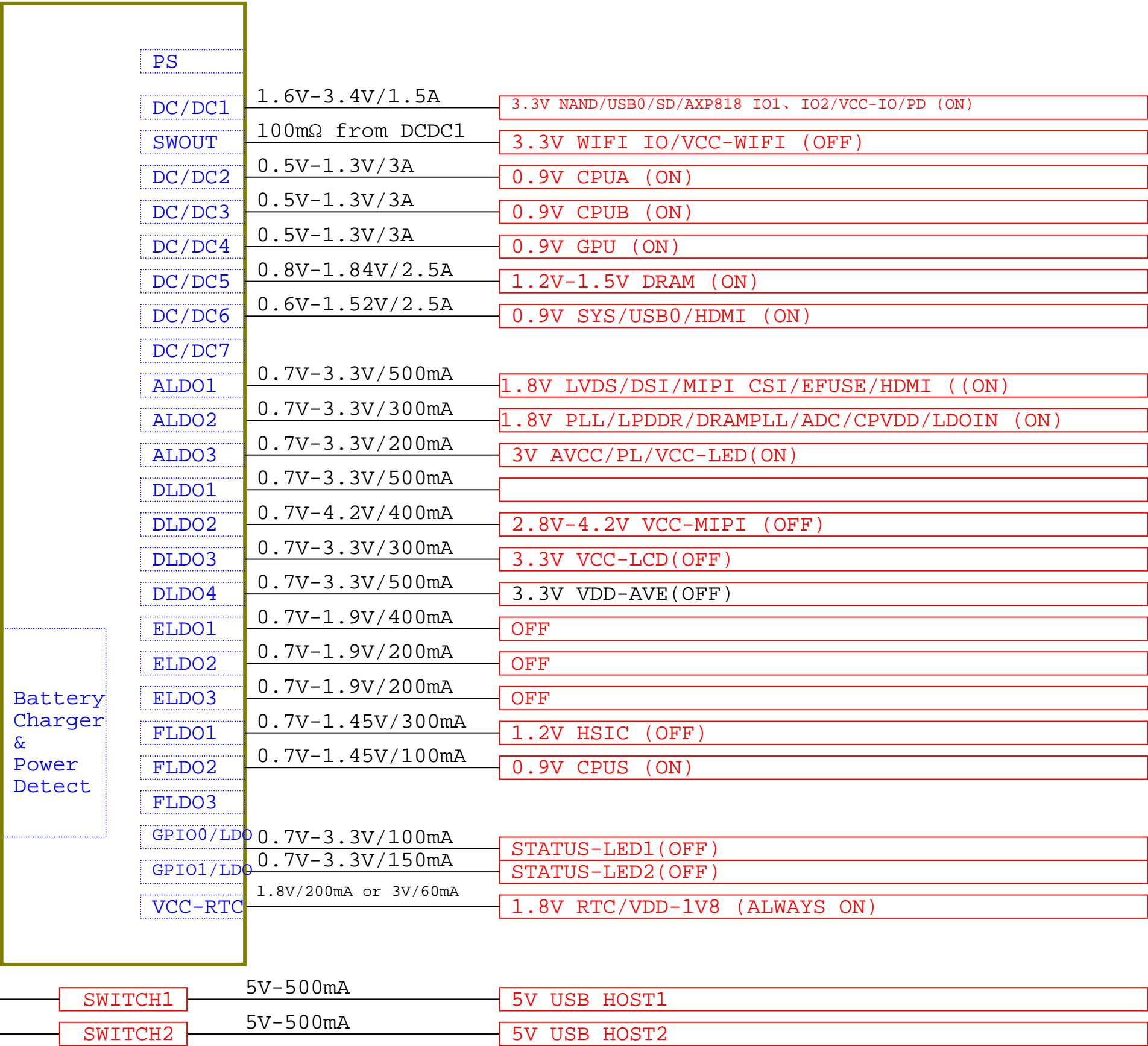
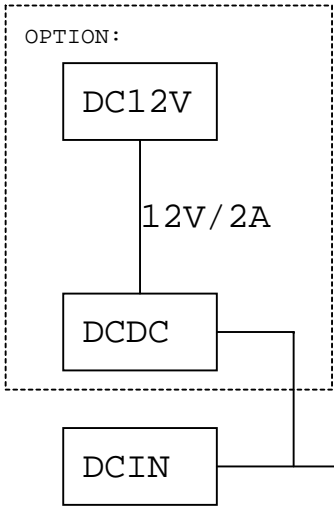
Schematics Index:

- P01: REVISION HISTORY
- P02: BLOCK
- P03: POWER TREE
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Revision	Description	Date	Drawn	Checked
Ver 1.0	Draft	2014-10-29		
Ver 2.0		2015-04-07		



POWER TREE



GPIO ASSIGNMENT

PIN	Define	CFG	Function
PB0			
PB1	ID2		ID
PB2	ID3		
PB3	AVE-IRQ		AC200
PB4	I2S-LRCK	3	CODEC
PB5	I2S-BCLK	3	
PB6	I2S-DOUT	3	
PB7	I2S-DIN	3	
PB8	I2S-MCLK	3	
PB9	UART0-TX	2	DEBUG
PB10	UART0-RX	2	

PIN	Define	CFG	Function
PC0	NWE	2	NAND /eMMC /tSD
PC1	NALE	2	
PC2	NCLK	2	
PC3	NCE1	2	
PC4	NCE0	2	
PC5	NRE/CLK	2/3	
PC6	NRB0/CMD	2/3	
PC7	NRB1	2	
PC8	NDQ0/D0	2/3	
PC9	NDQ1/D1	2/3	
PC10	NDQ2/D2	2/3	
PC11	NDQ3/D3	2/3	
PC12	NDQ4/D4	2/3	
PC13	NDQ5/D5	2/3	
PC14	NDQ6/D6	2/3	
PC15	NDQ7/D7	2/3	
PC16	NDQS/RST	2/3	
PC17	NCE2	2	
PC18	NCE3	2	

PIN	Define	CFG	Function
PD2	GRXD3	2	EMAC (AC200)
PD3	GRXD2	2	
PD4	GRXD1	2	
PD5	GRXD0	2	
PD6	GRXCK	2	
PD7	ERXDV	2	
PD10			
PD11	GTXD3	2	
PD12	GTXD2	2	
PD13	GTXD1	2	
PD14	GTXD0	2	
PD15	ECRS	2	
PD18	ETXCK	2	
PD19	ETXEN	2	
PD20			
PD21	ECOL	2	
PD22	GMDC	2	
PD23	GMDIO	2	
PD24			
PD25			
PD26			
PD27			
PD28	AVE-24M	1	AC200
PD29	LCD-BL-EN	1	LCD

PIN	Define	CFG	Function
PE0	PCLK	2	CVBS (AC200)
PE1			
PE2	HSYNC	2	
PE3	VSYNC	2	
PE4			
PE5			
PE6	D2	2	
PE7	D3	2	
PE8	D4	2	
PE9	D5	2	
PE10	D6	2	ID
PE11	D7	2	
PE12	D8	2	
PE13	D9	2	
PE14			
PE15			
PE16	ID4		
PE17	ID5		
PE18	OWA-OUT	1	OWA
PE19			

PIN	Define	CFG	Function
PF0	D1	2	CARD0
PF1	D0	2	
PF2	CLK	2	
PF3	CMD	2	
PF4	D3	2	
PF5	D2	2	
PF6	SDC0-DET	0	

PIN	Define	CFG	Function
PG0	WL-SDIO-CLK	2	WIFI
PG1	WL-SDIO-CMD	2	
PG2	WL-SDIO-D0	2	
PG3	WL-SDIO-D1	2	
PG4	WL-SDIO-D2	2	
PG5	WL-SDIO-D3	2	
PG6			
PG7			
PG8			
PG9			
PG10			
PG11			
PG12			
PG13			

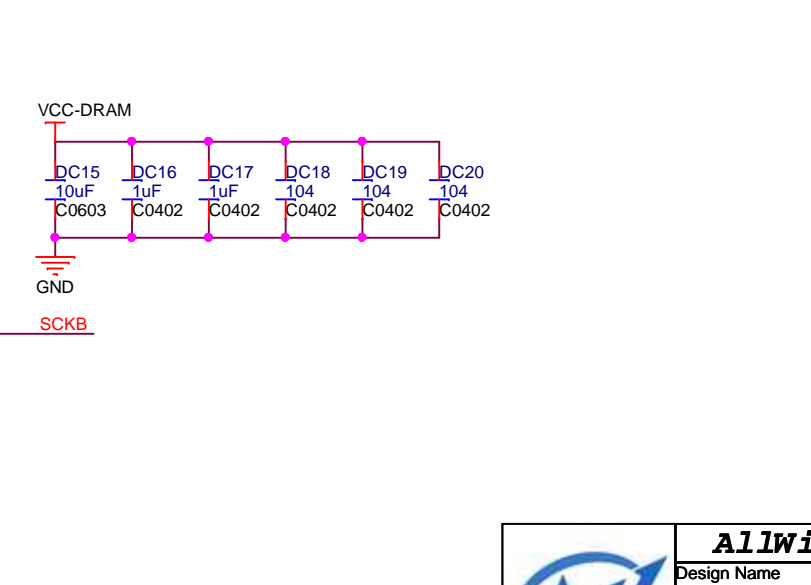
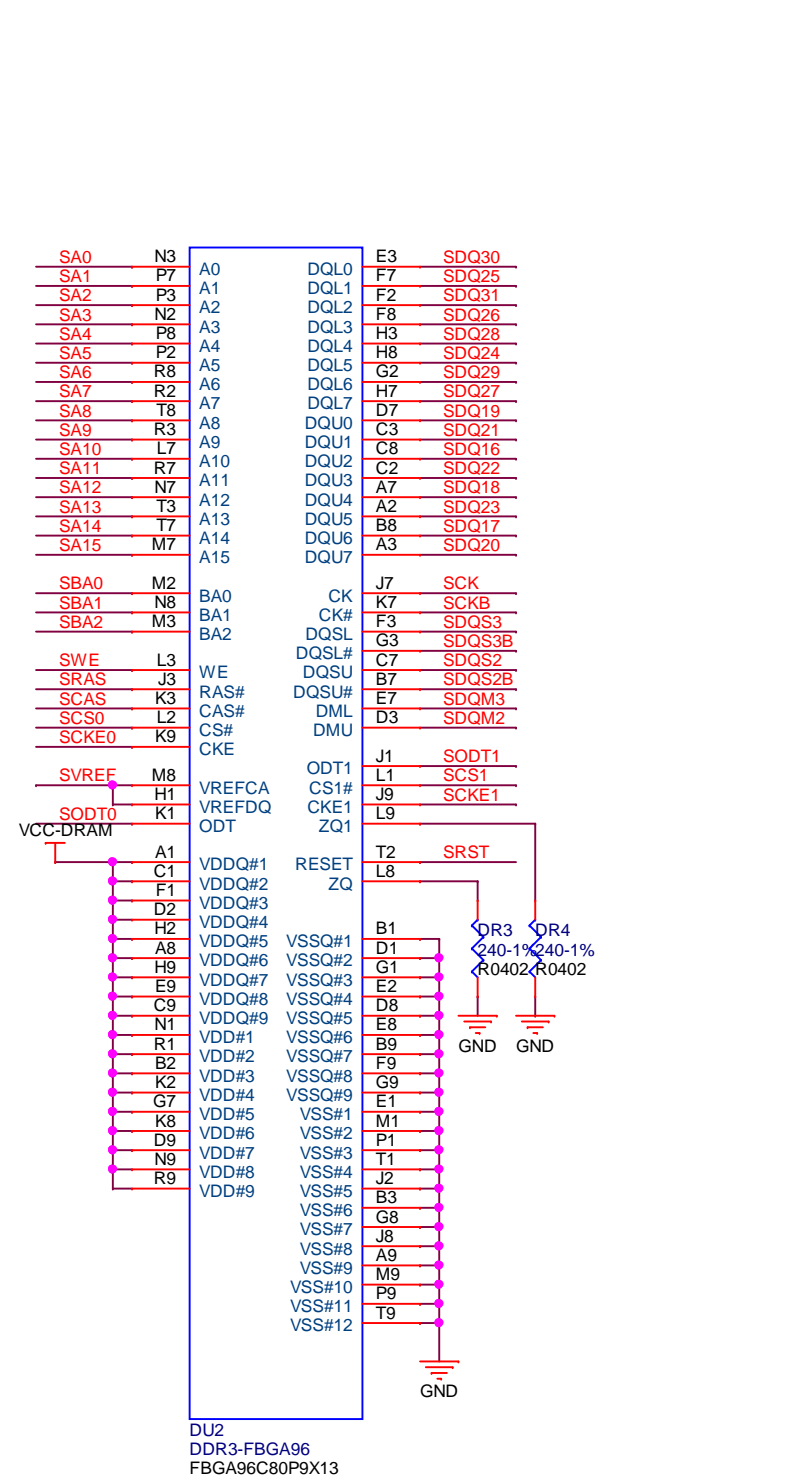
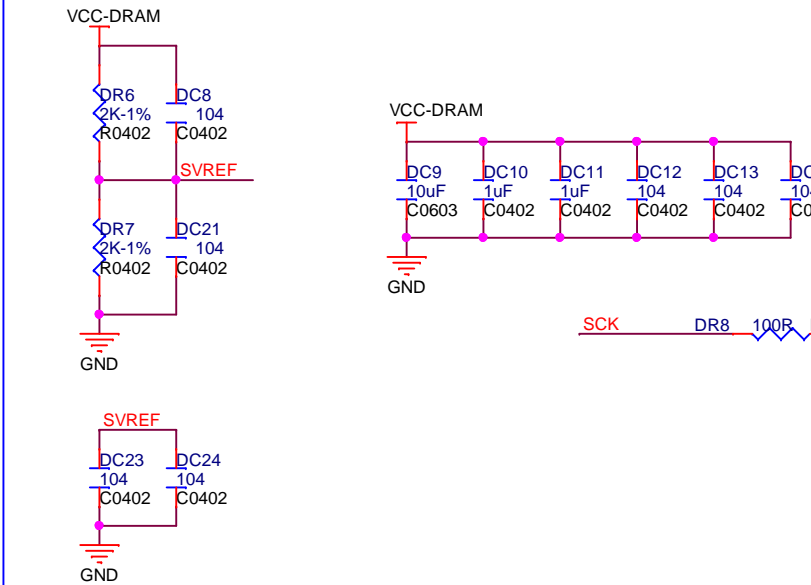
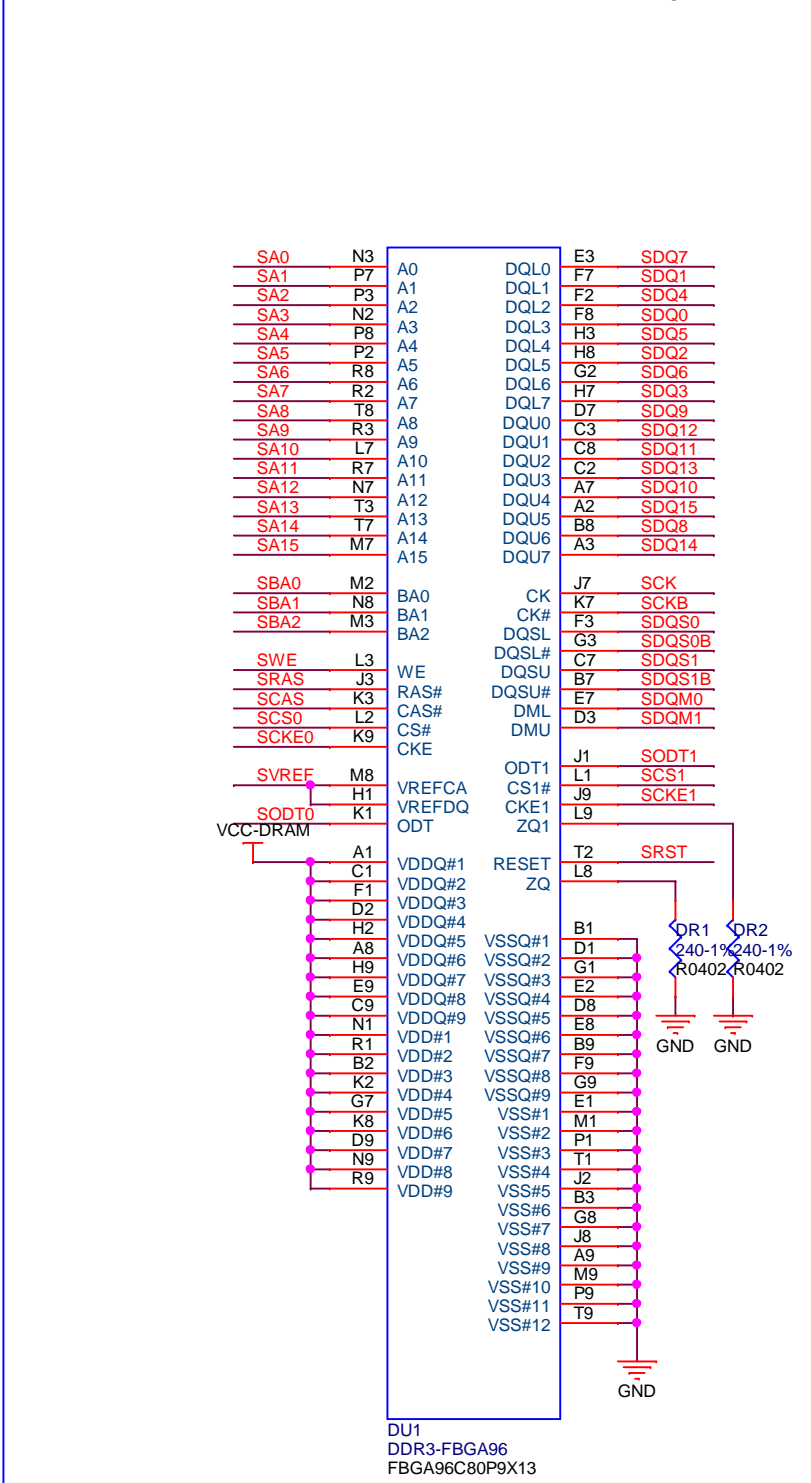
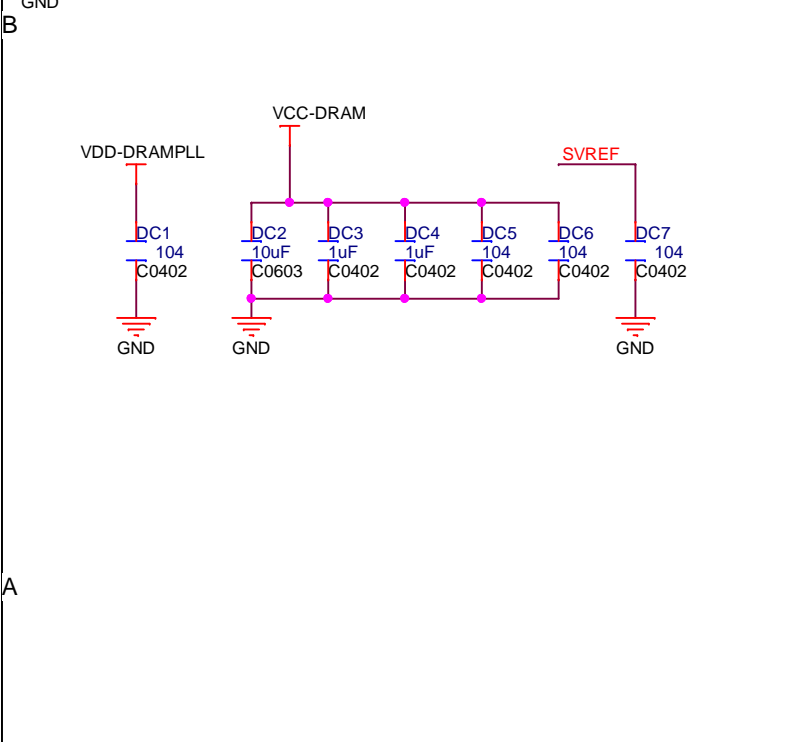
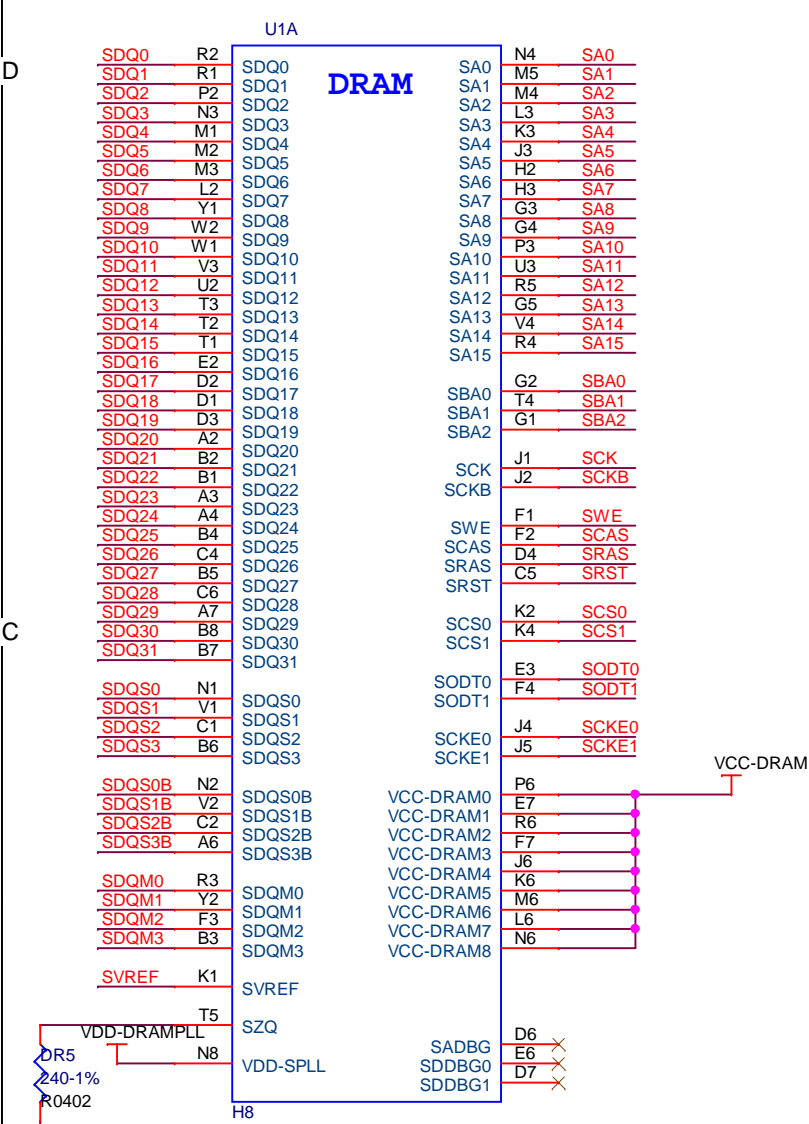
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PH0	TWI0-SCK	2	CVBS
PH1	TWI0-SDA	2	EEPROM
PH2	ID0		ID
PH3	ID1		
PH4	LCD-RST		LCD
PH5			
PH6	HSCL	2	HDMI
PH7	HSDA	2	
PH8	HCEC	2	
PH9			
PH10			
PH11			

PIN	Define	CFG	Function
PL0	RSB-SCK	2	PMIC
PL1	RSB-SDA	2	
PL2	WL-PMU-EN	1	WIFI
PL3	WL-WAKE-AP	0	
PL4			
PL5	USB0-DRVVBUS	1	USB0
PL6	USB1-DRVVBUS	1	USB1
PL7	MUTE	1	AUDIO
PL8			
PL9	RECOVERY	2	KEY
PL10			
PL11	IRQ-AUDIO	6	CODEC
PL12	IR-RX	2	IR

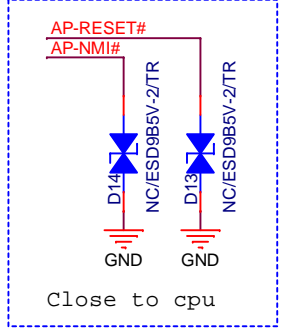
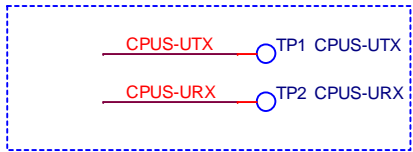
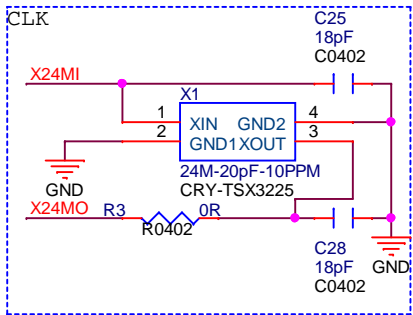
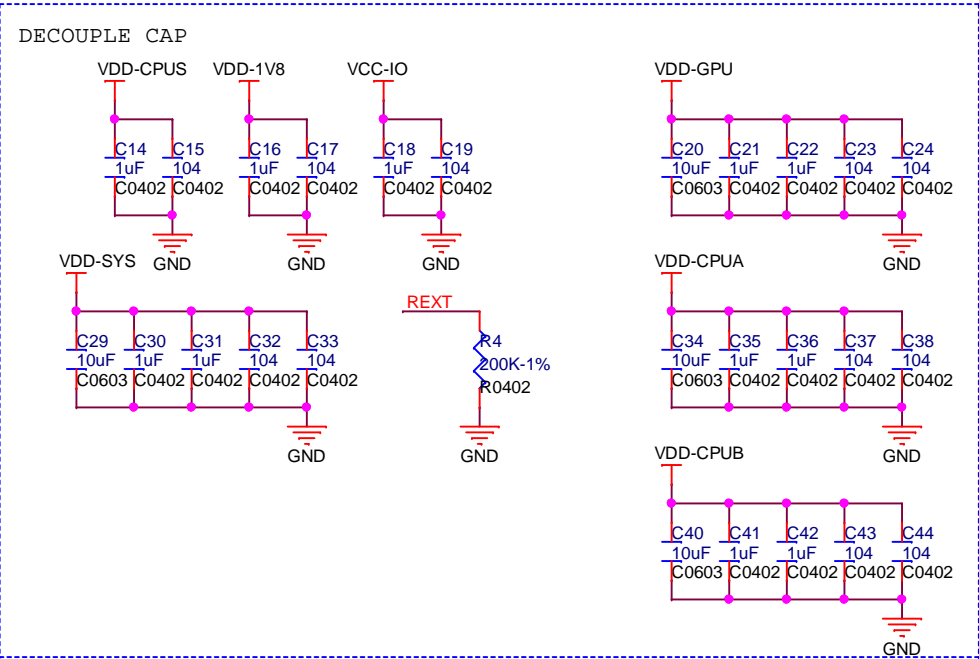
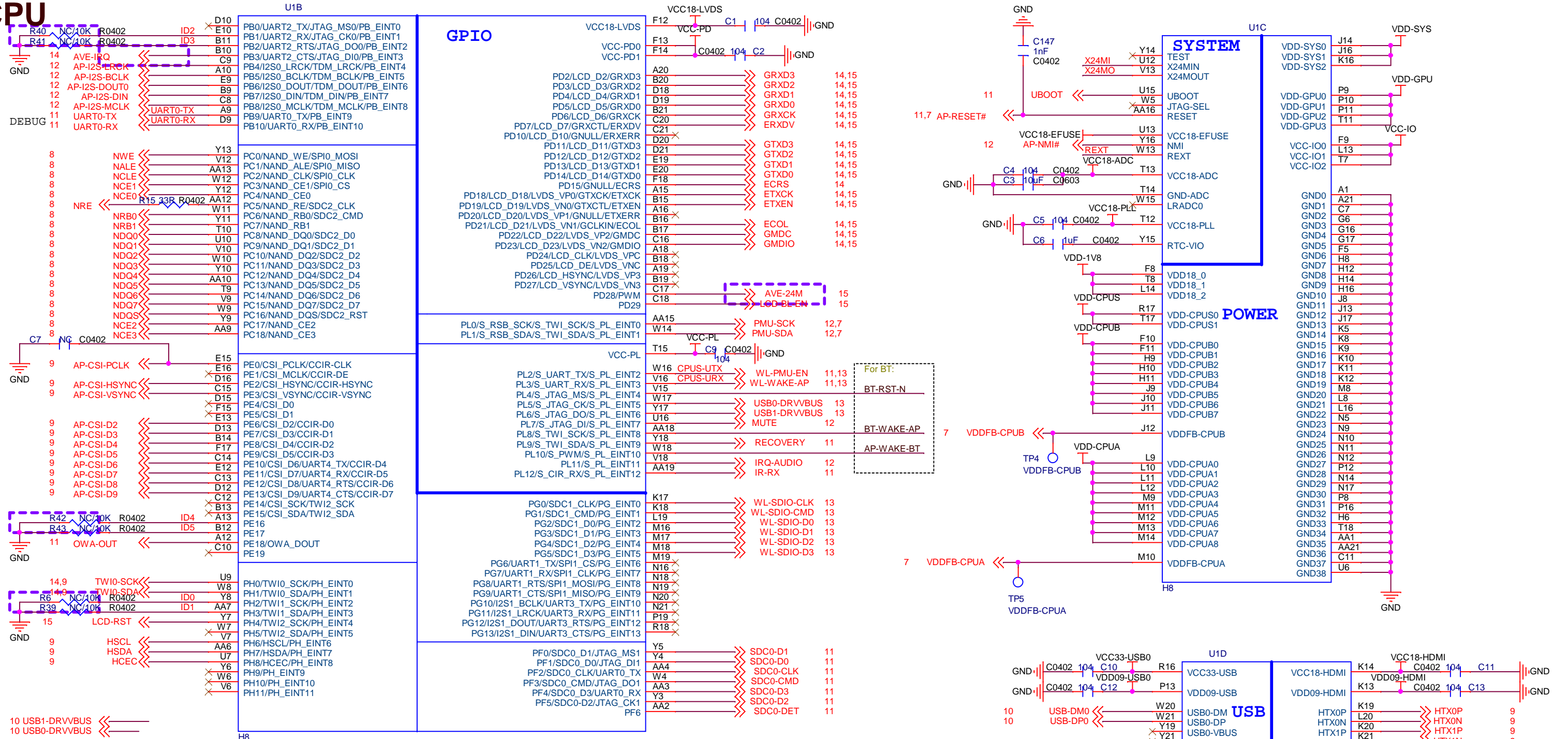
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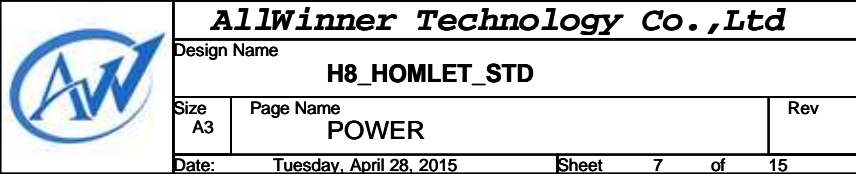
PIN	Define	Function
GPIO0		
GPIO1		

DDR3_16X2

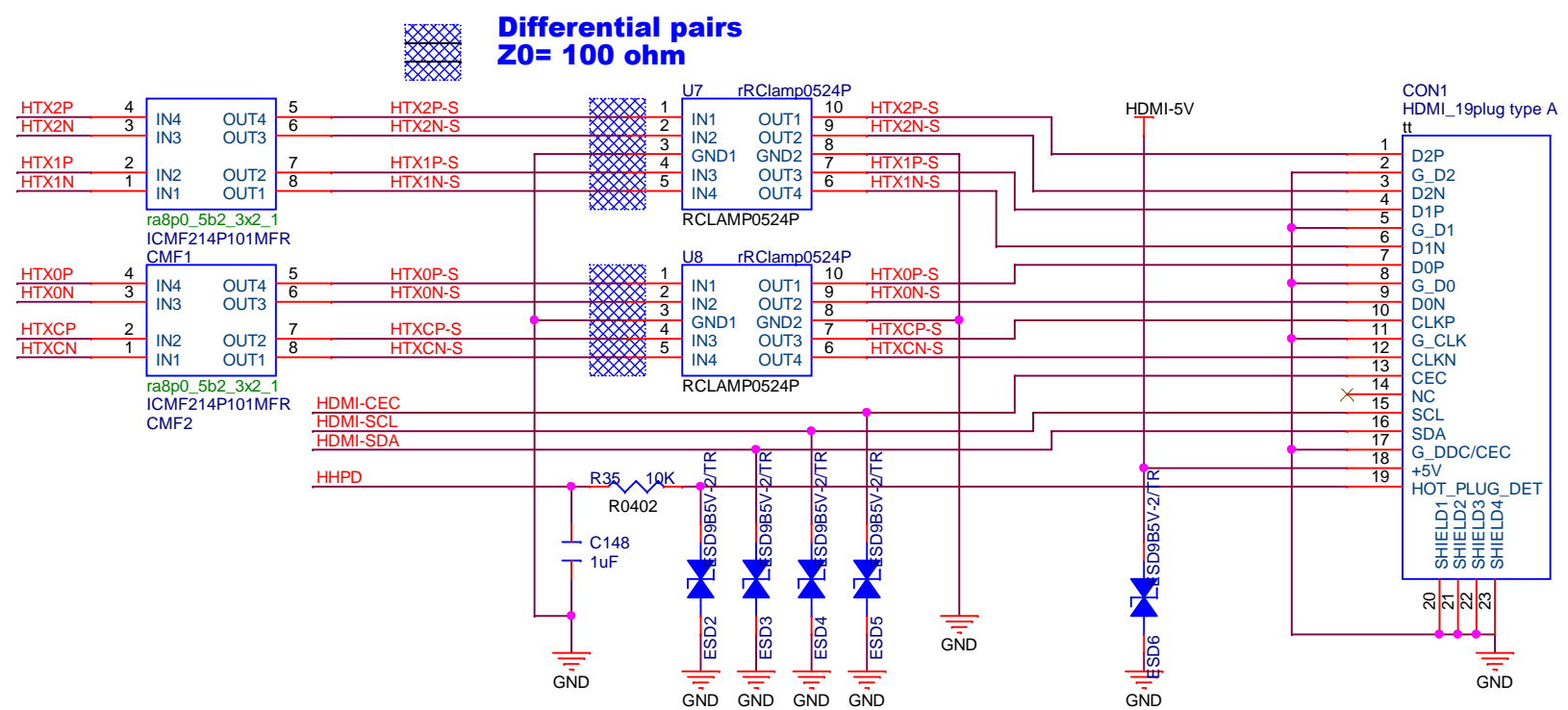
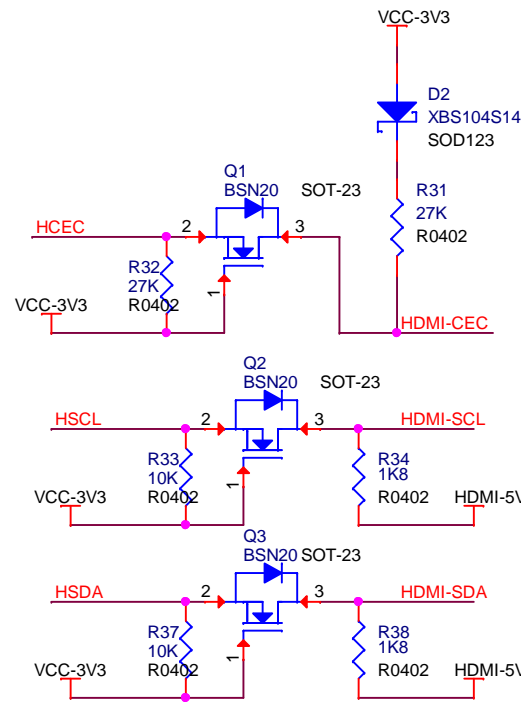
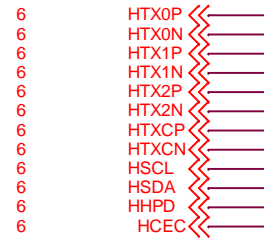


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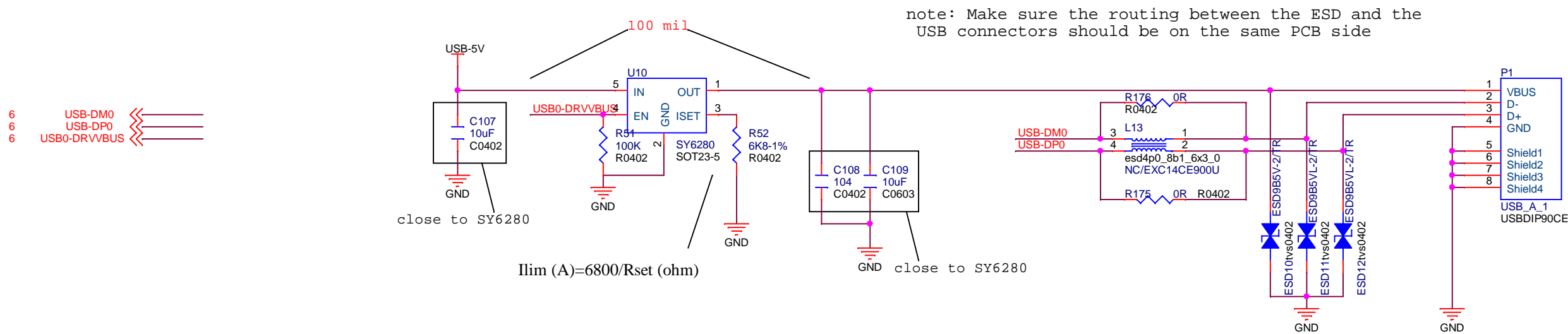




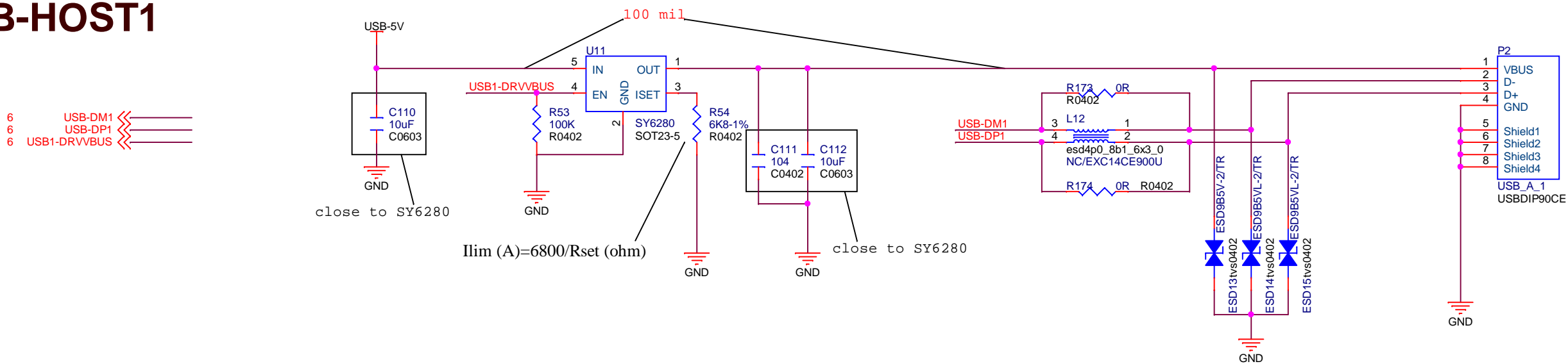
HDMI



USB-HOST0



USB-HOST1



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Design Name

H8_HOMLET_STD

Size
A3

Page Name

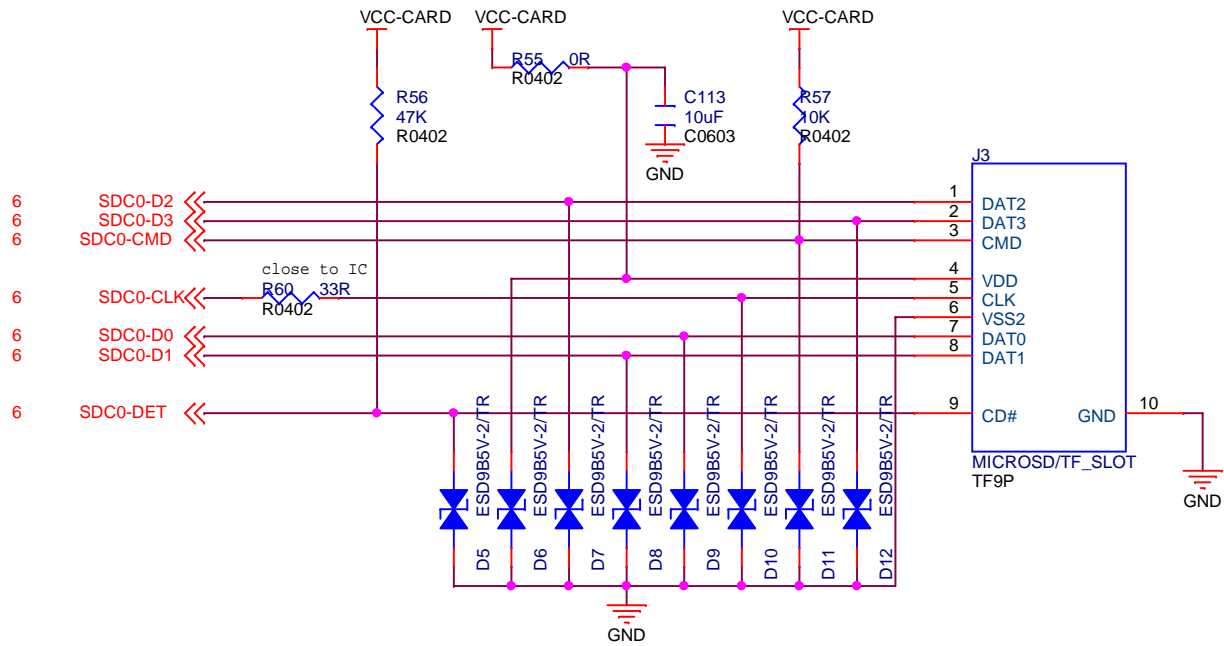
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Rev

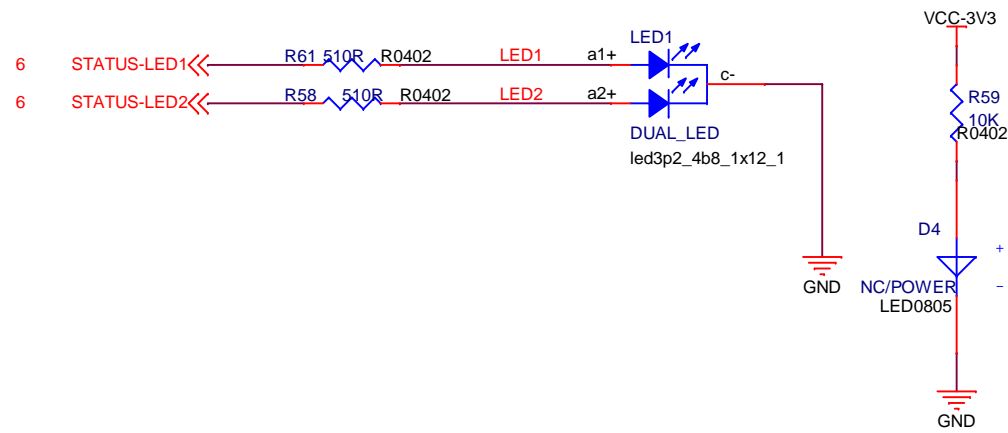
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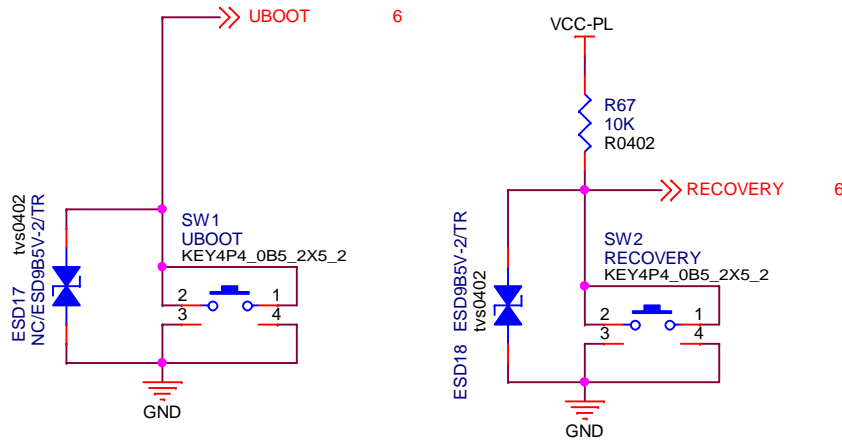
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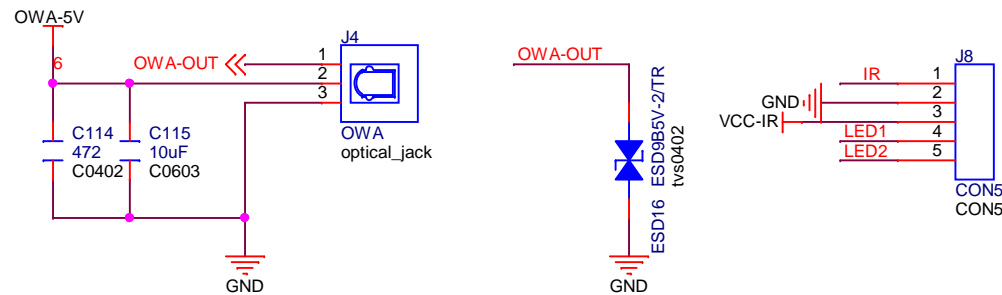
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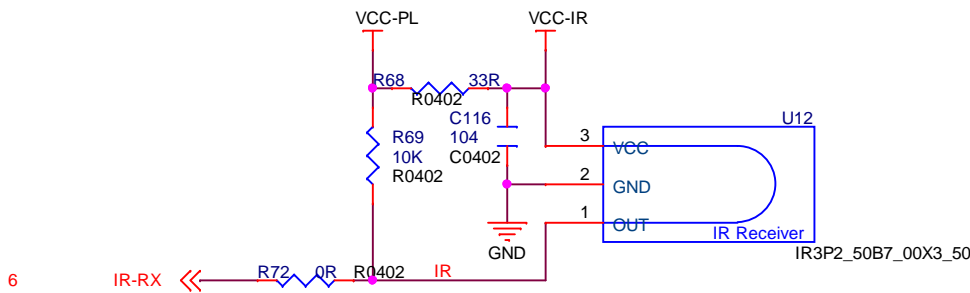
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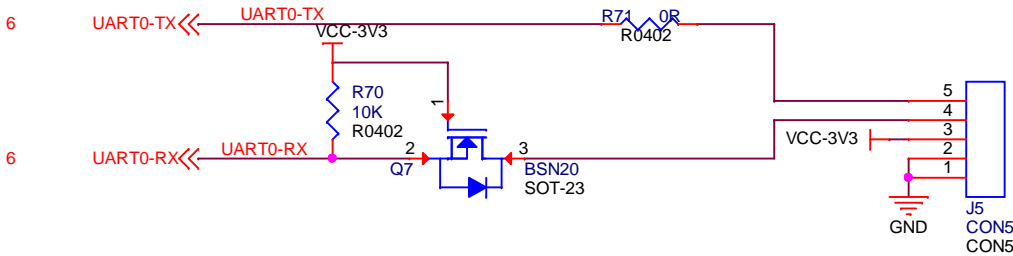
OWA



IR

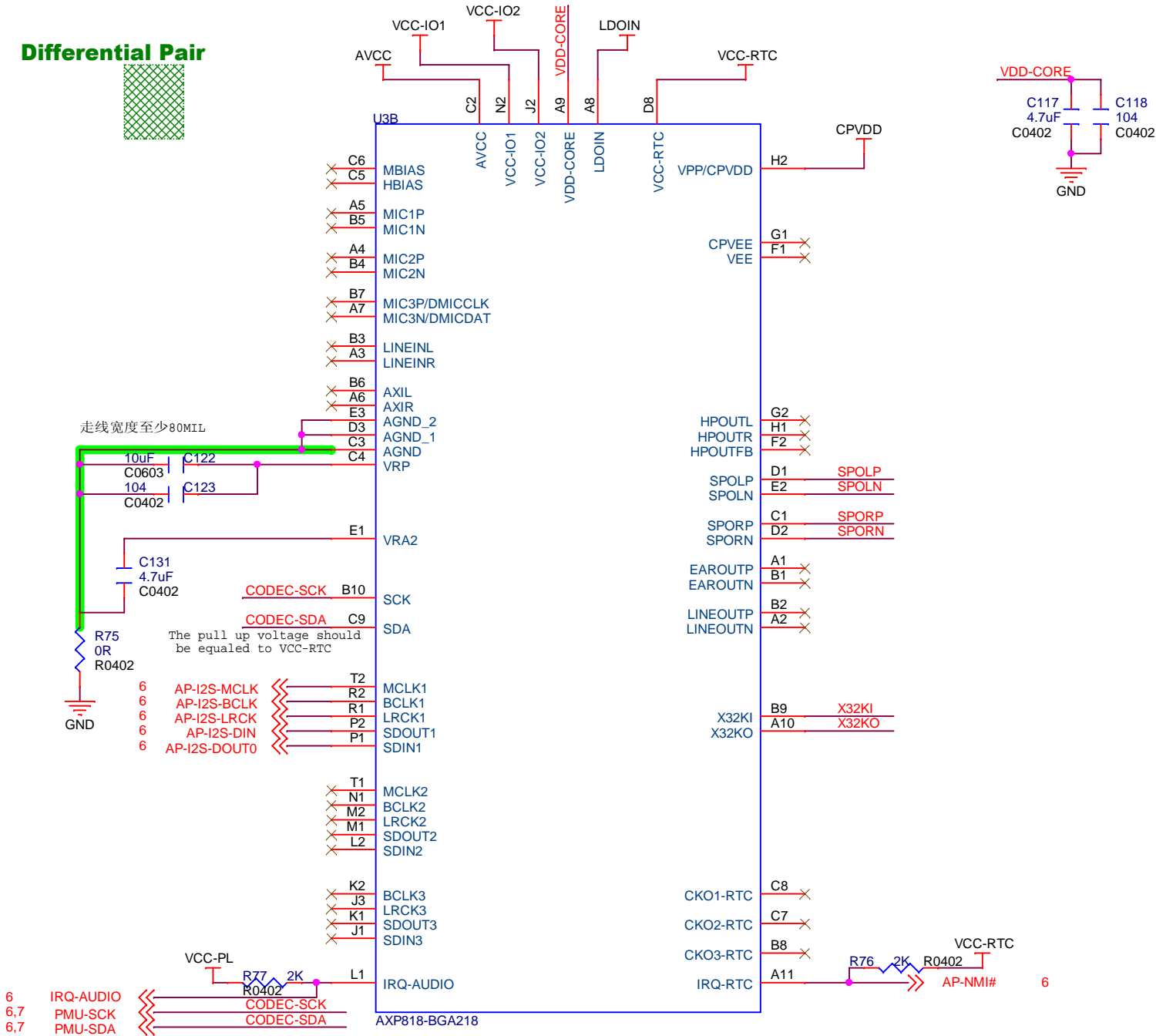
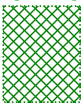


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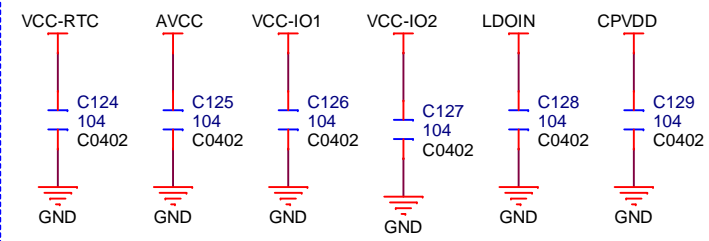


CODEC

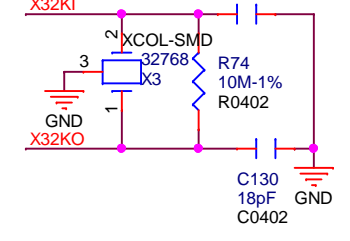
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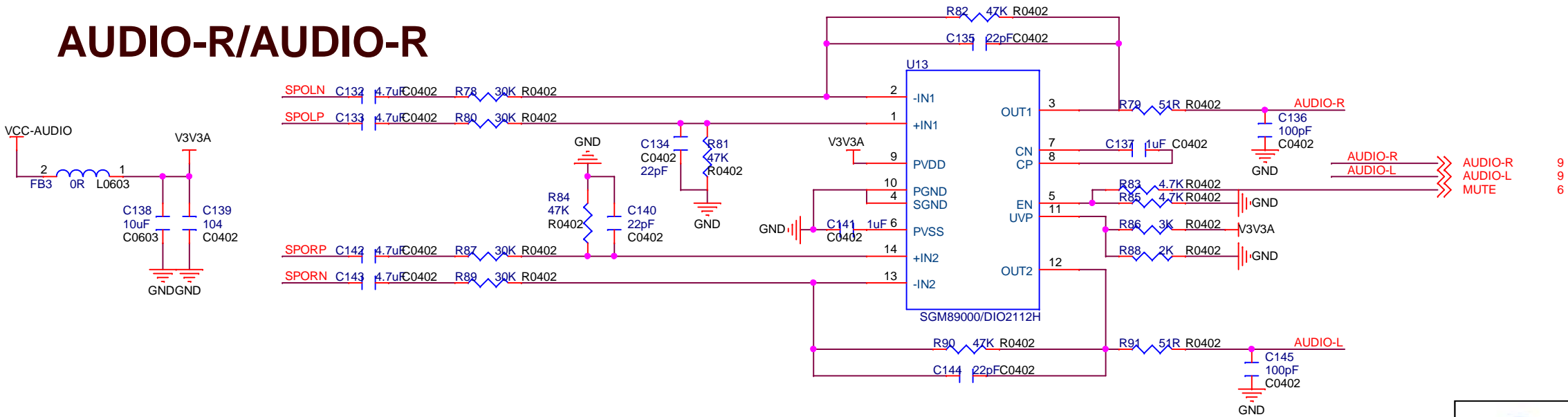
POWER



OSC-RTC



AUDIO-R/AUDIO-R



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Design Name

H8_HOMLET_STD

Size

A3

Page Name

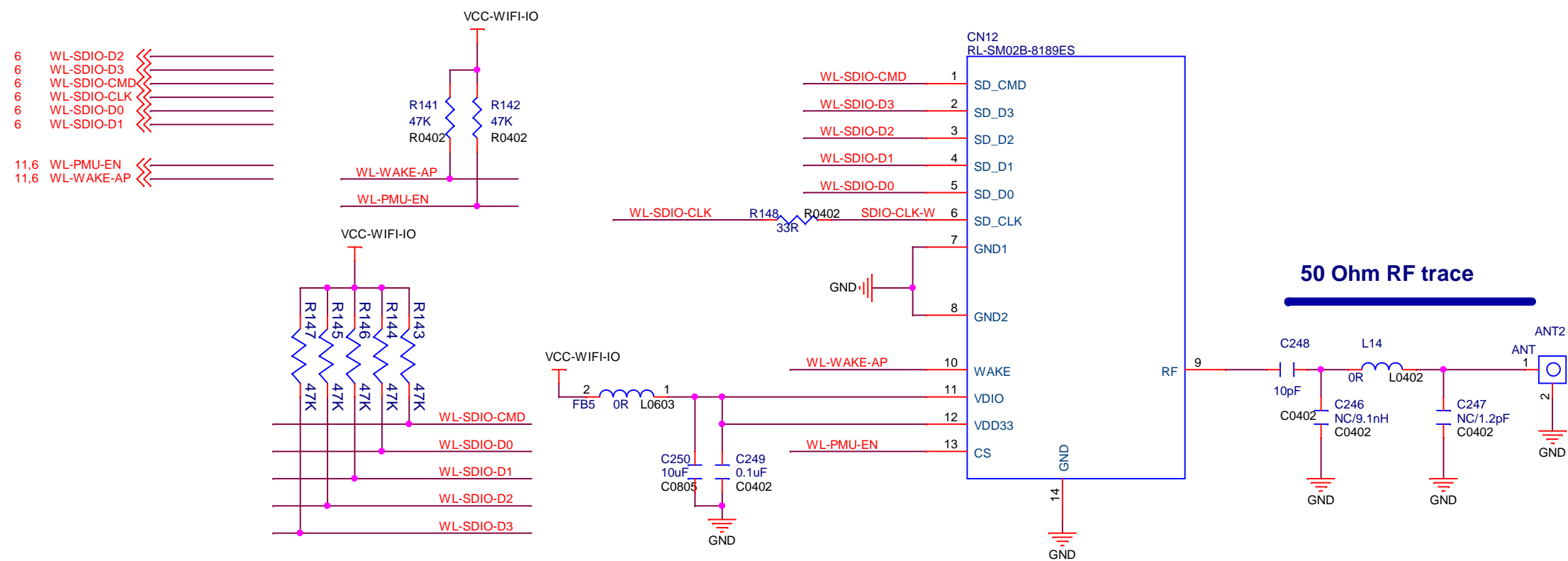
CODEC

Rev

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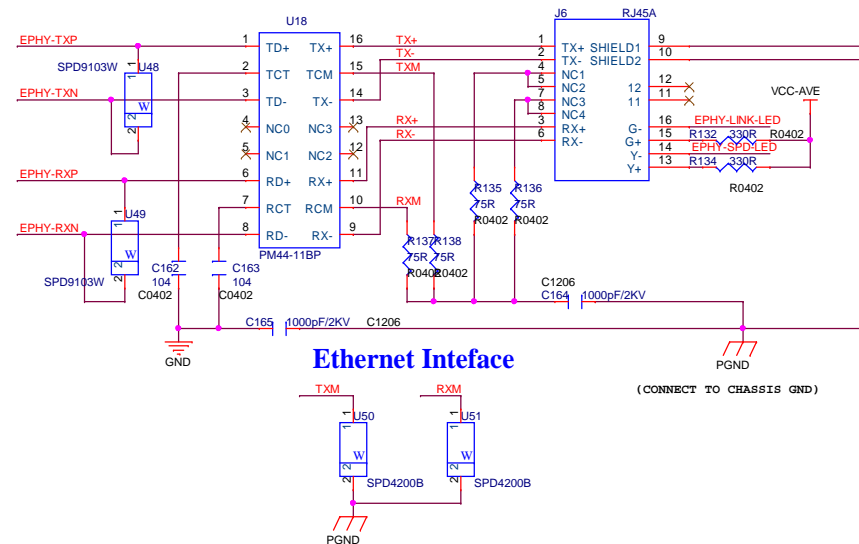
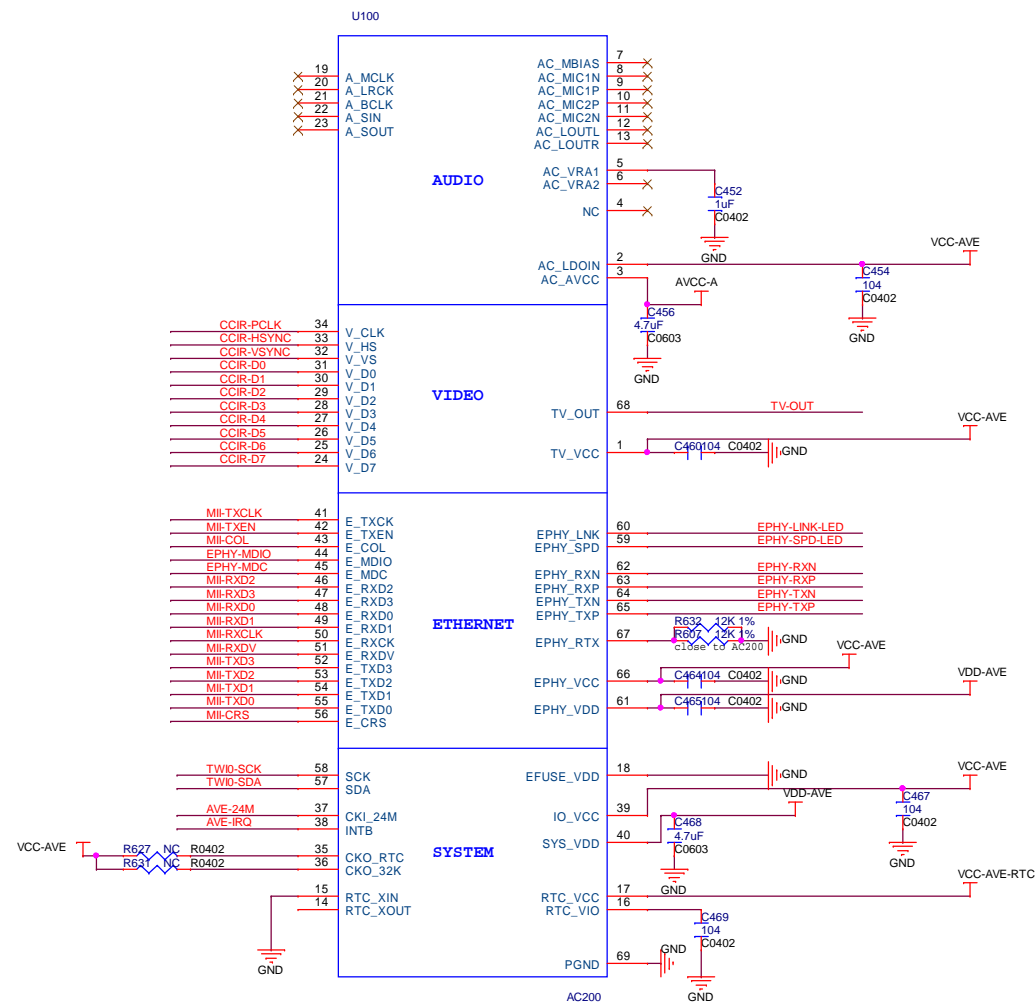
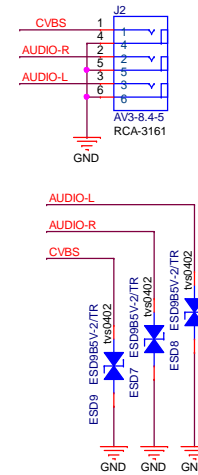
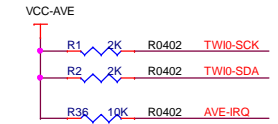
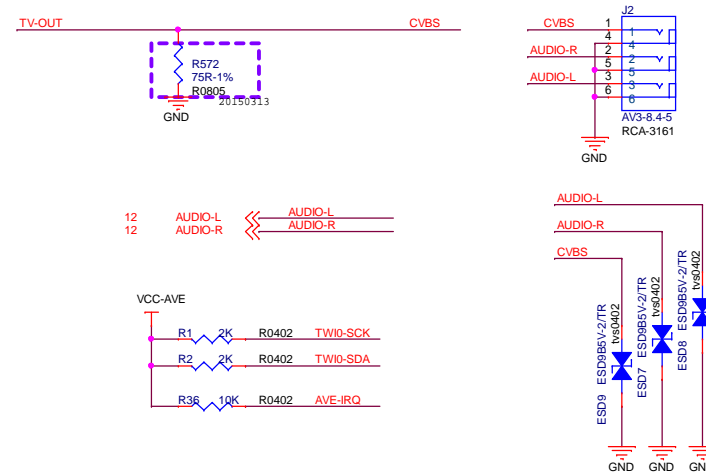
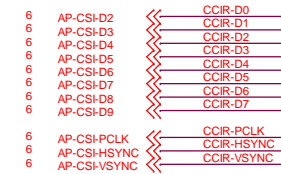
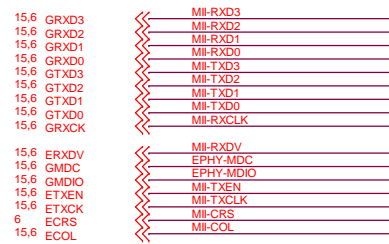
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SDIO WIFI



AC200

EMAC-MII

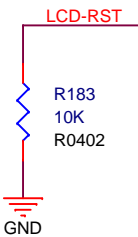
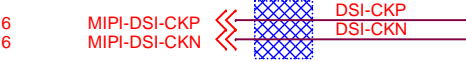
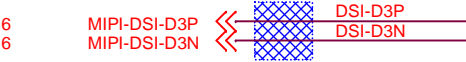
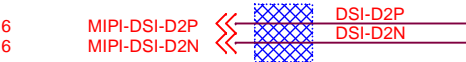
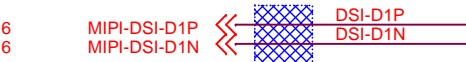
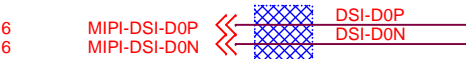


Ethernet Interface

MIPI DSI



Differential pairs
Z0= 100 ohm



MIPI

