Project Multi-Functional Clock

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I. Abstract

• This design uses STC89C52 microcontroller as the core and is composed of DS1302 chip clock control module, 7-segment nixie tube display module, on time alarm module, infrared remote control module, DS18B20 temperature sensor module, AT24C02 chip(EEPROM) stored time and temperature module, UART sent EEPROM data module.

II. Task

1. Basic requirement:

• a) Realizing a digital clock, which can count hour, minute, second, and include the second dots.

2. Required functions:

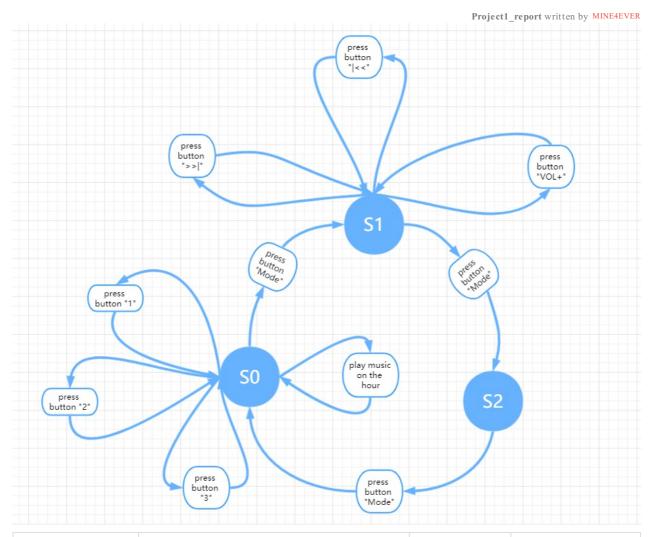
- a) Display can choose LCD or LED 7-segment. Indicate the current state (timer, setting, or temperate) in the display as well as the values. (15')
- b) Realizing the time setting by using the keyboard. The timing and display should not be stop during setting (15')
- c) Play some music on every hour (整点唱歌报时). The timing and display should not be stop during music (30')
- d) Use the ADC & thermistor to measure the temperature every second, display it and set some alarm above a specific level (10')
- e) Send the instant time and temperature data to the PC through the UART (10')

3. Suggested functions for extra scores.

- a) Replace the keyboard in (2.b) with infrared remote control for realizing the time setting (+10')
- b) Replace the ADC + thermistor with the DS18B20 1-wire temperature sensor for the measurement in (2.d)(+10')
- c) Use the EEPROM to store the offline time and temperature history and later send through UART in (2.e) (+10')
- d) Accelerating digit setting at key holding (键按下不放加速数字跳动) (+10)

III. Total design scheme

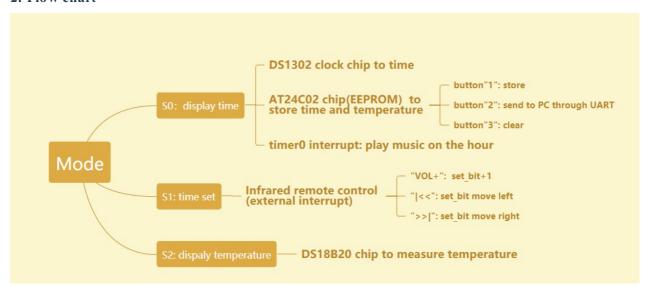
1. Finite State Machine



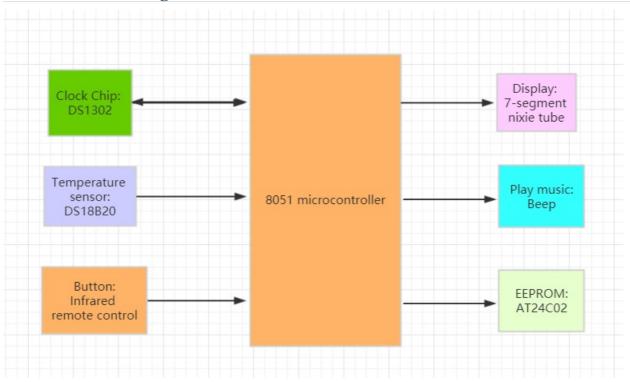
Current state	S0: display time	S1: set time	S2: display temperature
press button "Mode"	S1	S2	S0
press button " <<"	null	Set_bit move left	null
press button ">> "	null	Set_bit move right	null
press button "VOL+"	null	Set_bit+1	null
press button "1"	store current time and temperature	null	null
press button "2"	send current time and temperature to PC through UART	null	null
press button "3"	clear the data stored in EEPROM	null	null

On the hour	play music on the hour	null	null
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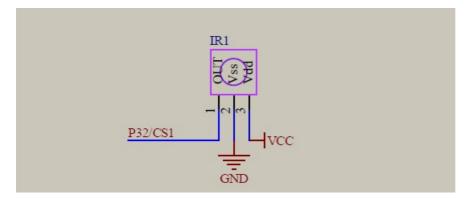
2. Flow chart



IV. Hardware design



1. The principle of infrared communication



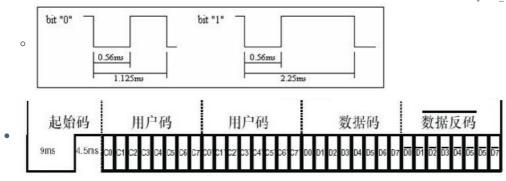
- The infrared remote control system generally consists of two parts: an infrared emitting device and an infrared receiving device.
 - The infrared emitting device can be composed of a keyboard circuit, an infrared encoding chip, a power source and an infrared transmitting circuit.
 - The infrared receiving device can be composed of an infrared receiving circuit, an infrared decoding chip, a power source and an application circuit.
- Usually, in order to make the signal better transmitted to the transmitting end, the baseband binary signal is modulated into a burst signal, which is transmitted through the infrared transmitting tube.
- There are two ways to realize:
 - Pulse width modulation (PWM) for signal modulation by pulse width
 - Pulse time modulation (PPM) for signal modulation by time interval between two pulse
- The carrier wave:
 - Using 455KHz crystal, the signal is modulated at 37.91KHz and the duty ratio is 1/3 through the internal frequency circuit
 - Modulation frequency (when the crystal oscillator is used at 455KHz)
 - $(f_{CAR}) = \frac{1}{T_C} = \frac{f_{OSC}}{12} \approx 38KHz$
 - (f {OSC}) is crystal frequency
 - $duty = (\frac{T_1}{T_c} = \frac{1}{3})$

• Data Format:

- The data format includes the start code, user code, data code and data inverse code, and the total code has 32 bits
- The data inverse code is the complement code of data code and can be used for error correction of the data when encoding
- o Start code: 9ms low level, 4.5ms high level

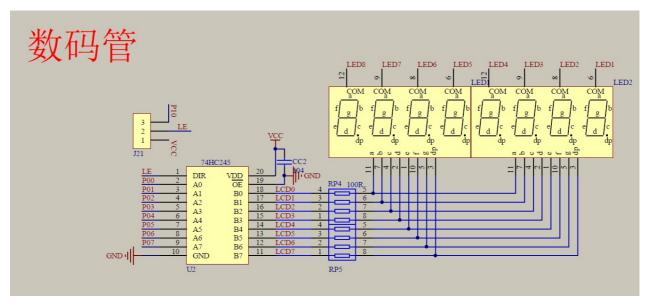
• bit difinition:

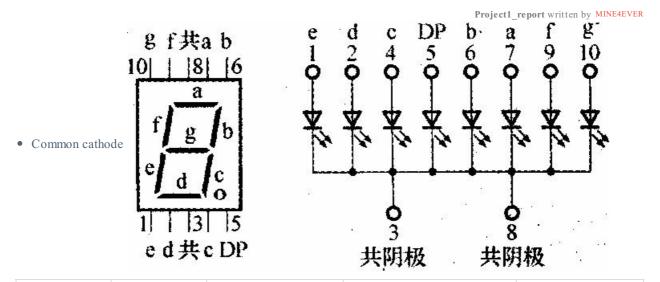
- Each bit in the user code or data code can be either bit '1' or bit '0'. The distinction between '0' and '1' is distinguished by the time interval of the pulse. This encoding method is called the pulse position modulation method, and the English shorthand PPM.
 - Low level time: 0.56ms
 - Hige level time: 0:0.56ms<1ms; 1:1.69ms>1ms



Button	Data code	button	Data code	Buttoon	Data code
OFF	0x45	Mode	0x46	Mute	0x47
Pause	0x44	<<	0x40	>>	0x43
EQ	0x07	VOL-	0x15	VOL+	0x09
0	0x16	RPT	0x19	U/SD	0x0D
1	0x0C	2	0x18	3	0x5E
4	0x08	5	0x1C	6	0x5A
7	0x42	8	0x52	9	0x4A

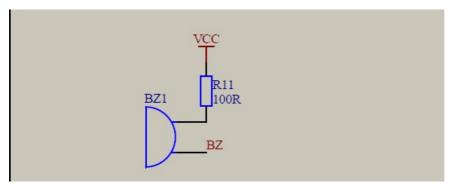
2. Nixie Tube





Number	Pin	Decimal code	Hexadecimal code	Binary code
0	abcdef	63	3F	0011 1111
1	bc	6	6	0000 0110
2	abdeg	91	5B	0101 1011
3	abcdg	79	4F	0100 1111
4	bcfg	102	66	0110 0110
5	acdfg	109	6D	0110 1101
6	acdefg	125	7D	0111 1101
7	abc	7	7	0000 0111
8	abcdefg	127	7F	0111 1111
9	abcdfg	111	6F	0110 1111

3. Buzzer



- The function of the buzzer alarm module:
 - When the timer on the hour, the buzzer makes a preset sound.

- The BZ is connected to the pin P1.5 of the single-chip STC89C52.
- 定时器每10ms重置一次,整点调用
- Fn[0][1]函数每次播放一个note

4. Temperature sensor DS18B20

A. Introduction of DS18B20

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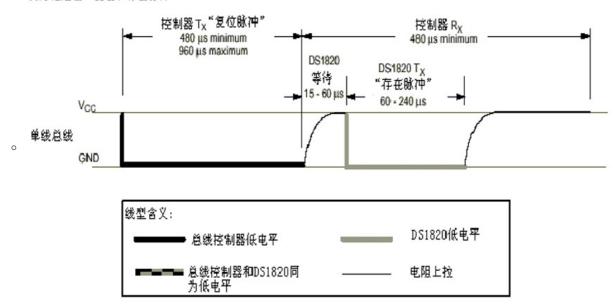
- DS18B20: single-line digital temperature sensors, or "one-line devices," has unique advantages:
 - The single-bus interface is used to connect the microprocessor with only one port line to achieve two-way communication between the microprocessor and the DS18B20.
 - $^{\circ}$ Wide measurement temperature range and high measurement accuracy. The DS18B20 has a measurement range of -55 $^{\circ}$ C to +125 $^{\circ}$ C and an accuracy of ± 0.5 $^{\circ}$ C in the range of -10 to +85 $^{\circ}$ C.

B. DS18B20 single bus working mode

Timing Sequence

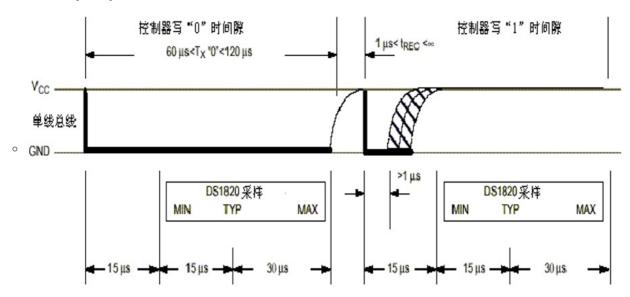
- Initializatoin
 - The host first sends a low-level pulse of 480-960 ms, then releases the bus to a high level, and detects the bus for the next 480 ms. If there is a low level, the device on the bus has been responded. If there is no low level, it is always high to indicate no device response on the bus.
 - As a slave device, the DS18B20 always detects if there is a low level of 480-960 us on the bus after power-on. If it is, wait for 15-60 us after the bus goes high. The level is pulled down 60-240 us to respond to the presence of a pulse, telling the host that the device is ready. If it is not detected, it is always waiting for detection.

初始化过程"复位和存在脉冲"



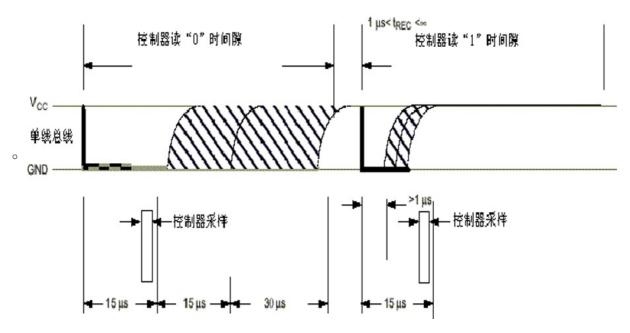
- Write sequence
 - o Data line set to 0 at first
 - o delay 15us

- send data from LSB to MSB(one by ont)
- o delay 60us
- o Data line set to 1
- Repeat 8 times to send the whole byte
- o Data line pulls up



• Read sequence

- o Data line set to 0 at first
- o delay 1 us
- $\circ~$ Data line pulls up to 1 and ready to read data
- o delay 10us
- o Data line set to 1
- o get one bit from data line and save it
- o delay 45us
- Repeat 8 times to save the whole byte
- o return the whole byte



Read temperature

- The simple steps to read the temperature value are as follows
 - Skip ROM operation
 - Send transform temperature command
 - Skip ROM operation
 - Send read temperature command
 - o Read temperature value
- The first 5 bits of temperature value are sign bits, 0 represents positive; 1 represents negative
 - o 0(positive): multiply by 0.0625 to get the actual temperature
 - o 1(negative): minus 1, take the complement and then multiply by 0.0625 to get the actual temperature

5. DS1302 Clock Chip

A. Introduction of DS1302

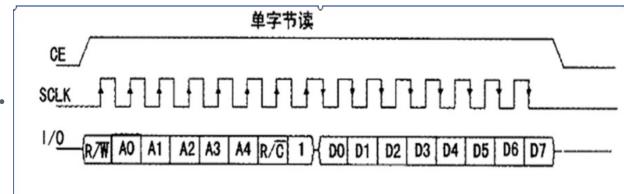
- The DS1302 is a high-performance, low-power real-time clock chip with 31 bytes of static RAM. It communicates with the CPU using the SPI three-wire interface.
- The real-time clock provides seconds, minutes, hours, days, weeks, months, and years, and can be automatically adjusted in one month and 31 days, with leap year compensation.
- The working voltage is as wide as 2.5 to 5.5V.

B. working mode

Single byte read

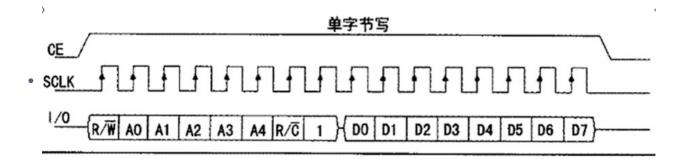
The DS1302 communicates with the microcontroller through the SPI serial bus. When performing a read and write
operation, it must read and write two bytes at a minimum. The first byte is the control byte, which is a command that
tells the DS1302 whether to read or write. Is the RAM or the CLOK register operation. The second byte is the data
to be read or written.

Single Byte Read: CE can be asserted high only when SCLK is low. So set SCLK low before performing the
operation, then set CE high, then start to put the level signal to be transmitted on IO, then jump SCLK. When the
data is on the rising edge of SCLK, the DS1302 reads the data. On the falling edge of SCLK, the DS1302 places the
data on the IO.



Single byte wirte

• RST can be asserted high only when SCLK is low. Say to set SCLK low before performing the operation, then set RST high, start to put the level signal to be transmitted on the IO, and then jump SCLK. When the data is on the rising edge of SCLK, the DS1302 reads the data. On the falling edge of SCLK, the DS1302 places the data on the IO.



6. EEPROM((I^2C)Bus) and UART

A. Introduction of AT24C02

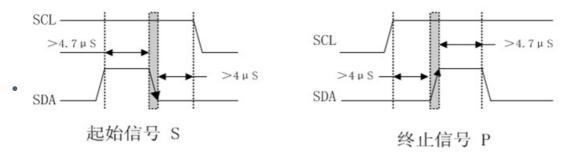
- The 24C02 is a 2K-bits serial CMOS EEPROM with 256 8-bit bytes internally.
- \bullet Compatible with 400KHz (I^2C) bus
- 1.8 to 6.0 V operating voltage range
- Data can be saved for 100 years

B. AT24C02 Working mode

Start and stop signal

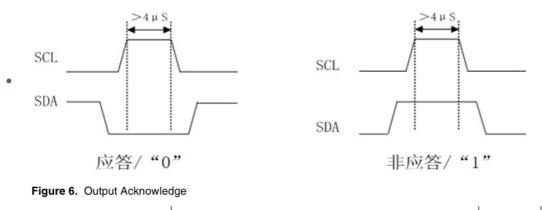
- Start signal: SCL=1, SDA failling edge
 - SDA=1 keep more than 4.7us, then SDA=0 keep more than 4us
- Stop signal: SCL=1, SDA rising edge

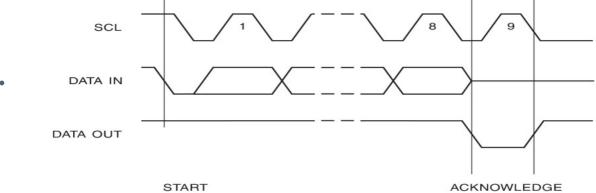
• SDA=0 keep more than 4us, then SDA=1 keep more than 4.7us



Answer signal

- Each byte transmitted must be guaranteed to be 8 bits long. When data is transmitted, the most significant bit (MSB) is transmitted first, and each transmitted byte must be followed by a response signal (that is, one frame has 9 bits in total).
- For example, after the host sends one byte of data to the bus, the bus is released, and the bus is pulled low (ie, the answer signal) to indicate that the byte is successfully transmitted.
- Similarly, after the host reads a byte of data from the bus, the host will pull the bus low, "telling" the slave to receive the data in this byte.





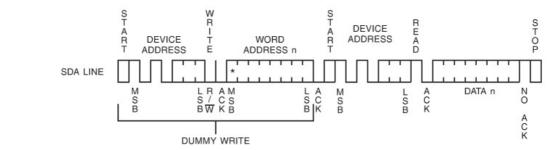
Read-Write Operation

- Write one byte of data to the specified address of the AT24C02 in the following order:
 - o Start signal→write device address(0xA0, write)→answer signal→write memory address→answer signal→write

data→answer signal→stop signal

- Read one byte of data from the specified address of the AT24C02 in the following order:
 - o Start signal→write device address(0xA0, write)→answer signal→write memory address→answer signal→start signal→write device address(0xA1, read)→answer signal→read data→stop signal→return the data

Figure 11. Random Read



C. UART

С

- The UART is a kind of asynchronous serial communication protocol. The working principle is to transmit each character of the transmitted data one by one. When two devices use serial communication, they must first agree on a data transmission rate, and the two devices The respective clock frequencies must be kept close to this rate, and a large difference in the clock frequency of one side will cause data transmission confusion.
- UART Pin:
 - o TX: CPU sends data to the device, corresponding to the RX of the device.
 - RX: CPU receives the data from the device, and corresponds to the TX of the device.
 - o GND: ground line
- UART definition:
 - Start bit a logic "0" signal indicating the beginning of the transmitted character
 - Data bit: The number can be 5-8, forming a character, starting from the lowest bit
 - Parity: The data bit plus this bit, so that the number of bits in "1" should be even (even parity) or odd (odd parity) to verify the correctness of data transmission.
 - Stop bit: the end of the character data
 - o Baud Rate: A measure of the data transfer rate. Indicates the number of bits of binary code transmitted per second



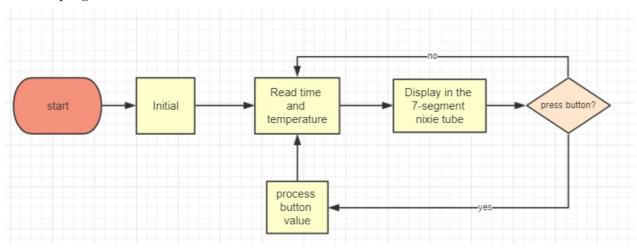
Notes:

- (I^2C) bus data validity provisions:
 - When SCL=1, SDA must remain stable. When SCL=0, SDA is allowed to change.

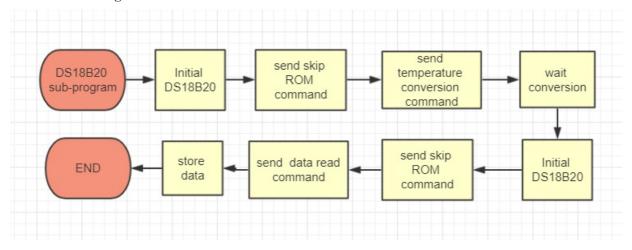
- o Reference:
 - 基于51单片机IIC通信的AT24C02学习笔记: https://www.cnblogs.com/whik/p/6650092.html

V. Software Design

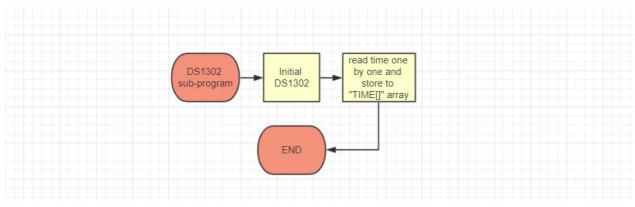
1. main program



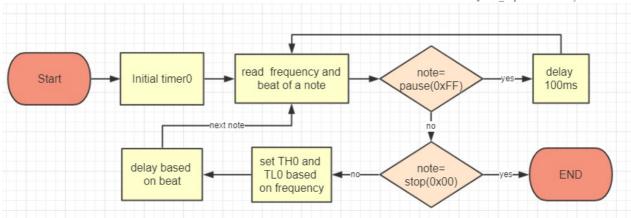
2. DS18B20 Program



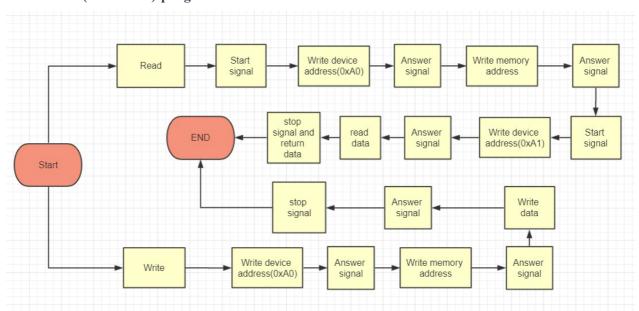
3. DS1302 clock program



4. music program(play on the hour)

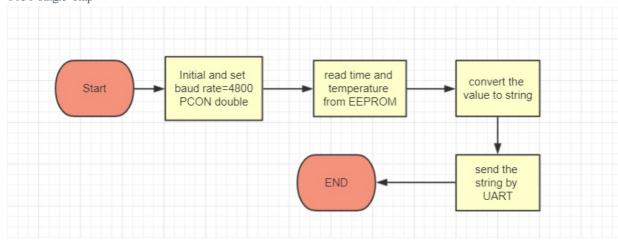


5. AT24C02(EEPROM) program



6. UART program

• baud rate should be set to 4800 and use PCON register to double since the error of the way is minimum using the PZ 8051 single-chip



1. Multifunction_clock.c(Main program)

```
1. #include <Multifunction clock.h>
3. #define BCD(n) n/10*16+n%10
5. code unsigned char SegmentData[16]={6. 0x3F,0x06,0x5B,0x4F,0x66,0x6D,0x7D,0x07,
7. 0x7F,0x6F,0x77,0x7C,0x39,0x5E,0x79,0x71
8. }; //common cathode
10. code void(code *Fn[3][2])()={{SO_timer,SO_music},{S1_set,S1_add},
11. {S2_tem,null}};// the state-input transition table using the function pointer
13. uchar State=0; // state variable
14. uchar Key=0; // input variable
16. uchar FlashLED=0; // use variables to communicate among modularized functions
17. uchar set bit=0;
18. uchar debouncing=0;
19. uint time count=0:
20. int temp=0;
21. float tp0;
22. int tp1=0;
23. uchar s=0;
24. uchar DispSegment[8];
25. uchar Infrared_value[4];
27. uchar str0[]=":";
28. uchar str1[]="-";
29. uchar str2[]=".";
30. uchar str3[]="*C
31. ";
33. void main(void)
34. {
35. Init();
36. while(1){
37. temp = DS18B20ReadTemp();

38. tp0 = temp;

39. tp1 = tp0*0.0625*100+0.5;

40. DS1302ReadTime();
         Fn[State][Key]();
          Fn_disp_buffer();
Fn_refresh_disp();
42.
44. }
45. }
47. void Init()
47. Vold Init()

48. {
49. // only do initializatoin in main function

50. TMOD=0x21; // timer0 mode1

51. TH0=0xD08; // T0 with highest priority for tasks which needs rigid timing

52. TL0=0xF0; //10ms
53. TH1=0xF3;
54. TL1=0xF3; //bund rate=4800
54. TL1=0xF3; //bur
55. //PCON register
56. PCON=0x80;//bund rate double
 57. //SCON register
58. SM0=0;
59. SM1=1; //serial
60. REN=1; //enable
61. //TCON register
                         //serial mode1
62. TR1=1; //open timer1
63. IT0=1; //open external interrupt0, drop trigger
64. //IE register
65. ET0=1; //enable timer0 interrupt 1
66. EX0=1; //enable external interrupt 0
67. EA=1; //enable all interrupt
67. EA=1;
68. Key=0;
69.
       Beep=0;
70. Infrared=1;
71. if(TIME[0]==0x00){
72. TIME[0]=AT24C02Read(3);
73. TIME[1]=AT24C02Read(2);
74. TIME[2]=AT24C02Read(1);
76. [
77. }
      Ds1302Init();
79. void delay (uint m) //控制频率延时
81.
        while(m--);
84. void Fn_refresh_disp(void)
```

```
uchar i;
for(i=0;i<8;i++){
 87.
 88.
        switch(i){
 90.
          case(0):
            LSA=0;LSB=0;LSC=0;break;
 92.
          case(1):
  LSA=1;LSB=0;LSC=0;break;
 93.
          case(2):
  LSA=0;LSB=1;LSC=0;break;
 94.
 95.
 96.
           case(3):
           LSA=1;LSB=1;LSC=0;break;
 97.
           case(4):
            LSA=0:LSB=0:LSC=1:break;
 99.
            case(5):
             LSA=1;LSB=0;LSC=1;break;
 101.
 102.
            case(6):
 103.
             LSA=0;LSB=1;LSC=1;break;
 104.
            case(7):
 105.
            LSA=1;LSB=1;LSC=1;break;
106. }
             if(i==set_bit&FlashLED>=15&State==1){
 107.
             P0 = 0x00;
}else{
 108.
 109.
               P0 = DispSegment[7-i];
 110.
 111.
 112.
           delay(80); //0.8ms
 113.
          P0 = 0x00; //blanking
115. }
 116.
 117. void Fn_disp_buffer(void)
118. {
119. // function for buffering the display variables
 121.
             f(State!=2){
    DispSegment[0] = SegmentData[TIME[2]/16];
    DispSegment[1] = SegmentData[TIME[2]%16];
    DispSegment[2] = 0x40;
    DispSegment[3] = SegmentData[TIME[1]/16];
    DispSegment[4] = SegmentData[TIME[1]%16];
    DispSegment[5] = 0x40;
    DispSegment[6] = SegmentData[TIME[0]/16];
    DispSegment[7] = SegmentData[TIME[0]%16];
    else{
122.
 123.
 124.
 125.
 126.
 127.
 128.
129.
 130.
           }else{
131.
132.
          //Temperature (Infrared_value)
if(temp==0){}
 133.
           else{
           DispSegment[0] = 0x00:
 134.
 135.
            tp = temp;
temp = tp*0.0625*100+0.5;
 136.
 137. }
             DispSegment[1] = SegmentData[temp/10000];
DispSegment[2] = SegmentData[temp%10000/1000];
DispSegment[3] = SegmentData[temp%10000%1000/100]|0x80;
DispSegment[4] = SegmentData[temp%100/10];
DispSegment[5] = SegmentData[temp%100%10];
 138.
 139.
140.
 141.
 142.
143.
 144. DispSegment[6] = 0x63;
145. DispSegment[7] = 0x39;
145. [
146. }
147. }
 148.
 149. void null(void){}
 150. void S0_timer(void)
151. {
152. if(TIME[0]==0&TIME[1]==0&TIME[2]!=0&State==0) Key=1;
154. void S0_music(void)
155. {
156. // input your own code
157. TR0 = 1;
 158. music();
159. }
 160. void S1_set(void)
160. Volu 32_--
161. {
162. // input your own code
163. Ds1302ReadTime();
164. if(++FlashLED==30) FlashLED=0; //500ms
 166. void S1_add(void)
case(0):
  if(TIME[0]*16<9){TIME[0]++;}else{TIME[0]-=9;}break;</pre>
 171.
             case(1):
   TIME[0]+=0x10;if(TIME[0]>=0x60){TIME[0]-=0x60;}break;
 172.
 173.
 174.
175.
               hreak:
176.
             case(3):
```

```
if(TIME[1]%16<9){TIME[1]++;}else{TIME[1]-=9;}break;
177.
178.
179.
           TIME[1]+=0x10;if(TIME[1]>=0x60){TIME[1]-=0x60;}break;
180.
          case(5):
181.
182.
          case(6):
183.
            if(TIME[2]<0x20){if(TIME[2]%16<9){TIME[2]++;}else{TIME[2]-=9;}}
184.
            \texttt{else}\{\texttt{if}(\texttt{TIME}[2]\%16<3)\{\texttt{TIME}[2]++;\}\texttt{else}\{\texttt{TIME}[2]-=3;\}\}
185.
186.
          case(7):
            TIME[2]+=0x10;if(TIME[2]>=0x24){TIME[2]&=0x0F;}break;
187.
188.
189. Ds1302Init():
       Key = 0;
191. }
192. void S2_tem(void)
193. {
194. if(tp1%1000%1000/100>=8){TR0=1;T0RH=0xFE;T0RL=0x05;}
195.
      else {TR0=0;Beep=0;}
196. }
197.
198. //infrared interrupt
199. void IR_ISR(void) interrupt 0
200. {
201.
       uchar Htime=0; //high_level time, decide bit whether 0 or 1 uint timeout; //prevent a timeout
202.
203.
204.
        Fn_refresh_disp();
       205.
207.
208.
          timeout = 500;
while(Infrared==1&timeout>0){delay(1);timeout--;}
209.
210.
          211.
212.
213.
214.
215.
216.
             delay(10);
217.
             Htime++;
218.
             if(Htime>30) return; //beyond 3ms, wrong
219.
220.
             Infrared_value[i]>>=1;
            if(Htime>=10) Infrared_value[i]|=0x80;
Htime = 0;
221.
222.
223
224.
225.
226.
227.
        Fn_refresh_disp();
        //data code != ~data complement code, wrong
if(Infrared_value[0]!=0x00|Infrared_value[1]!=0xFF) return;
228.
229.
        if(Infrared_value[2]!=~Infrared_value[3]) return;
if(++debouncing==3){
230.
231.
           if(Infrared_value[2]==0x46) {
   if(State++>=2){State=0;set_bit=0;}
232.
233.
                                                        //set mode
           234.
235.
236.
237.
                                                       //set_bit move right
238.
           if(set_bit--==0) set_bit=7;
}else if(Infrared_value[2]==0x09&State==1) {
239.
240.
241.
          Key=1;
242.
243.
         else if(Infrared_value[2]==0x0C){
244.
          AT24C02Write(1,TIME[2]);
245.
246.
          AT24C02Write(2,0);
AT24C02Write(2,TIME[1]);
247.
248.
          AT24C02Write(3,0);
249.
250.
          AT24C02Write(3,TIME[0]);
251.
252.
          AT24C02Write(4,0);
253.
          AT24C02Write(4,BCD(tp1/100));
254.
          AT24C02Write(5,0);
AT24C02Write(5,BCD(tp1%100));
255
256.
257.
         }else if(Infrared_value[2]==0x5E){
for(i=1;i<6;i++) AT24C02Write(i,0);</pre>
258.
          AT24C02Write(2,0);
260.
          AT24C02Write(2,0);
AT24C02Write(4,0);
261.
         AT24C02Write(4,0);
}else if(Infrared_value[2]==0x18){
Uchar2String(AT24C02Read(1));
262.
263.
264.
265.
          SendString(str0);
266.
          Uchar2String(AT24C02Read(2));
          SendString(str0);
Uchar2String(AT24C02Read(3));
267.
268.
```

```
269. SendString(str1);
270. Uchar2String(AT24C02Read(4));
271. SendString(str2);
272. Uchar2String(AT24C02Read(5));
273. SendString(str3);
274. }
275. debouncing=0;
276. }
277. }
```

2. Multifunction_clock.h

```
#ifndef _MULTIFUNCTION_CLOCK_H_
#define _MULTIFUNCTION_CLOCK_H_
#include cobslage.hb
#include cobslage.hb
#include cobslage.hb
#include cobslage.hb
#include cobslage.hb
#include cols.hb
```

3. AT24C02.c

```
2. [功能] EEPROM储存时间和温度
5. #include <AT24C02.h>
7. void Delay5us()
8. {
9. uchar a;
10. for(a=1;a>0;a--);
11. }
13. void Start()
14. {
15. SDA = 1;
16. Delay5us();
17. SCL = 1;
18. Delay5us();
19. SDA = 0;
20. Delay5us();
                      //failing
 21. SCL=0;
22. Delay5us();
24.
 25. void Stop()
26. {
27. SDA = 0;
28. Delay5us();
29. SCL = 1;
30. Delay5us();
31. SDA = 1;
32. Delay5us();
                     //rising
33. }
35. void Answer()
36. {
37. uchar i=0;
38. SCL = 1;
```

```
39. while(SDA&i<250) i++;
40. SDA = 0;
41. Delay5us();
 42. SCL=0;
 43. Delay5us();
44. }
46. void AT24C02WriteByte(uchar dat) 47. {
56. Delaysus();

57. }

58. SCL = 0;

59. Delaysus();

60. SDA = 1;

61. Delaysus();
                                         //release SDA
62. }
63.
63.
64. uchar AT24C02ReadByte()
65. {
66. uchar i;
67. uchar dat=0;
68. SDA=1;  //起始和发送一个字节之后SCL都是0
69. Delay5us();
70. for(i=0;i<8;i++){
71. SCL = 1;  //send dat to SDA when SC
72. Delay5us();
73. dat = (dat<<1)|SDA;
74. Delay5us();
75. SCL = 0;
76. Delay5us();
77. }
78. return dat;
                                         //send dat to SDA when SCL=0
 78. return dat;
79. }
 81. void AT24C02Write(uchar addr,uchar dat)
82. {
83. Start();
84. AT24C02WriteByte(0xA0);
 85. Answer();
86. AT24C02WriteByte(addr);
 87. Answer();
88. AT24C02WriteByte(dat);
 89. Answer();
 90. Stop();
 92.
 93. uchar AT24C02Read(uchar addr)
94. {
95. uchar dat;
96. Start();
97. AT24C02WriteByte(0xA0);
97. AT24C02WriteByte(0xA0);
98. Answer();
99. AT24C02WriteByte(addr);
100. Answer();
101. Start();
102. AT24C02WriteByte(0xA1);
103. Answer();
104. dat = AT24C02ReadByte();
105. Stop();
106. return dat;
107. }
```

4. AT24C02.h

```
#ifndef __AT24C02_H__
#define __AT24C02_H__
#include <reg52.h>
#define uchar unsigned char
#define uint unsigned int

sbit SCL=P2^1;
sbit SDA=P2^0;

void AT24C02Write(uchar addr,uchar dat);
uchar AT24C02Read(uchar addr);
#endif
```

5. DS18B20.c

```
2. [功能] 温度读取
 5. #include <DS18B20.h>
  void delay1ms(uchar i)
 8. {
 11. for(j=110;j>0;j--);
12. }
  13. }
14. uchar --
15. {
16. uint i;
17. TemPort = 0;
18. for(i=0;i<80;i++); //8*80+3=643us
19. TemPort = 1; //high
20. while(TemPort){ //wait for low in 480us
21. delayIms(1); //wait 1ms
22. i++; //Initialization
23. i++; //Initialization
24. uchar --
25. ceturn 0; //Initialization
26. ceturn 0; //Initialization
27. ceturn 0; //Initialization
  14. uchar DS18B20Init() //success=1,fail=0
 22.
23.
24. }
           if(i>5) return 0; //Initialization failed
        return 1; //Initialization successful
  26. }
  27.
  28. void DS18B20Write(uchar dat)
  29. {
         for(i=0;i<8;i++){
  31.
           TemPort = 0;
  33.
            j++;
            TemPort = dat&0x01;
  34.
  35.
            for(j=0;j<8;j++); //delay≈64us+3us>60us
           TemPort = 1;
dat>>=1; //next bit
  36.
  37.
  38. }
39. }
  40.
  41. uchar DS18B20Read()
 42. {
43. uchar i,j,k;
 43.
         uchar b,byte;
for(i=0;i<8;i++){
  45.
            TemPort = 0;
          k++;
TemPort = 1;
  47.
                                   //delay >1us
  49.
           for(j=0;j<1;j++); //delay >10us
          b = TemPort;
byte = (byte>>1)|(b<<7);
  50.
  51.
           for(j=0;j<6;j++); //delay>45us
  52.
  53. }
54. return byte;
  55. }
 56.
57. //transform temperature command
  58. void DS18B20TransTempCom()
  59. {
 67. void DS18B20ReadTempCom()
  68. {
         DS18B20Init();
       delayIms(1);
D518B20Write(0xCC); //jump ROM
D518B20Write(0xBE); //start to read temperature
  72.
  73. }
  74.
  75. //read temperature value
  76. int DS18B20ReadTemp()
 77. {
78. int temp0 = 0;
79. uchar TemH,Temi
80. DS18B20TransTer
         uchar TemH,TemL;
DS18B20TransTempCom();
 80. DS188201rans1empc.om();
81. DS18820ReadTempCom();
82. TemL = DS18820Read();
83. TemH = DS18820Read();
84. temp0 = TemH;
85. temp0 | TemH;
86. temp0 | TemL;
                                        //read low 8 bits
                                        //read high 8 bits
                                          //joint 16bits high-low temperature
         return temp0;
 88. }
```

6. DS18B20.h

```
#ifndef _D518820_H
#define _D518820_H
#include <reg52.h>
#ifndef uchar
#define uchar unsigned char
#endif

#ifndef uint
#define uint unsigned int
#endif

int D518820ReadTemp();

sbit TemPort = P3^7;
#endif
```

7. DS1302.c

```
2. [功能] 时钟计时
5. #include <DS1302.h>
7. uchar code READ_RTC_ADDR[7] = {0x81, 0x83, 0x85, 0x87, 0x89, 0x8b, 0x8d};
8. uchar code WRITE_RTC_ADDR[7] = {0x80, 0x82, 0x84, 0x86, 0x88, 0x8a, 0x8c};
9.
10. //second,minute,hour,day,month,week,year,BCD code---//
11. uchar TIME[7] = {0, 0, 0x00, 0x07, 0x05, 0x06, 0x16};
12.
13. void Ds1302Write(uchar addr, uchar dat)
14. {
15. uchar n;
16. RST = 0;
17. _nop_();
18. SCLK = 0;
18. SCLK = 0;

19. _nop_();

20. RST = 1;

21. _nop_();

22. for (n=0; n<8; n++)//send 8bits address
24. DSIO = addr & 0x01;//from LSB to MSB
24. DSLO = addr & 0X01;//from LSB to MSB
25. addr >>= 1;
26. SCLK = 1;//DS1302 read when SCLK rising
27. _nop_();
28. SCLK = 0;
31. for (n=0; n<8; n++)//write 8bits data
//DS1302 read when SCLK rising
36. _nop_();
37. SCLK = 0;
38. _nop_();
39. }
40.
41. _nop_();
42. }
43.
44. uchar Ds1302Read(uchar addr)
45. {
46. uchar n,dat,dat1;
47. RST = 0;
47. RSI = 0;

48. _nop_();

49. SCLK = 0;

50. _nop_();

51. RST = 1;
52. _nop_();
53. for(n=0; n<8; n++)//send 8bits address
55. DSIO = addr & 0x01;

56. addr >>= 1;

57. SCLK = 1;
58. _nop_();
59. SCLK = 0;
60. _nop_();
```

```
61. }
62. _nop_();
63. for(n=0; n<8; n++)//read 8bits data</pre>
64. {
65. dat1 = DSIO;//LSB-MSB

66. dat = (dat>>1) | (dat1<<7);

67. SCLK = 1;
68. _nop_();
69. SCLK = 0;//get data when SCLK failling
70. _nop_();
71. }
72. //RST = 0;
73. _nop_(); //wait the DS1302 reset
74. SCLK = 1;
75. _nop_();
76. DSIO = 0;
77. _nop_();
78. DSIO = 1;
79. _nop_();
80. return dat;
82.
 83. void Ds1302Init()
84. {
85. uchar n;
86. Dsi302Write(0x8E,0X00); //close wirte protection
87. for (n=0; n<7; n++)//write initial time
88. {
89. Ds1302Write(WRITE_RTC_ADDR[n],TIME[n]);
B9. Ds1302Write(WRITE_RTC_ADDR[n],TIME[n]);
90. }
91. Ds1302Write(0x8E,0x80); //open wirte protection
 92. }
 93.
94. void Ds1302ReadTime()
 95. {
96. uchar n;
97. for (n=0; n<7; n++)//read current time
98. {
99. TIME[n] = Ds1302Read(READ_RTC_ADDR[n]);
100. }
101. }
```

8. DS1302.h

```
#include<reg52.h>
#include<intrins.h>
#ifndef uchar
#define uchar unsigned char
#endif

#ifndef uint
#define uint unsigned int
#endif

sbit DSIO=P3^4;
sbit RST=P3^5;
sbit SCLK=P3^6;

void Ds1302Write(uchar addr, uchar dat);
uchar Ds1302Read(uchar addr);
void Ds1302Init();
void Ds1302ReadTime();
extern uchar TIME[7]; //m入全局变量
#endif
```

9. music.c

```
12. 0xF88B,0xF95B,0xFA14,0xFA66,0xFB03,0xFB8F,0xFC0B,
13. 0xFC43,0xFCAB,0xFD08,0xFD33,0xFD81,0xFDC7,0xFE05,
   14. 0xFB21,0xFE55,0xFE84,0xFE99,0xFEC0,0xFEE3,0xFF02,0xFF,0x00
   15. };
  16.
17. uchar code music_tab[] = { //简谱+节拍
18. mid(1),40, mid(2),40, mid(3),40, mid(1),40,
19. mid(1),40, mid(2),40, mid(3),40, mid(1),40,
20. mid(3),40, mid(4),40, mid(5),40,mid(5),20,
21. mid(3),40, mid(4),40, mid(5),40,mid(5),20,
22. mid(5),20, mid(6),20, mid(5),20,mid(4),20, mid(3),40, mid(1),40,
23. mid(5),20, mid(6),20, mid(5),20,mid(4),20, mid(3),40, mid(1),40,
24. mid(1),40, 5,40, mid(1),40,mid(1),40,
25. mid(1),40, 5,40, mid(1),40,mid(1),40,
26. }:
   26. };
   28. void delayms(uint x)
   29. {
    30.
                     uint y;
                   for(;x>0;x--)
for(y=110;y>0;y--);
    31.
   33. }
   35. void music(void)
   38. if(n=e){
39. p=noteFreq[music_tab[i]];
40. if(p==0x00) { i=0;TRO=0;Key=0;Beep=0;return;} //如果碰到结束符,结束
41. else if(p==0xff) { i=i+1;delayms(100);return;} //若碰到休止符,延时100ms,继续取下一音符
42. else {m=noteFreq[music_tab[i++]], n=music_tab[i++]/2;} //取频率常数 和 节拍常数
43. TORH = m/256;
44. TORL = m%256;
45. }
   46.
              else{n--;}
   47. }
   49. void InterruptTmr0() interrupt 1 {
50.    TH0 = TORH;
51.    TL0 = TORL;
                    Beep = ~Beep;
    52.
```

10. MUSIC.h

```
#ifndef __MUSIC_H_
#define __MUSIC_H_
#include <reg52.H>
#include <Multifunction_clock.h>

#define uchar unsigned char
#define uint unsigned int
#define mid(n) (n+7)
#define high(n) (n+14)

sbit Beep=P1^5;

void music(void);

extern uchar TORH,TORL;
#endif
```

11. UART.c

```
20. }
21.
22. void Uchar2String(uchar x)
23. {
24.    uchar string0[10]={"0123456789"};
25.    uchar string[3];
26.    string[0] = string0[x/16];
27.    string[1] = string0[x/816];
28.    string[2] = 's';
29.    SendString(string);
30. }
```

12. UART.h

```
#ifndef __UART_H__
#define __UART_H__
#include <reg52.h>
#define uchar unsigned char
#define uint unsigned int

void SendString(uchar *dat);
void Uchar2String(uchar x);
#endif
```

VII. Reference

- 基于51单片机IIC通信的AT24C02学习笔记: https://www.cnblogs.com/whik/p/6650092.html
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