

Assignment

Subject: Microprocessor

Date:19/03/2020

Class: Se-III

Subject Teacher: Prof. R.S.PASWAN

1. Explain various Bus control signals.
2. list and explain various signals comprising memory read bus cycle.
3. Draw following bus cycles.
 - a. I/O read bus cycle without wait state
 - b. Memory write bus cycle with wait state.
 - c. Pipelined read bus cycle.
4. Explain following 80386 pins.
 - a. NA
 - b. Ready
 - c. Buzy
 - d. INTA
 - e. Error