Q.1. What are the different levels of security in 80386? a. Task level security b. Page level security c. Segment level security c. Both b & c Answer: c Q.2. Which is the highest and most secure privilege level in 80386? a. Privilege Level 0 b. Privilege Level 1 b. Privilege Level 1 c. Privilege Level 2 Answer: a Q.3. What is the privilege level of Kernal in Operating System? a. Privilege level 2 b. Privilege level 1 c. Privilege level 3 d. Privilege level 0 Answer: d Q.4. What fields are checked by Privilege check unit of CPU? a. CPL b. RPL c. DPL d. All of the above. Answer: d Q.5. Where is the RPL located. b. Selector a. Segment register c. Segment descriptor d. Both a & b. Answer: Both a & b Q.6. Where is the CPL located. a. Hidden portion of segment register b. Segment Descriptor c. TSS Descriptor d. GDT Answer: a Q.7. How many bits are allocated for Limit field in Code/Data Segment descriptor. a. 8 bits b. 5 bits c. 20 bits d. 16 bits Answer: c Q.8 What is the maximum size of segment when Granularity bit is 0. a. 4 GB b. 1 MB c. 64 Terabyte d. 64 KB Answer: b Q.9. What is the maximum size of segment when Granularity bit is 1. a. 4 GB b. 1 MB

d. 64 TB

Answer: a

c. 64 KB

Q. 11 Where is the Granularity bit located.

a. Segment Descriptor b. Segment Register

b. Flag register d. Selector

Answer: a

Q.12 What is the size of each descriptor in GDT.

a. 32 bit b. 64 KB c. 8 byte d. 1 MB

Answer: c

Q.13 Where is the Conforming bit located.

a. Selector b. Code segment descriptor

c. Data segment descriptor d. TSS

Answer: b

Q.14 What is stored in Task State Segment.

a. General purpose registers

b. Segment registers

c. Flag registers d. All of the above.

Answer: d

Q.15 What is stored in Task Register

- a. Selector to TSS descriptor of currently executing task
- b. Base address of GDT
- c. Limit of GDT
- d. All of the above.

Answer: a

Q.16 When T(Trap bit ) is set it generates debug exception on following,

a. Macro callb. Push instructionc. Pop instructiond. Task switching

Answer: d