

Q.1. What is the size of an Interrupt Gate descriptor located in IDT.

- a. 64 KB
- b. 8 byte
- c. 1 MB
- d. 32 bit.

Answer : b

Q.2. Task Gate , Interrupt Gate , Trap Gate descriptors are located in ?

- a. IDT (Interrupt Descriptor Table)
- b. GDT (Global Descriptor Table)
- c. LDT (Local Descriptor Table)
- d. Task Register

Answer : a

Q.3. Maskable interrupts are signalled via.

- a. INTR pin
- b. INT pin
- b. NMI pin
- d. BUSY pin

Answer : a

Q.4. Interrupts given using INT instruction are called.

- a. Hardware interrupts
- b. Software interrupts
- c. Procedures
- d. Macros

Answer : b

Q.5. When IF(Interrupt Flag) is disabled following is true.

- a. Procedures are disabled
- b. Multitasking is disabled
- c. INTR interrupts are disabled
- d. Macros are disabled

Answer : c

Q.6. Base address of IDT(Interrupt Descriptor Table) is stored in following.

- a. IDTR(Interrupt Descriptor Table Register)
- b. LDTR (Local Descriptor Table Register)
- c. GDTR (Global Descriptor Table Register)
- d. TR (Task Register)

Answer : a

Q.7. What is stored in Interrupt Gate Descriptor.

- a. Selector to segment descriptor of Interrupt Procedure segment
- b. Offset within segment of Interrupt procedure segment
- c. DPL of Interrupt Gate descriptor.
- d. All of the above

Answer : d

Q.8. The last instruction of Interrupt procedure is usually.

- a. RET instruction
- b. IRET instruction
- c. CALL instruction
- d. JMP instruction

Answer : b

Q.9. Interrupt 1 is used for following exception.

- a. Divide Error
- b. Debug Exception
- c. Overflow exception
- d. None of the above

Answer : b

Q.10. Interrupt 4 represents following exception.

- a. Overflow exception
- b. Bounds Check exception
- c. Interrupt exception
- d. Divide error

Answer : b