

Maximum Allowable Current Determination of RBS By Using a Directed Graph Model and Greedy Algorithm

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Abstract

Reconfigurable Battery Systems (RBSs) present a promising alternative to traditional battery systems due to their flexible and dynamically changeable topological structure subjected to battery charging and discharging strategies. During the operation of the RBS, the Maximum Allowable Current (MAC) of system that ensures each battery's current remains within a safe range, is a critical indicator to guide the system's reconfiguring control, ensuring its safety and reliability. In this paper, we firstly propose a calculation method for the MAC of arbitrary RBS using a greedy algorithm in conjunction with a directed graph model of the RBS. In this method, a new directed graph model is developed to model the structure of RBS, and a greedy algorithm is designed to find the possible circuit that enable MAC. Then, the MAC is calculated based on the circuit in-cooperate with the equivalent model of batteries and switches. The effectiveness of the proposed method is validated by a novel and complex RBS structure. The results show that this method is capable to calculated the MAC of RBSs with different structures or different battery sizes efficiently, which proves the correctness of this method and its potential in facilitating next-generation RBS designs and applications, including battery isolation.

1 Introduction

Battery Energy Storage Systems (BESSs) are extensively employed in various applications, such as wind power plants and space power systems, to store and release high-quality electrical energy [1, 2, 3, 4, 5]. Typically, a BESS consists of numerous batteries interconnected by series-parallel circuitry to provide the required capacity storage. However, traditional BESSs, in which the batteries are connected in a fixed topology, exhibit a significant weakness in their worst battery due to the so-called cask effect. Moreover, if this worst battery fails during operation, it can exacerbate the

degradation of other batteries with a high possibility, leading to reliability and safety issues [6, 7, 8]. These problems have become significant technical barriers in the development of new-generation space vehicles and urgently need to be addressed [9].

Reconfigurable Battery System (RBS), which can dynamically switch to different circuit topology configurations as required, is expected to solve the above problems[10]. The ability of switching circuit helps to isolate unhealthy batteries, and thereby improve the safety and reliability of the battery system. Figure 1a shows a typical RBS structure developed by Visairo [11] for dynamically adjusting the output voltage and current. In this structure, the batteries can be connected not only in series when the switches $S_1, S_5, S_6, S_7, S_8, S_9$, and S_{13} are closed (Figure 1b), but also in parallel when $S_1, S_2, S_3, S_4, S_5, S_9, S_{10}, S_{11}, S_{12}$, and S_{13} are closed (Figure 1c). Furthermore, when an unhealthy battery, for instance the orange one B_3 in Figure 1d, appears in the RBS, it can be isolated by opening its two adjacent switches (i.e. S_4 and S_{11}), ensuring the system still remains a reliable working mode.

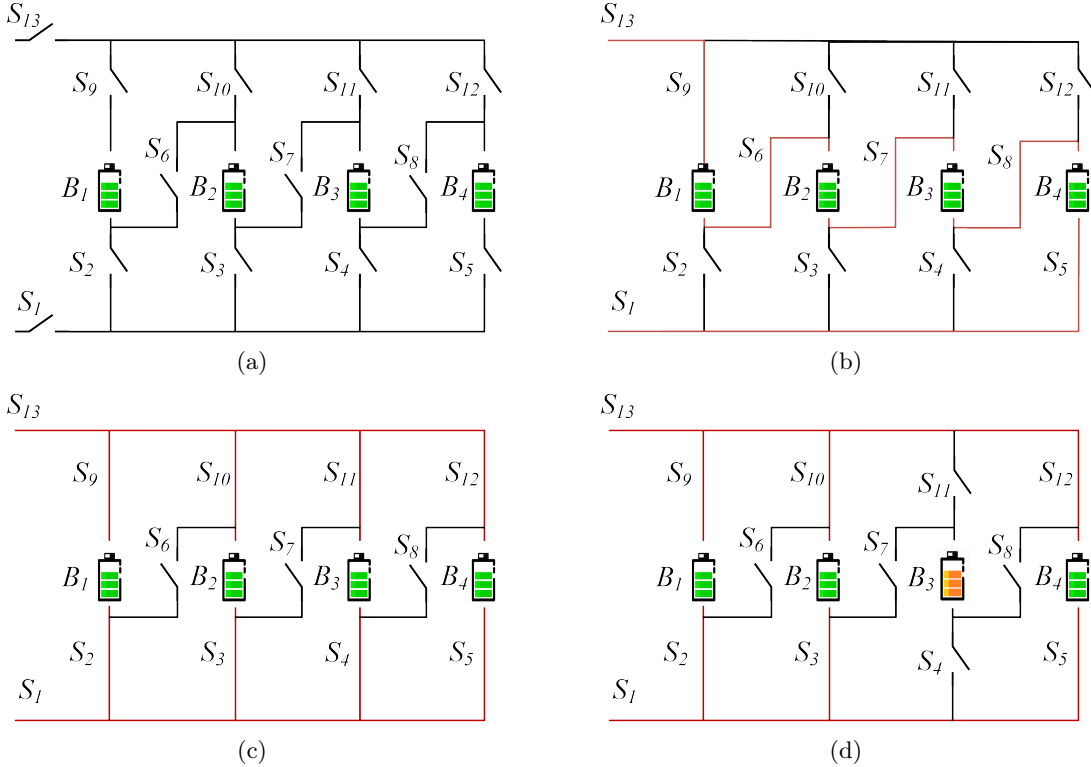


Figure 1: (a) The RBS structure proposed by Visairo[11], with all batteries in (b) series connection, (c) parallel connection, and (d) battery B_3 isolated.

The complex connection structure between batteries and switches in the RBS provides flexibility but also introduces challenges in design and operational control. Unlike traditional BESSs with fixed outputs, the RBS output must be dynamically adjusted by controlling switch states to meet external load requirements. This necessitates additional, time-consuming output performance analysis during design and corresponding control strategies. An incorrect switch control strategy

may cause battery short-circuiting or overload, risking the entire system. The Maximum Allowable Current (MAC), an RBS performance indicator, can guide designers in addressing this issue. MAC is defined as the maximum RBS output current that ensures each battery's current remains within a safe range. Therefore, it provides a benchmark for RBS output current, protecting individual batteries and identifying overall system output limits during operation. Despite its importance, no method currently exists for automatically evaluating MAC for RBSs. In particular, when one or more random cells are isolated, there is still no method to determine the MAC of the remaining RBS in time to assist the system in adjusting the control strategy timely. A universal and automatic method for calculating RBS MAC is urgently needed for practical applications. In this study, a directed graph model and greedy algorithm are employed to determine the MAC of RBS and the corresponding control strategy, effectively calculating the MAC for RBSs with arbitrary structures, including scenarios with isolated batteries.

The remainder of this paper is organized as follows: Section II presents the framework and details of the proposed directed graph model and the greedy algorithm. Section III demonstrated a case study of using the proposed method to determine the MAC of a novel and complex structure. The calculation results and scenarios such as batteries isolation also are discussed. Finally, the concluding remarks are drawn in Section IV.

2 Methodology

The central principle of this method is to make the batteries in RBS connected in parallel as much as possible, thereby maximizing the output current of the RBS. To universally and automatically achieve this, the overall process is divided into four steps, as shown in Figure 2a. Firstly, a directed graph model is established for subsequent computing, which not only contains the connected relationships between batteries and switches, but also retains the performance parameters of the batteries. Subsequently, based on the equivalent circuit, the MAC problem is transformed into specific objective functions and constraints. Then, the shortest paths (SPs, where additional batteries and switches on the path are penalized as distance) for the batteries are obtained using the Dijkstra algorithm to guide the batteries in the RBS connect in parallel. Finally, a greedy algorithm is employed to organize the switches, allowing the batteries to connect via their SPs while satisfying the constraints, resulting in the MAC of the RBS.

2.1 Directed graph Model

He et al. [12] once proposed an abstracted directed graph model for RBS, where the nodes represented the batteries, the edges represented the configuration flexibility, and the weight of each vertex corresponded to the battery voltage (Figure 3a). The model effectively captured all potential system configurations and offered a direct metric for configuration flexibility, but it did not specify the physical implementation of the connectivity between batteries, meaning one graph might have had multiple RBS structures. We previously proposed a novel directed graph model that, in contrast to He's model, used nodes to represent the connections between batteries and switches,

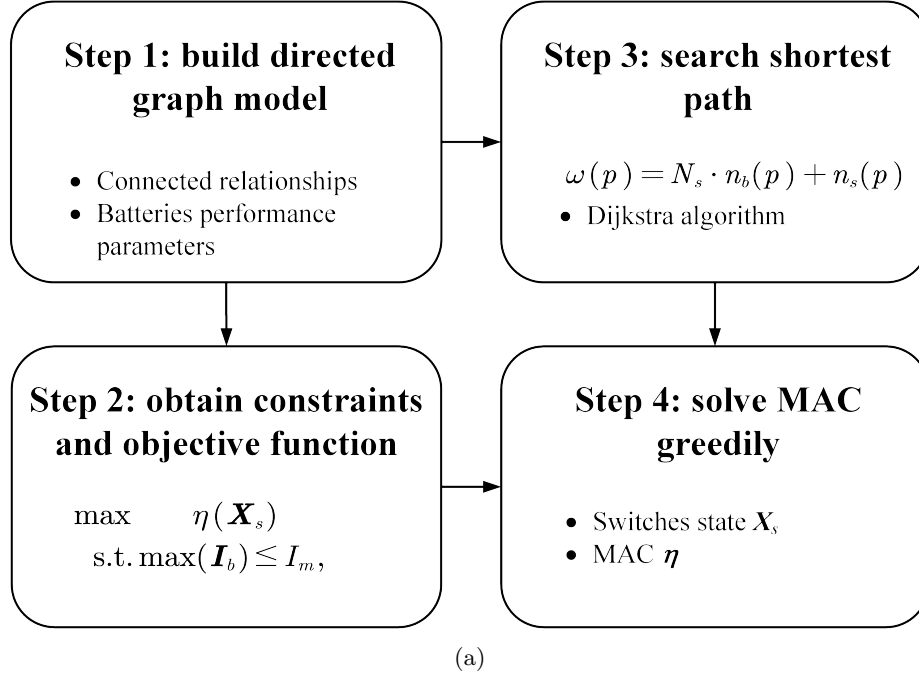


Figure 2: Diagram of this method, which contains four main steps.

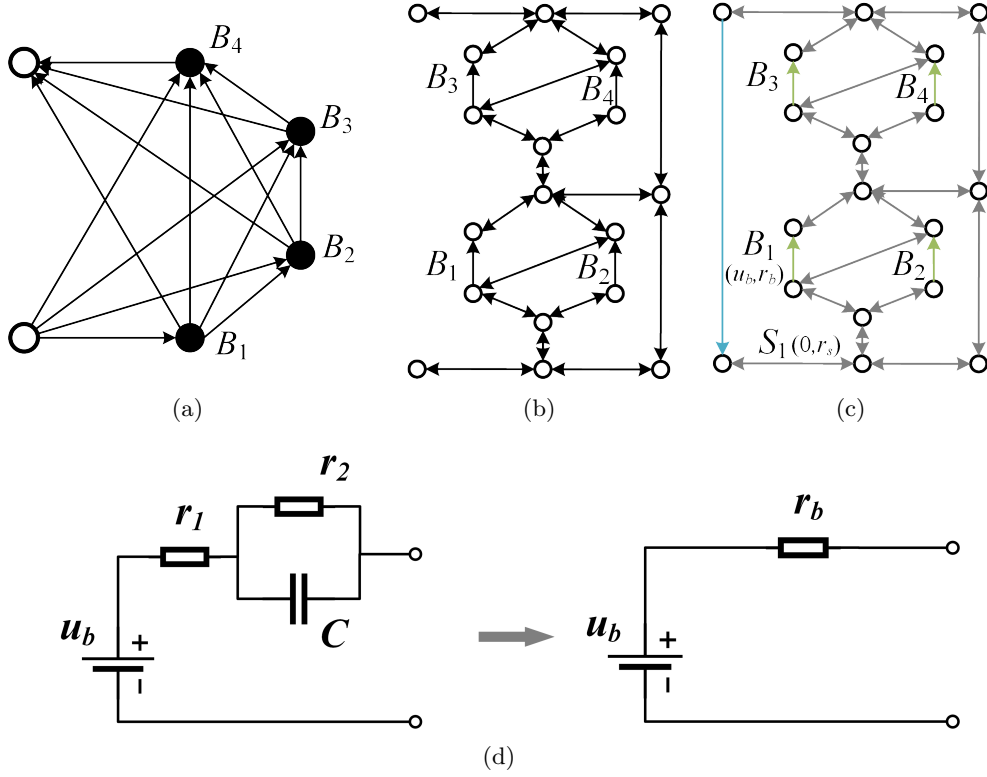


Figure 3: Directed graph models used in (a) He's work [12], (b) our previous work , and (c) this paper. (d) The equivalent circuit of a battery in this method.

and directed edges to represent batteries and switches (Figure 3b), allowing for a one-to-one correspondence between the RBS structure and the directed graph model. This model was able to accurately and comprehensively represent the RBS topological structure but could not be used for quantitative MAC calculations due to the lack of consideration for battery and switch performance parameters. To address this, an improved directed graph model is used here based on our original model, adding electromotive force and resistance attributes on the edges based to equivalent circuits (Figure 3c). The model also considers the external load as an equivalent resistance and integrate it into the analysis, making it a complete circuit model for later circuit analysis. The following will provide a detailed explanation of the method for equating the components in RBS and constructing the directed graph model.

In order to use circuit analysis methods to solve the MAC of the RBS, the components in the RBS are equated to ideal circuit elements. As shown in Figure 3d, the battery in the RBS can be represented as a black-box circuit consisting of two resistors (i.e., r_1 and r_2) and a capacitor (i.e., C), known as the Thevenin model[13, 14]. With an emphasis on the stable output of the RBS, the capacitor in the Thevenin model can be considered as an open circuit without affecting the steady-state current. Therefore, the battery i in the RBS can be simplified as the series connection between a constant voltage source u_i and a resistor r_i . Furthermore, the state of switch j in the RBS is represented by a binary variable x_j , where 0 is for ON and 1 is for OFF, respectively. When the switch is closed, it can be regarded as a resistor with a very small resistance value r_j . Lastly, the external load is considered as a resistor with a value of R_o .

For a given RBS structure, the directed graph model for the RBS is constructed as a directed graph $G(V, E)$ in such a way that:

1. Nodes: The nodes in the directed graph correspond to the connection points of components in the actual RBS. Assuming there are a total of N nodes in the RBS, for the sake of convenience, the anode of the RBS is denoted as v_1 and the cathode as v_N .
2. Edges: The edges in the directed graph correspond to the batteries, switches, and external electrical loads in the actual RBS. Therefore, there are three types of directed edges. For a battery B_i , its directed edge e_i is drawn from the cathode to the anode, as the battery only allows current to flow in one direction when in operation. For a switch S_j , since it is allowed to work under bi-directional currents, it is represented by a pair of directed edges with two-way directions. Regarding the external electronic load, as it is connected to the anode and cathode of the RBS, a directed edge from v_N to v_1 is used to represent it. In conclusion, for a given RBS structure with N_b batteries and N_s switches, the total number of directed edges is $N_b + 2N_s + 1$, where 1 refers to the external electrical load.
3. Edges' attributes: Each edge is assigned two attributes, voltage difference and resistance, based on the equivalent method mentioned above. The values for the battery B_i , switch S_j , and external loads correspond to (u_i, r_i) , $(0, r_j)$, and $(0, R_o)$, respectively.

2.2 Constraints and Objective Function

Based on the definition of MAC, determining the MAC of RBS involves maximizing the RBS output current while ensuring that the currents of all batteries do not exceed the batteries' maximum allowable current. In this subsection, the constraints and objective function to solve the RBS's MAC will be established through circuit analysis, based on the previously constructed directed graph model.

First, the topology in the directed graph model is represented in matrix form \mathbf{A} , known as the incidence matrix, to facilitate circuit analysis. The specific definition of the incidence matrix is shown in Equation 1.

$$a_{kl} = \begin{cases} 1, & \text{edge } l \text{ leaves node } k, \\ -1, & \text{edge } l \text{ enters node } k, \\ 0, & \text{otherwise.} \end{cases} \quad (1)$$

For a directed graph consisting of N nodes and $N_b + 2N_s + 1$ directed edges, its incidence matrix \mathbf{A} is an $N \times (N_b + 2N_s + 1)$ matrix. In this matrix, the rows and columns represent the nodes and edges of the directed graph, respectively. By distinguishing the components in the RBS corresponding to each column, \mathbf{A} can be rewritten as:

$$\mathbf{A} = \begin{bmatrix} \mathbf{A}_b & \mathbf{A}_s & \mathbf{A}_o \end{bmatrix}, \quad (2)$$

where \mathbf{A}_b , \mathbf{A}_s and \mathbf{A}_o are the sub-matrices corresponding to the batteries, switches and external electrical load, respectively. To alleviate computational complexity, matrix \mathbf{A} undergoes dimensionality reduction. Since each directed edge has one node to leave and one to enter, the sum of the values in every column of \mathbf{A} is zero. Therefore removing any single one row will not result in a loss of information. Without loss of generality, the last row is removed here. On the other hand, since each switch in the RBS is represented by a pair of directed edges with two-way directions, the two columns corresponding to the switch are mutually opposite. Thus, for the sub-matrix \mathbf{A}_s , only one column is retained for each pair of columns representing the same switch. As a result, \mathbf{A} can be reduced to a $(N - 1) \times (N_b + N_s + 1)$ matrix, denoted as $\tilde{\mathbf{A}}$, for further calculation of current and voltage. Similar to Equation 2, $\tilde{\mathbf{A}}$ can be rewritten as:

$$\tilde{\mathbf{A}} = \begin{bmatrix} \tilde{\mathbf{A}}_b & \tilde{\mathbf{A}}_s & \tilde{\mathbf{A}}_o \end{bmatrix}. \quad (3)$$

After obtaining the incidence matrix, the currents of all batteries and output in RBS are determined by solving the circuit equations. According to Kirchhoffs law, we have

$$\begin{cases} \tilde{\mathbf{A}}\mathbf{I} = \mathbf{0}, \\ \mathbf{U} = \tilde{\mathbf{A}}^T \mathbf{U}_n, \end{cases} \quad (4)$$

where \mathbf{I} and \mathbf{U} indicate the current and voltage difference arrays of the $N_b + N_s + 1$ edges, respectively;

145 \mathbf{U}_n is the voltage array of the $N - 1$ nodes. These directed edges are treated as generalized branches
 146 and expressed in matrix form as follows

$$\mathbf{I} = \mathbf{Y}\mathbf{X}\mathbf{U} - \mathbf{Y}\mathbf{X}\mathbf{U}_s + \mathbf{I}_s, \quad (5)$$

147 where \mathbf{U}_s and \mathbf{I}_s denote the source voltage and source current of the generalized branches, respec-
 148 tively. Because all batteries have been equivalent to voltage sources rather than current sources in
 149 the previous subsection, all elements of the array \mathbf{I}_s are 0, while the elements of the array \mathbf{U}_s are
 150 equal to the first attribute of the corresponding edges in the directed graph. The \mathbf{Y} in 5 is the ad-
 151 mittance matrix of the circuit, defined as the inverse of the impedance matrix. That is the elements
 152 of the diagonal matrix \mathbf{Y} are equal to the reciprocal of the second attribute of the corresponding
 153 edges in the directed graph, and the off-diagonal elements are 0. The \mathbf{X} is the state matrix, which
 154 describes whether the RBS batteries and switches are allowed to pass current. It is defined as

$$\mathbf{X} = \text{diag}(\underbrace{1, 0 \dots, 1}_{N_b \text{ of } 0/1}, \underbrace{1, 0 \dots, 1}_{N_s \text{ of } 0/1}, 1) = \begin{bmatrix} \mathbf{X}_b & & \\ & \mathbf{X}_s & \\ & & 1 \end{bmatrix}. \quad (6)$$

155 Where the elements x_i of the matrix \mathbf{X}_b represent whether the battery i has been removed from
 156 the circuit, with $x_i = 1$ indicating removal and $x_i = 0$ indicating that it is still available to supply
 157 power. When all batteries are health and capable of providing current to the external load, \mathbf{X}_b is
 158 an identity matrix. The elements x_j of the matrix \mathbf{X}_s represent whether the switch j is closed, with
 159 $x_j = 1$ indicating closure and $x_j = 0$ indicating disconnection, which is consistent with the previous
 160 subsection.

161 Theoretically, the output current I_o and the currents of each battery \mathbf{I}_b in the RBS can be
 162 determined by solving Equations 4, 5, and 6 under any given state \mathbf{X} . In order to obtain specific
 163 constraint conditions and objective functions, it is further assumed that all batteries have the same
 164 electromotive force and internal resistance, denoted as u_b and r_b , respectively. This allows for the
 165 derivation of explicit expressions for I_o and \mathbf{I}_b . After derivation and simplification, the output
 166 current I_o and the currents of each battery \mathbf{I}_b are ultimately represented as Equations 7 and 8,
 167 respectively.

$$I_o = \frac{1}{R_o r_b} \tilde{\mathbf{A}}_o^T \mathbf{Y}_n^{-1}(\mathbf{X}) \tilde{\mathbf{A}}_b \mathbf{U}_b, \quad (7)$$

$$\mathbf{I}_b = \frac{1}{r_b^2} [\tilde{\mathbf{A}}_b^T \mathbf{Y}_n^{-1}(\mathbf{X}) \tilde{\mathbf{A}}_b \mathbf{U}_b - r_b \mathbf{U}_b], \quad (8)$$

169 where \mathbf{U}_b is a $N_b \times 1$ array with all elements equaling to u_b ; \mathbf{Y}_n is the equivalent admittance matrix
 170 of the circuit, defined as

$$\mathbf{Y}_n(\mathbf{X}) = \frac{1}{R_o} \tilde{\mathbf{A}}_o \tilde{\mathbf{A}}_o^T + \frac{1}{r_b} \tilde{\mathbf{A}}_b \mathbf{X}_b \tilde{\mathbf{A}}_b^T + \frac{1}{r_s} \tilde{\mathbf{A}}_s \mathbf{X}_s \tilde{\mathbf{A}}_s^T. \quad (9)$$

171 To characterize the current output capacity of the RBS structure under different switching states,

an indicator η is defined by the ratio of I_o and $\max(\mathbf{I}_b)$ shown in Equation 10:

$$\eta = \frac{I_o}{\max(\mathbf{I}_b)}. \quad (10)$$

Finally the problem of solving MAC can be formulated as

$$\max \eta(\mathbf{X}_s) \quad (11)$$

$$\text{s.t. } \max(\mathbf{I}_b) \leq I_m, \quad (12)$$

where I_m is the maximum allowable current of the battery.

However, it is computationally difficult to solve 11 because of the \mathbf{Y}_n^{-1} . On one hand, due to the introduction of nonlinear terms by \mathbf{Y}_n^{-1} , many effective methods in linear optimization are not suitable for this problem. On the other hand, the rank of \mathbf{Y}_n is proportional to the number of batteries and switches, which can be very large for a large RBS system, leading to significant computational burden. Therefore, intelligent algorithms that rely on evolving by iteration may face efficiency issues when dealing with large RBS system. In order to address this issue, the problem should be considered from the perspective of guiding the RBS to reconstruct as many parallel structures as possible. Consequently, a greedy algorithm based on the shortest path is proposed. The detailed implementation process is presented in the following two subsections.

2.3 Shortest Path

The path p used in this method is defined as the complete route that passes through one battery (or a consecutive series of batteries) and closed switches, connecting the anode v_1 to the cathode v_N of the RBS. By applying a penalty to the series-connected batteries on the path, where additional batteries imply a longer distance, the algorithm encourages the RBS to form parallel structures as much as possible. Meanwhile, to reduce the number of switches controlled during the reconstruction process, a penalty is also applied to the total number of switches on the path, while ensuring the minimum number of batteries. Therefore, the distance ω of the path p is defined by the following equation:

$$\omega(p) = N_s \cdot n_b(p) + n_s(p), \quad (13)$$

where N_s is the total number of switches in the system; $n_b(p)$ and $n_s(p)$ are number of batteries and switches in the path p respectively. Moreover, the shortest path SP_i is defined as the path with the minimum ω for battery i , as shown in the following equation:

$$SP_i = \arg \min_{p \in P_i} \omega(p), \quad (14)$$

where P_i is the set of all paths from v_1 to v_N which pass through the directed edge i .

The SP_i can be solved by the Dijkstra algorithm. The Dijkstra algorithm is a graph search method that finds the shortest path between two given nodes in a weighted graph, efficiently solving the single-source shortest path problem. Assuming that the cathode and anode of battery i are

denoted as v_i^- and v_i^+ respectively, the path p of battery i can be divided into three segments : $v_1 \rightarrow v_i^-$, $v_i^- \rightarrow v_i^+$, and $v_i^+ \rightarrow v_N$. The $v_i^- \rightarrow v_i^+$ is the directed edge corresponding to battery i . With the Dijkstra algorithm, shortest paths for the $v_1 \rightarrow v_i^-$ and $v_i^+ \rightarrow v_N$ can be calculated under the weights given in Equation 13, denoted as $SP(v_1 \rightarrow v_i^-)$ and $SP(v_i^+ \rightarrow v_N)$, respectively. Finally, the SP_i for battery i is formed by the complete path with $SP(v_1 \rightarrow v_i^-)$, $v_i^- \rightarrow v_i^+$, and $SP(v_i^+ \rightarrow v_N)$.

2.4 Greedy Algorithm

From the perspective of series/parallel connections, integrating more batteries into the circuit through their shortest paths (SPs) results in a larger number of batteries connected in parallel, thereby increasing the total output current of the RBS. However, conflicts may arise between the SPs of different batteries. For instance, the SPs of two batteries might form a short-circuited RBS structure, which is not allowed. To address this issue, a greedy algorithm is employed to incorporate as many SPs as possible while satisfying the reconstruction requirements.

The algorithm, as illustrated in Figure ??, can be summarized as follows, with the corresponding pseudo-code presented in Algorithm 1. First, the shortest paths (SPs) are obtained using Equations 13 and 14 in conjunction with Dijkstra Search. Next, the matrix \mathbf{A} is calculated using Equation 1, and the initial N_{set} is set to N_b . The algorithm iteratively checks different combinations of c_b batteries from N_b and updates N_{set} using a dichotomy method until convergence is reached. For each combination, the algorithm constructs an effective solution if possible, and calculates the currents I_o and I_b using Equations 7 and 8. If the maximum current I_b is less than or equal to I_m , the η is calculated using Equation 10, and the maximum η is updated accordingly. Finally, the algorithm outputs the maximum η once N_{set} converges.

3 Case Study

3.1 Structures

Currently, two types of RBS structures have been proposed by Visairo et al. [11] and Lawson et al. [15], both of which have been applied in practice. The primary goal of Visairo's structure (Figure 4b) was to achieve dynamic adjustment of RBS output; however, the isolation of unhealthy batteries was not sufficiently addressed. When batteries need to be isolated in the RBS of Visairo's structure, the methods for isolating them and the subsequent changes in RBS output warrant further investigation. Lawson et al. conducted research on battery isolation in RBS and specifically designed the structure shown in Figure 4a. This structure has the advantage of easily isolating batteries, but it cannot dynamically adjust the output current of RBS. Based on the structures of Visairo and Lawson, this paper presents a new structure, as shown in Figure 4c, which combines the advantages of both. By integrating the Visairo RBS structure into the Lawson RBS structure, the new structure not only allows the flexibility to switch the batteries between series, parallel, and mixed series-parallel modes, but also easily enables the isolation of highly degraded batteries from the RBS. And their variations in output current under battery isolation conditions will be studied. This RBS structure will be

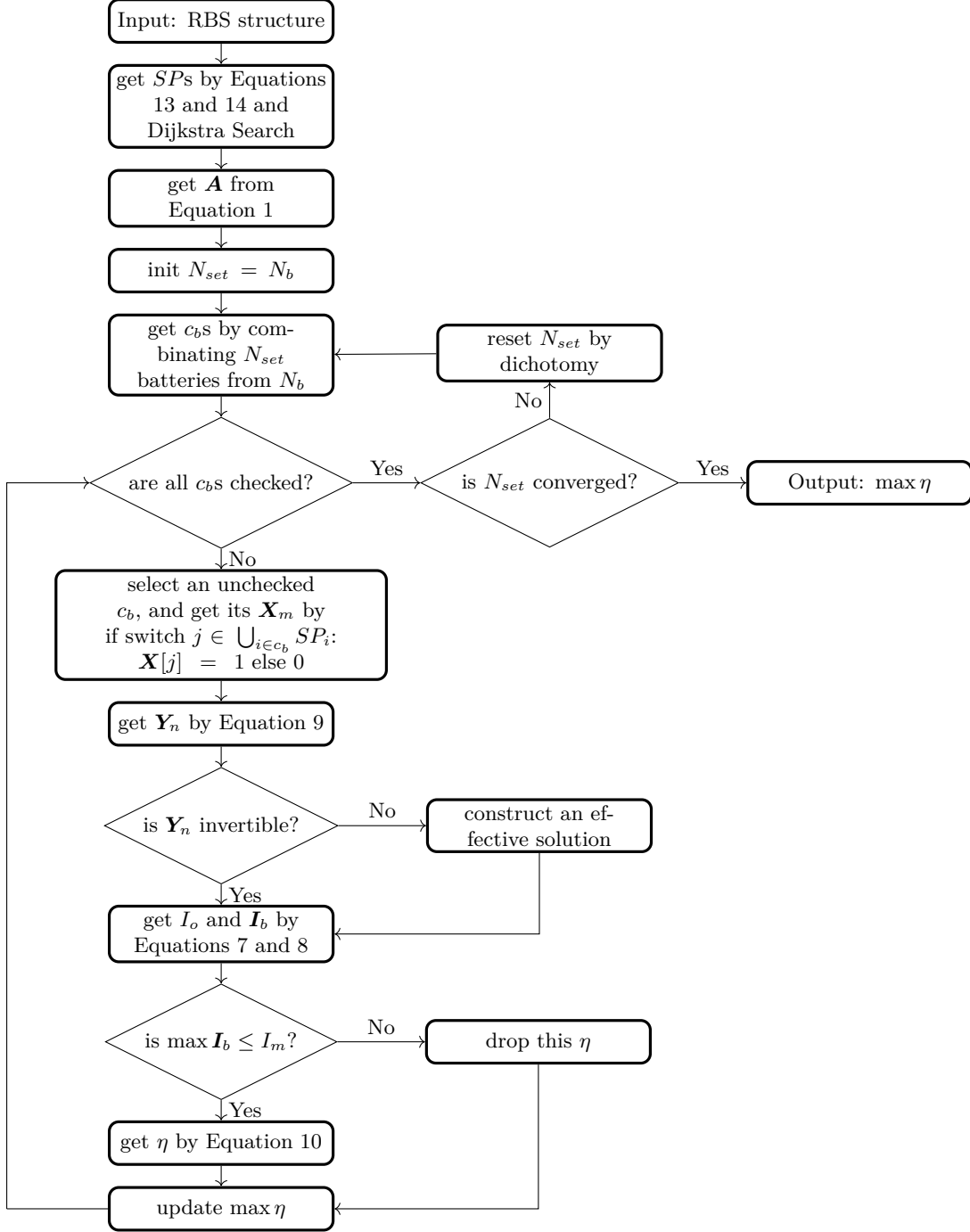


Figure 4: The computational flowchart of the MAC for a given RBS.

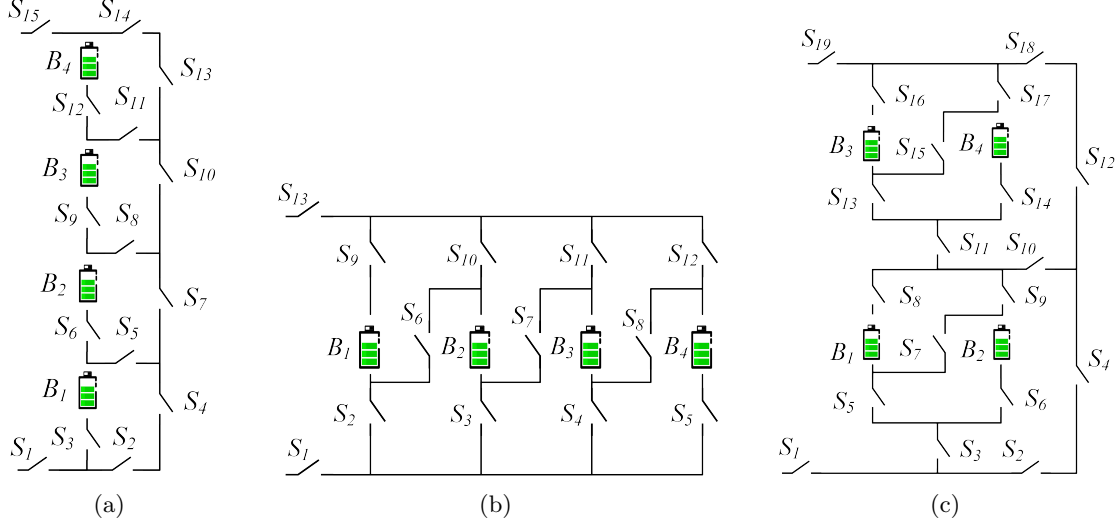


Figure 5: The 4-battery RBS structures proposed by (a)Lawson[15], (b)Visairo[11] and (c)this paper.

used to validate the effectiveness of the proposed method for calculating the MAC, and be compared with the Lawson's and Visairo's structure to illustrate its advantage on battery isolation.

3.2 Result

As shown in Figure 4c, the new RBS structure consists of 4 batteries and 19 switches. The corresponding directed graph is depicted in Figure 5a, which is composed of a total of 18 nodes and 43 edges. Batteries B_1 , B_2 , B_3 , and B_4 are denoted by green directed edges in the graph, while the 19 switches are represented by gray directed edges with bi-directional arrows. The external electrical load is treated as a directed edge from the cathode of the RBS (i.e., node 18) to the anode (i.e., node 1), as indicated by the blue directed edge in the graph. Utilizing Equation 13 and the Dijkstra algorithm, the SPs of the four batteries in the RBS structure of Figure 4c are highlighted by red in Figures 5b-5e. Finally, the MAC calculation results of the structure in Figure 4c are shown as Table 1 and Figure 5f, obtained by the greedy algorithm 1. Table 1 contains the switches states, the output current I_o , battery current I_b and ratio η of the RBS structure with all batteries in good health when the RBS output reaches the MAC. Figure 5f presents the corresponding circuit, with the red highlight indicating that current is flowing through the respective branches.

Table 1: MAC Calculating result of the 4-battery RBS structure in Figure 4c.

Structure	Figure 4c with 4 batteries and 19 switches
Switch ON	$S_1, S_3, S_5, S_6, S_8, S_9, S_{10}, S_{12}, S_{18}, S_{19}$
I_o	$2u_b/(2R_o + r_b)$
I_b	$[u_b/(2R_o + r_b), u_b/(2R_o + r_b), 0, 0]$
η	2

Similarly, the MAC calculation results of the structures in Figures 4a and 4b are shown as Table

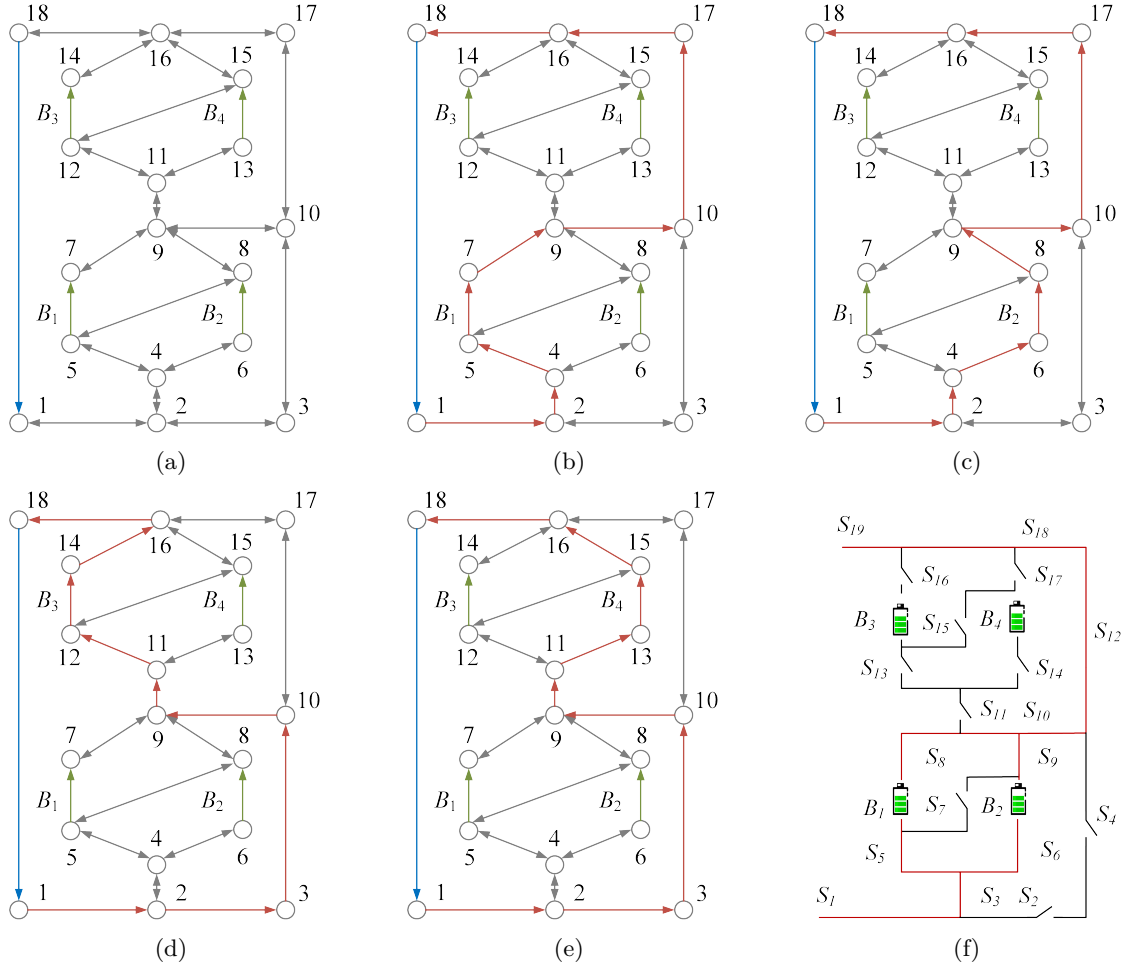


Figure 6: For the RBS structure in Figure 4c, (a) its directed graph and the SP s (highlighted in red) of battery (b) B_1 , (c) B_2 , (d) B_3 , (e) B_4 . (f) The circuit of the RBS with its output reaching the MAC.

252 2 and Table 3, respectively.

Table 2: MAC Calculating result of the 4-battery RBS structure in Figure 4a.

Structure	Figure 4a with 4 batteries and 15 switches
Switch ON	$S_1, S_3, S_5, S_7, S_{10}, S_{13}, S_{14}, S_{15}$
I_o	$u_b/(R_o + r_b)$
I_b	$[u_b/(R_o + r_b), 0, 0, 0]$
η	1

Table 3: MAC Calculating result of the 4-battery RBS structure in Figure 4b.

Structure	Figure 4b with 4 batteries and 13 switches
Switch ON	$S_1, S_2, S_3, S_4, S_5, S_9, S_{10}, S_{11}, S_{12}, S_{13}$
I_o	$4u_b/(4R_o + r_b)$
I_b	$[u_b/(4R_o + r_b), u_b/(4R_o + r_b), u_b/(4R_o + r_b), u_b/(4R_o + r_b)]$
η	4

253 Furthermore, the RBS under the scenario of isolated batteries is taken into consideration and
 254 calculated. The MAC calculation results for the three structures under study, with varying numbers
 255 of isolated batteries, are presented in Table 4. Figures 6a-6d illustrate the corresponding switch con-
 256 trol schemes for the new structure proposed in this paper under different isolated battery conditions.
 257 The characteristics of these three structures in the context of battery isolation will be discussed in
 258 the next subsection.

Table 4: The variation of MAC with the number of isolated batteries for different RBS structures, including the structure proposed by Lawson et al., Visairo et al. , and the structure proposed in this paper.

number of isolated batteries	η of RBS structure		
	our	Visairo's	Lawson's
0	2	4	1
1	2	3	1
2	2 ^a or 1 ^b	2	1
3	1	1	1

^a isolate two batteries within the same substructure, as shown in Figure 6b

^b isolate one battery in each of the two substructures, as shown in Figure 6c

259 3.3 Discussion

260 In this subsection, we firstly discuss the correctness of the results presented in Figure 5 and Table 1.
 261 When B_1 and B_2 or B_3 and B_4 are connected in parallel, the RBS can output the maximum current,
 262 which is $\eta = 2$, i.e., twice the current output of a single battery in RBS. Adding more batteries to
 263 the main circuit can only form a series structure and will not improve the MAC. Therefore, the
 264 switches state given in Table 1 can make the RBS output current reach the maximum.

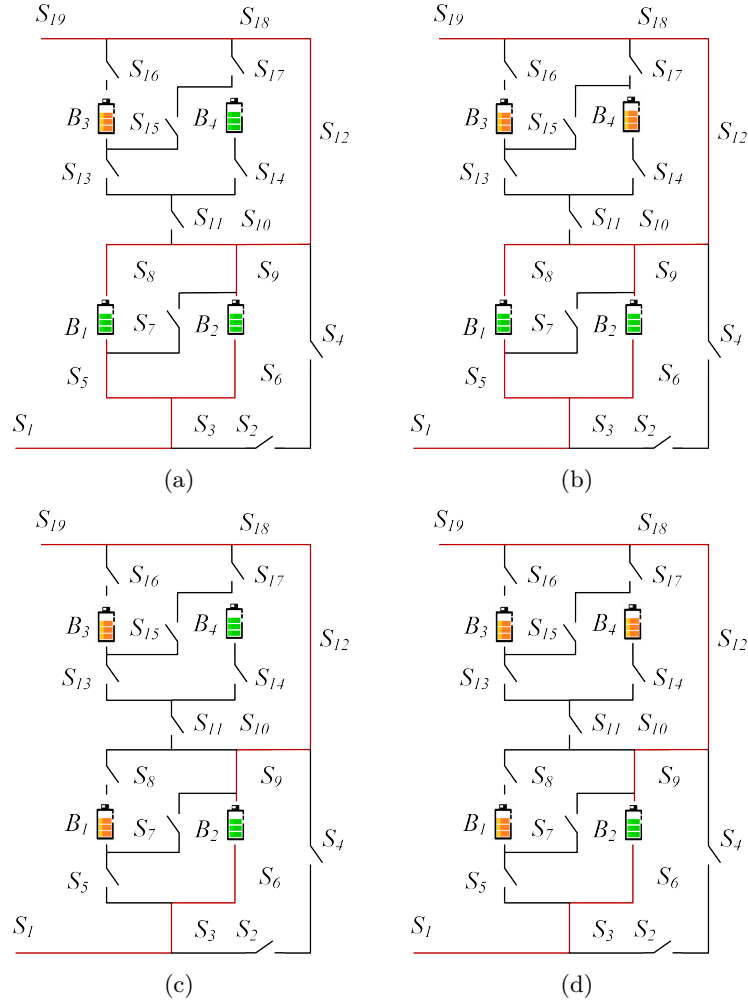


Figure 7: The circuit states of MACs when isolating (a) one, (b) two (best case), (c) two (worst case) and (d) three batteries for the structure in Figure 4c.

It is important to note that when solving for MAC, η is used as the objective function instead of I_o . This choice makes the result of MAC more reasonable. As shown in Table 1, I_o and I_b are functions of R_o , u_b , and r_b . If I_o were used as the objective function, even for the same RBS structure, the MAC result and corresponding switches state could change due to different external electrical appliances. It would increase the difficulty and uncertainty in RBS structure design. In contrast, by using η as the objective function, which is defined as the ratio of I_o and $\max I_b$, the influence of these factors on the results can be eliminated. η solely reflects the maximum output current capability of the RBS structure. Assuming that the maximum allowed current of batteries in the RBS is I_m , the maximum output current of the RBS structure can be calculated as ηI_m by determining the η of the structure. Therefore, compared to I_o , η is more suitable for structure design.

The method proposed in this paper is significant for the design of next-generation RBSs in the following aspects. Most of the currently proposed RBS structures[16, 17, 18, 19, 20, 21] exhibit simple topological characteristics, and the calculation of MACs is relatively straightforward, even intuitive. However, these simple structures do not always fully satisfy the requirements of complex applications, such as dynamically adapting the circuit to variable and random operating conditions, and actively equalizing differences among the batteries in the RBS. Moreover, isolating the batteries disrupts the original regularity and symmetry of the topology, which complicates the otherwise simple structure, and the maximum output current of the system becomes more challenging to obtain. Owing to the advantages of pervasiveness and automation, the proposed method can be employed to calculate the MAC of arbitrary RBS structures, which helps to address the aforementioned issues and paves the way for more complex and flexible RBS structure design.

To illustrate this point, the MACs of the three RBS structures mentioned above are calculated after the batteries are isolated, as shown in Table 4. Specifically, for the structure presented in Figure 4c, the corresponding circuit states of MACs when isolating different numbers of batteries are depicted in Figures 6a-6d. This structure has two cases of isolating two batteries: one is to isolate two batteries within the same substructure (Figure 6b), in which case $\eta = 2$; the other is to isolate one battery in each of the two substructures (Figure 6c), in which case $\eta = 1$. From the results, it can be observed that the proposed method provides reasonable outcomes for isolating batteries with any number and position.

Furthermore, the performance of output current for the three RBS when isolating batteries is also shown in Table 4. For the structure proposed by Lawson et al., the MAC remains the same as that without isolated battery cells, i.e., $\eta = 1$, when the number of isolated battery cells increases, until all the cells in the RBS are isolated. For Visairo's structure, the MAC decreases as the number of isolated battery cells increases, until $\eta = 0$. In contrast, the MAC of the structure proposed in this work is positioned between the two structures. This indicates that the structure proposed in this paper, compared to Lawson's structure, has a larger MAC under the same number of batteries, which means a wider output current regulation range. On the other hand, by simply changing the states of S_2 , S_4 , S_{11} , and S_{12} in the conversion structure, this structure can address the majority of battery isolation scenarios, whereas Visairo's structure requires specific battery targeting and switch control. In summary, the structure proposed in this paper has the advantages of both Lawson's and

306 Visairo's structures.

307 4 Conclusion

308 This paper proposes a pervasive and automatical method for computing the MAC of the given RBS.
 309 The method is implemented by a greedy algorithm combined with a directed graph model, whose
 310 effectiveness is tested on a novel and complex RBS structure. The method remains effective for
 311 the application scenario of RBS battery isolation and demonstrates that the novel structure has the
 312 advantage on flexible output current and convenient battery isolation. Future research could focus
 313 on developing new indicators to evaluate the performance of the RBS with the currents and voltages
 314 obtained by the method, as well as modifying the equivalent model of the battery to allow for more
 315 accurate simulations of the RBS, including transient analysis.

316 5 Appendix

Algorithm 1: Get the max available currents of a certain RBS

Data: Directed graph model $G(V, E)$ of the RBS
Result: $\max \eta$

```

1 for  $i \in E_b$  do
2    $P_i \leftarrow \{path | \text{starts at } v_1 \text{ and ends at } v_n\};$ 
3    $SP_i \leftarrow p_i$  which has the minimum  $\omega(p_i)$  among all  $p_i \in P_i$ .
4 end
5 get  $A$  by Equation 1;
6 while not yet determine  $\max \eta$  do
7    $N_{set} \leftarrow$  number of selected  $SP$ s calculated by dichotomy;
8    $C_b \leftarrow$  set of all combinations of  $N_{set}$  batteries from  $N_b$ ;
9   for  $c_b \in C_b$  do
10     $x_s \leftarrow$  list of all switches' state:  $x_s[j] = 1$  if  $j \in \bigcup_{i \in c_b} SP_i$  else 0;
11     $X \leftarrow diag[1, 1, \dots, 1, x_s];$ 
12    get  $Y_n$  by Equation 9;
13    if  $Y_n$  is invertible then
14      else
15         $\mid$  construct an effective solution
16      end
17      get  $I_o$  by Equation 7;
18      get  $I_b$  by Equation 8;
19      if  $\max(I_b) \leq I_m$  then
20         $\mid \eta \leftarrow I_o / \max(I_b);$ 
21      else
22         $\mid$  break
23      end
24    end
25 end
```

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Author Contributions

B. Xu conceived the main idea, formulated the overarching research goals and aims, designed the algorithm, and reviewed and revised the manuscript. G. Hua developed and analyzed the model, implemented the code and supporting algorithms, and wrote the initial draft. C. Qian provided critical review, commentary, and revisions. Q. Xia contributed to shaping the research, analysis, and manuscript. B. Sun conducted the research and investigation process. Y. Ren secured the funding and supervised the project. Z. Wang verified the results and provided necessary resources.

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Conflicts of Interest

The authors declare that there is no conflict of interest regarding the publication of this article.

Data Availability

This work does not require any data to be declared or publicly disclosed.

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