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DS50002423A

PIC24FJ1024GB610 Plug-In Module (PIM) Information Sheet

Overview

The PIC24FJ1024GB610 Plug-in Module is designed to demonstrate the capabilities of the PIC24FJ1024GB610 family using the Explorer 16 Board. Most of the pins from the device are mapped directly to the PIM connector (100-Pin ICE). The exceptions are those pins that are remapped to provide remappable functionality to the pins in the PICtail™ Plus socket. Table 1 below shows the exceptions of the PIC24FJ1024GB610 pins that are not directly connected to the ICE connector.

Pin Mapping

There are several device pins that don't have a one-to-one connection to the ICE connector. Some are hard-wired to a certain location, while others are mapped to the connector through jumpers.

Table 1 summarizes the device pins that don't have one-to-one connections.

Table 1: Pin Mapping Exceptions (No Direct Connections)

Signal Name	Device Pin	PIM Pin
OCM1C/CTED3/RG15	1	8
RPI40/OCM2D/RC3	8	66
AN16/RPI41/OCM3C/PMCS2/RC4	9	52
USBID/RF3	51	95
RP30/RF2	52	51
VBUS/RF7	54	1
VUSB3VS	55	N/C
RPI36/SCL1/PMA22/RA14	66	57
RPI35/SDA1/PMBE1/RA15	67	56
SOSCI/C3IND/RC13	73	N/C
SOSCO/C3INC/RPI37/PWRLCLK/RC14	74	N/C
CTED11/PMA16/RG14	95	9

Legend: N/C = Signal is Not Connected to the PIM header

Jumpers

Table 2 and Table 3 are the jumper tables on the silkscreen of the PIM.

Table 2: USB Mode

Jumper	Shunt	MCU Pin	Signal	PIM
J1	1-2	90	PMD8/RG0	79
J2	1-2	56	D-/RG3	89
J3	1-2	57	D+/RG2	90
J4	1-2	89	PMD9/RG1	77

Table 3: 16-Bit EPMP Mode

Jumper	Shunt	MCU Pin	Signal	PIM
J1	2-3	79	PMD12/RD12	79
J2	2-3	89	PMD9/RG1	89
J3	2-3	90	PMD8/RG0	90
J4	Open	89	Not Used	Not Used

