[Pre-lecture Assignment#5]

- Reading 1: Read Sections 3.5 IPC in Shared-Memory Systems (page 125-127), 6.1 Background (page 257-260). Note that reading assignments may appear in your homework and quiz.
- Video Lecture 1 (00:00~33:00, around 33 minutes): Synchronization; Race condition

https://core.ewha.ac.kr/publicview/C0101020170403151644920369

- <u>No</u> submissions. However, pre-lecture assignments, as well as slides, class discussions, programming assignments, and textbook contents covering last week's lecture may appear in your next quiz.

데이터의 절근

Storage Box Execution - Box 2. OFNOL data Memory, as CPU, सम्माप्त मडमार TEMPE ZERZ SM DS.

Race Condition S-box 를 공유라는 E-60x가 여전 있는 경우 Race Condition 의 개상에 있음

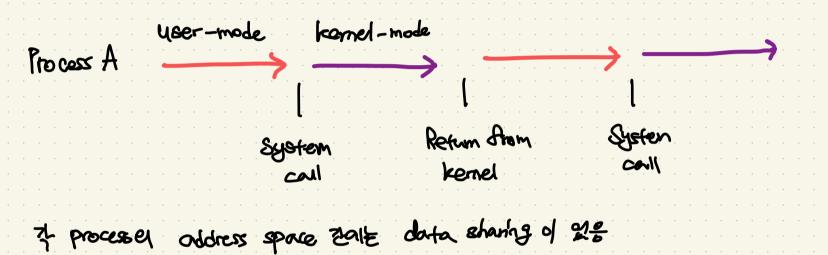
Address-space

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- 2. Process? Lystem call 2 aleq leanel mode 2 fin 301211

 Context switch? 2101112 759
- 3. Multiprocessor and shared memory well kernel data

race condition by a offer



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interrupt handler v.s. kernel

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) disable/enable interrupt.

CPU Multiprocessor Memory प्राप्त CPUIL व्ययमिटड count है store सुरिश ...? 이거는 interrupt enable/disable을 해덜볼가능

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Critical Section on हेनड़ र सुरह वेहरा.