

Debates/Discussions – Week 11

1. Is the “safe sequence” order in Banker’s Algorithm and the “CPU scheduling” order the same or different? Explain why.
2. In class, we discussed that “OS view of cache is same as main memory.” Explain what this means, and the reason why it is the same.
3. In class, we gave two reasons why paging is used: transparency, no external fragmentation. Explain each reason.
4. If TLB access time=1ns, Memory access time=200ns, TLB hit ratio=90%, then what is the **EAT**?