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[illegible]

```
add.w #0002h, R6
mov.w R6 , 0202h(R6)
```

;Lab Instruction 9 :

; Copy the contents of IO port inputs P1IN and P2IN to registers R10 and R11 respectively using absolute mode

```
mov.b &P1IN , R10
mov.b &P2IN , R11
```

;Lab Instruction 10 :

; Copy the word in RAM starting at 0x0204 to register R12 using indirect register mode addressing.

```
mov.w # 0204h, R11
mov.w @ R11 , R12
```

;Lab Instruction 11 :

; Copy the words in RAM starting at 0x0206 to 0x020E to register R13 using indirect autoincrement register mode.

```
mov.w #0206h , R15
mov.w @R15+, R13
```

```
mov.w @R15+, R13
```

```
mov.w @R15+, R13
```

```
mov.w @R15+, R13
```

```
mov.w @R15+, R13
```

```
mov.w @R15+, R13
```

```
; S
```

```
JMP main
```

```
; jump to current location '$'
; (endless loop)
```

```
END
```

