



## UNIVERSITY OF COLOMBO, SRI LANKA



## UNIVERSITY OF COLOMBO SCHOOL OF COMPUTING

## DEGREE OF BACHELOR OF INFORMATION TECHNOLOGY (EXTERNAL)

Academic Year 2017 – 1st Year Examination – Semester 1

IT1205 - Computer Systems I
Multiple Choice Question Paper

27<sup>th</sup> May, 2017 (TWO HOURS)

## Important Instructions:

- The duration of the paper is 2 (two) hours.
- The medium of instruction and questions is English.
- The paper has 50 questions and 11 pages.
- All questions are of the MCQ (Multiple Choice Questions) type.
- All guestions should be answered.
- Each question will have 5 (five) choices with **one or more** correct answers.
- All questions will carry equal marks.
- There will be a penalty for incorrect responses to discourage guessing.
- The mark given for a question will vary from 0 (All the incorrect choices are marked & no correct choices are marked) to +1 (All the correct choices are marked & no incorrect choices are marked).
- Answers should be marked on the special answer sheet provided.
- Note that questions appear on both sides of the paper.

  If a page is not printed, please inform the supervisor immediately.
- Calculators are not allowed.
- Mark the correct choices on the question paper first and then transfer them to the given answer sheet which will be machine marked. Please completely read and follow the instructions given on the other side of the answer sheet before you shade your correct choices.

<ul><li>(b) Extended Bit Coded</li><li>(c) Extended Bit Code I</li><li>(d) Extended Bit Case D</li></ul>	ded Decimal Interchange Code Decimal Information Code Decimal Interchange Code Decimal Interchange Code Decimal Interchange Code Decimal Interchange Code	
<del>-</del>	omputer implemented binary numb ted separate computation and mem	<del>-</del>
(a) Mark I	(b) ABC	(c) ENIAC
(d) Z3	(e) Difference Engine	
(a) Babbage (d) Pascal	(b) Napier (e) Mauchly	(c) Leibniz
	built by the Intel corporation was c	alled
(a) 8008	(b) 8080	(c) 8086
(d) 8080	(e) 4004	
The processing speed of f	irst generation computers was	
(a) milliseconds	(b) microseconds	(c) nanoseconds
(d) picoseconds	(e) attoseconds	
What is the decimal value	of the hexadecimal number 777?	
(a) 19	(b) 191	(c) 1911
(d) 1991	(e) 19111	
Convert the decimal number	per 151.75 to binary.	
Convert the decimal number		
(a) 10000111.11	(b) 11010	011.11
	(b) 11010 (d) 10010	

	(a) Most Significant B	it (MSB)	
		e following sequence of remainders	are written in descending order
	until the final rema		
	(c) Least Significant E		he original ICD which is then
	moved to the MSB	final remainder is used to replace to position.	the original LSB, which is then
		the following sequence of remaind	lers are written in descending ord
		ander is delicated.	
	_	is the correct decimal number 0001110101000000000000000000000000000	of the 32-bit IEEE floating p
	(a) +142.625	(b) +87.625	(c) +197.5
	(d) +119.875	(e) +43.75	
	0101 0101011010 with a	a sign bit, 5-bit exponent and 10-bit	t mantissa?
	_		6-bit floating point representation to mantissa?  (c) +89.625
010 WI	(a) +87.625 (d) +43.625	(b) +85.625 (e) +47.625 y (round-off-error) when converting	(c) +89.625
010 WI	(a) +87.625 (d) +43.625 hat is the loss of accuracy	(b) +85.625 (e) +47.625 y (round-off-error) when converting	(c) +89.625
010 WI	(a) +87.625 (d) +43.625 hat is the loss of accuracy 32-bit IEEE floating points	(b) +85.625 (e) +47.625  y (round-off-error) when converting the representation?	(c) +89.625 g the decimal value +1000576.96
Wi to	(a) +87.625 (d) +43.625 hat is the loss of accuracy 32-bit IEEE floating points (d) 0.5 (d) 0.0625	(b) +85.625 (e) +47.625  y (round-off-error) when converting the representation?  (b) 0.25 (e) 0.03125  gical operator(s) is/are used in relations.	(c) +89.625 g the decimal value +1000576.96 (c) 0.125
Wi to	(a) +87.625 (d) +43.625 hat is the loss of accuracy 32-bit IEEE floating points (a) 0.5 (d) 0.0625 hich of the following log	(b) +85.625 (e) +47.625  y (round-off-error) when converting the representation?  (b) 0.25 (e) 0.03125  gical operator(s) is/are used in relations.	(c) +89.625 g the decimal value +1000576.96 (c) 0.125

13) Consider the following Boolean function

$$F(x, y) = (\bar{x} + \bar{y} + x + y).(\bar{x}.\bar{y})$$

Which of the following Boolean functions provide(s) a simplified form of F?

(a)  $\bar{x}$ 

(b)  $\overline{y}$ 

(c) *x* 

(d) y

(e)  $\bar{x}.\bar{y}$ 

14) Consider the following Boolean function

$$F(x, y) = (\overline{x}.\overline{y}).(x + y)$$

Which of the following Boolean function provide(s) a simplified form of F?

(a)  $\bar{x}$ 

(b)  $\overline{y}$ 

(c) 0

(d) y

(e)  $\bar{x}.\bar{y}$ 

15) Consider the following Boolean function

$$F(A, B, C) = (A.B) + (A.C) + (B.C)$$

How many NAND gates are required for the above Boolean function, if it is to be implemented only using NAND gates?

(a) 3

(b) 4

(c) 5

(d) 6

(e) 7

Output of the Boolean function F(a,b,c) = (a+b).(b+c).(a+c) is 1 when

(a) a=1, b=1, c=0

(b) a=1, b=0, c=1

(c) a=1, b=1, c=1

(d) a=0, b=0, c=1

(e) a=0, b=1, c=0

Output of the Boolean function  $F(a,b,c) = (\overline{ab}) + (\overline{bc}) + (\overline{ac})$  is 1 when

(a) a=1, b=1, c=0

(b) a=1, b=0, c=1

(c) a=1, b=1, c=1

(d) a=0, b=0, c=1

(e) a=0, b=1, c=0

(a) AND, OR	(b) AND, NOT	(c) OR, NOT
(d) NAND	(e) NOR	
Which of the following compone	ents is/are not (a) part(s) of the	CPU?
(a) Registers	(b) Control Unit	(c) Memory
(d) Arithmetic Logic Unit	(e) System Clock	
Questions 20 and 21 are based	on the following:	
Suppose, a particular memory w	ith 4GB memory addresses an	d each location can hold a v
size 32 bits.		
What should be the size of the m	emory space?	
(a) 2GB	(b) 4GB	(c) 8GB
(d) 16GB	(e) 32GB	
(a)12	(b) 22	(c) 26
(d)32	(e) 40	(1)
. ,	. ,	
	he triggered within the process	sor as an interrupt?
Which of the following will not	or disgered within the process	
	oo arggerea waaan ale process	
(a) Arithmetic errors	oe arggerea wrami ale process	
<ul><li>(a) Arithmetic errors</li><li>(b) Overflow or Underflow</li></ul>	oe arggerea wrami the process	
<ul><li>(a) Arithmetic errors</li><li>(b) Overflow or Underflow</li><li>(c) Invalid Instructions</li></ul>		
<ul><li>(a) Arithmetic errors</li><li>(b) Overflow or Underflow</li><li>(c) Invalid Instructions</li><li>(d) User-defined break points</li></ul>		
<ul><li>(a) Arithmetic errors</li><li>(b) Overflow or Underflow</li><li>(c) Invalid Instructions</li></ul>		
<ul> <li>(a) Arithmetic errors</li> <li>(b) Overflow or Underflow</li> <li>(c) Invalid Instructions</li> <li>(d) User-defined break points</li> <li>(e) Input/output requests</li> </ul>	S	
<ul> <li>(a) Arithmetic errors</li> <li>(b) Overflow or Underflow</li> <li>(c) Invalid Instructions</li> <li>(d) User-defined break points</li> <li>(e) Input/output requests</li> </ul>	s/are true with respect to General	
(a) Arithmetic errors (b) Overflow or Underflow (c) Invalid Instructions (d) User-defined break points (e) Input/output requests  Which of the following statements in	s/are true with respect to General readily accessed by the CPU	
(a) Arithmetic errors (b) Overflow or Underflow (c) Invalid Instructions (d) User-defined break points (e) Input/output requests  Which of the following statements is  (a) GPR hold data that can be referred.	s/are true with respect to General readily accessed by the CPU arithmetic operations	
(a) Arithmetic errors (b) Overflow or Underflow (c) Invalid Instructions (d) User-defined break points (e) Input/output requests  Which of the following statements is (a) GPR hold data that can be recommended to the following statements in the following statements is the following statements is the following statements in the following statements is the following statements in the following statements is the following statements in the following statement in the following	s/are true with respect to General readily accessed by the CPU arithmetic operations Traffic Manager	
(b) Overflow or Underflow (c) Invalid Instructions (d) User-defined break points (e) Input/output requests  Which of the following statements is (a) GPR hold data that can be refused to the following statements of the GPR carries out logical and following statements of the GPR carries out logical and following statements is given by the following statements in the following statements is given by the following statements in the following statement in the following statements in the following statement in th	s/are true with respect to General readily accessed by the CPU arithmetic operations Traffic Manager	Purpose Registers (GPR)?

	(a) Primary and Secondary	(b) Random and Sequential	(c) ROM and RAM
	(d) Internal and External	(e) Primary and Microprocessor	
	A storage area used to store data nits can handle data is the	to compensate for the difference in	speed at which the different
	(a) Accumulator	(b) CPU Registers	(c) Buffer
	(d) Memory	(e) Hard Disk	
26) T	The ALU of a computer responds	s to the commands coming from the	
	(a) Primary Memory	(b) Cache Memory	(c) Control Unit
	(d) Secondary Memory	(e) External Memory	
27) T	To produce high quality graphics	(hardcopy) in color, you would wan	nt to use a/an:
	(a) RGB Monitor	(b) Plotter	(c) Ink-Jet Printer
	(d) Laser Printer	(e) Optical Printer	
28) V	Which of the following is/are not	t (an) input device(s)?	
	(a) OCR	(b) Optical Scanners	(c) Voice Recognition Device
	(d) Light Pen	(e) COM (Computer Output to Microfilm)	
29) V	Which of the following statemen	ts is/are true with respect to the CPU	cycle?
	(a) Multiplication takes long	er than addition	
	(b) CPU throughput does not	defend on the number of instruction	as in a program
	(c) Floating point operations	requires more cycles than integer op	perations
	(d) Accessing memory takes	longer than accessing CPU registers	
	(e) CPU throughput does not	defend on the number of CPU cycle	es per instruction

24) The two kinds of main memory are:

30)	Which of the following is/are true with respect to the CPU time required to run a program?
	(a) Time it takes to run the program
	(b) No. of instructions in a program
	(c) Average cycles per instruction
	(d) Does not defend on the amount of work in one CPU cycle
	(e) Time it takes to a CPU cycle
31)	What type of control pins are needed in a microprocessor to regulate traffic on the bus, in order to prevent two devices from trying to use it at the same time?
	(a) Bus Control (b) Status Register (c) Data Control
	(d) Control Unit (e) Bus Arbitration
32)	Which of the following statements is/are true with respect to Cache Memory?
	(a) Accessing cache memory faster than accessing main memory, but more expensive
	(b) Accessing cache memory faster than accessing CPU registers
	(c) Cache memory contains data which the CPU is likely to use next
	<ul><li>(d) Cache memory allows the computer to behave as though it has more memory than what is physically available</li><li>(e) Cache memory is an extension of main memory using the Hard Disk</li></ul>
	(e) Cache memory is an extension of main memory using the flatu bisk
33)	Registers which are partially visible to users and used to hold conditional codes (bits set by the CPU hardware as the result of operations), are known as
	(a) Status Register (b) Flags (c) Program Counter
	(d) Memory Address Registers (e) General Purpose Registers
34)	Which of the following statements is/are true about the Virtual Memory?
	<ul> <li>(a) Accessing virtual memory faster than accessing main memory, but less expensive</li> <li>(b) Accessing virtual memory slower than accessing main memory, but more expensive</li> <li>(c) Virtual memory need a scheme that allows us to translate virtual addresses into physical addresses</li> <li>(d) Virtual memory allows the computer to behave as though it has more memory than what is physically available</li> <li>(e) Virtual memory is an extension of main memory using the Hard Disk</li> </ul>

	(c) Magnetic disk memory
	(d) Magnetic bubble memory
	(e) Magnetic drum memory
6)	Different components of the motherboard of a PC unit are linked together by sets of parallel electrical conducting lines. What are these lines called?
	circultar conducting lines. What are these lines caned:
	(a) Conductors (b) Connectors (c) Buses
	(d) Consecutive lines (e) Protocols
7)	What is the main difference between a mainframe and a super computer?
	(a) A super computer is much larger than a mainframe computers
	(b) A super computer is much smaller than mainframe computers
	(c) A supercomputer is focused to execute a few programs as fast as possible while mainframe uses its power to execute as many programs concurrently
	(d) A supercomputer is focused to execute as many programs as possible while mainframe uses its power to execute few programs as fast as possible.
	(e) Performance of a supercomputer is measured in floating-point operations per second (FLOPS) instead of million instruction per second (MIPS)
)	Which of the following statements is/are <b>false</b> with respect to a magnetic disk?
	(a) It is expensive relative to magnetic tape
	(b) Users can easily update records by writing over the old data
	(c) It provides only sequential access to stored data
	(d) Magnetic storage uses different patterns of magnetisation in a magnetisable material to store data and is a form of non-volatile memory
	(e) Magnetic storage media can be classified as either sequential access memory or random

The magnetic storage chip used to provide non-volatile direct access storage of data and has no moving parts are known as

(a) Magnetic core memory(b) Magnetic tape memory

	(a) Keyboard	(b) System Bus	(c) Monitor
	(d) Memory	(e) CPU	
W	hich of the following inpu	t devices is/are user-programma	able?
	(a) Smart terminal	(b) Dumb terminal	(c) Intelligent terminal
	(d) VDT	(e) Think terminal	
W		e true about hybrid computer?	
	(a) Hybrid computers ar computers	e computers that exhibit feature	s of analog computers and digital
	(b) Nervous system in a	nimals is a form of hybrid comp	uter
		orid computing system was the I	
		inical problems to be overcome	in hybrid compliters is minimizing
	digital-computer noi	se in analog computing element hould not be distinguished from	s and grounding systems
W	digital-computer noi (e) Hybrid computers sh	se in analog computing element	s and grounding systems hybrid systems
W	digital-computer noi (e) Hybrid computers sh  hich of the following is/ar  (a) Used to transmission (b) Save space in cabling (c) Immune to electrical (d) Glass or plastic fiber	se in analog computing element hould not be distinguished from the true about Optical Fiber Cable at of data over long distances at hig specially in LAN environment interface preventing cross talks designed to guide light over its	s and grounding systems hybrid systems es?  igh data range like 40GB/s length
	digital-computer noi (e) Hybrid computers sh  hich of the following is/ar  (a) Used to transmission (b) Save space in cabling (c) Immune to electrical (d) Glass or plastic fiber	se in analog computing element hould not be distinguished from the true about Optical Fiber Cable of data over long distances at high specially in LAN environment interface preventing cross talks of designed to guide light over its lid carrying the signals in the span	s and grounding systems hybrid systems es?  igh data range like 40GB/s length
	digital-computer noi (e) Hybrid computers sh  hich of the following is/ar  (a) Used to transmission (b) Save space in cabling (c) Immune to electrical (d) Glass or plastic fiber (e) Electro-magnetic field	se in analog computing element hould not be distinguished from the distinguished from the true about Optical Fiber Cable of data over long distances at high specially in LAN environment interface preventing cross talks of designed to guide light over its light carrying the signals in the spatished fastest access time?	hybrid systems es?  igh data range like 40GB/s

d) Versatility  th of the following statements is	(e) Automatic	
h of the following statements is		
h of the following statements is		
	are true when replacing a motherboa	ard?
a) The motherhood has to be a	omnotible with the spectrum connect	ad to the cound cound
	•	ed to the sound card.
•	• •	
	-	uh o o u d
		iboard.
The momerodia has to be e	ompatione with the hard disk type.	
ch of the following best descr	ibes a dedicated computer?	
a) Which is used by one pers	on only	
b) Which uses one kind of sy	estem software	
c) Which uses one kind of ap	pplication software	
d) Which is assigned one and	l only one task	
e) Which is assigned to person	on for one and only one task	
, , ,	·	
TRAN programming languag	ge is more suitable for	
a) Business Applications	(b) Marketing	g Applications
c) Scientific Applications	(d) Foregoetin	ng Applications
c) belefitifie Applications	(u) Forecasui	
	b) The CPU has to be compatible. The motherboard has to be conditioned to the power supply has to be conditioned to the motherboard has to be conditioned to the following best described which is used by one personal. Which uses one kind of synchich uses one kind of applications are which is assigned to personal. TRAN programming languages.  TRAN programming languages.	TRAN programming language is more suitable for  a) Business Applications (b) Marketing

(a) Pulse Code Switching	(b) Pulse Code Packing	(c) Pulse Code Modularization
(d) Pulse Code Stretcher	(e) Pulse Code Modulation	
omputer is free from tiresome	e and boredom. We call it	
omputer is free from tiresome	e and boredom. We call it  (b) Reliability	y
-		