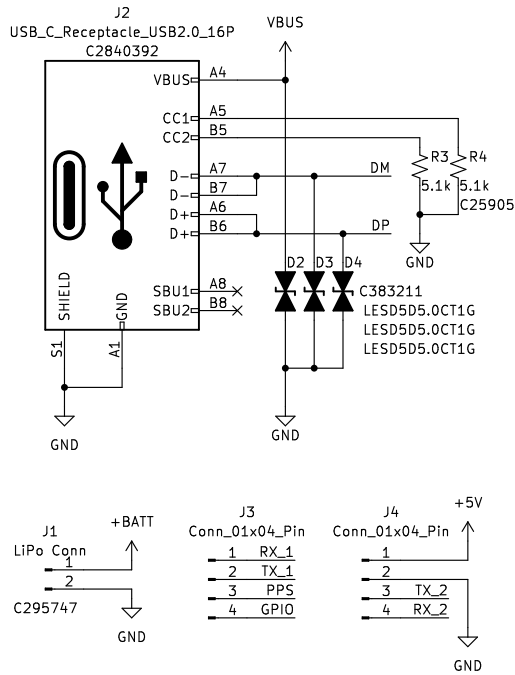
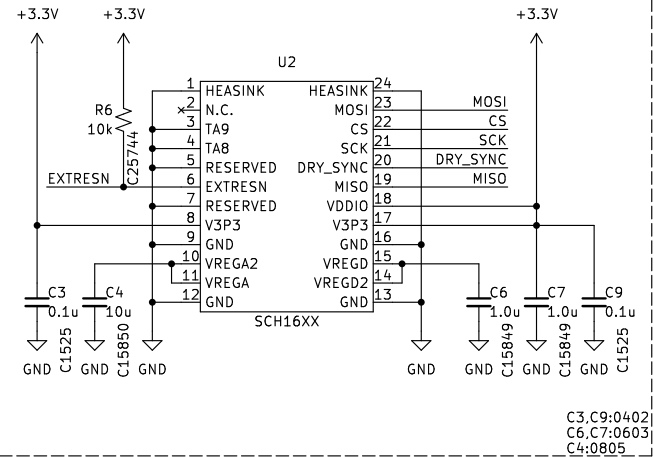


## Connector



## IMU

DRY\_SYNC : SYNC input (active high) DRY (Data Ready) outputs an interrupt signal when the internal output registers (decimated gyroscope and accelerometer) have been updated until the first decimated register is read.  
: SPI device selection Address 0 (static).  
Slave addressing in SafeSPI2.  
Max four slaves can be addresses by TA9:8.  
TA on the slave is defined by VDDIO logic level at pins TA9 and TAB.  
Connect to 0 V 0/PDR1) ground for default '0' address.



## MCU

