

D

C

B

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1

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

D

C

B

A

U1-A	
12C2_SCL/1019	1
12C2_SDA/1020	2
SD_D0/1013	3
SD_D1/SP11_D0/1011	4
SD_D0/SP11_CS1/1010	5
SD_CMD/SP11_CS0/12C1_SDA/109	6
SD_CLK/SP11_CLK/12C1_SCL/108	7
PWR_KEY	8
GND	9
VBAT	10
VBAT	11
SPK_P	12
SPK_N	13
	14

A9G-LS1

U1-D	
PADGND	42
PADGND	43
SIM_RS1	44
VDD	45
GND	46
GSM_RP	47
GND	48
UART2_RTS/12C3_SDA/107	49
UART2_CTS/12C3_SDA/106	50
UART2_RXD/104	51
UART2_TXD/105	52
UART1_RTS/103	53
UART1_CTS/102	54
UART1_RXD/100	55
UART1_TXD/101	56

A9G-LS1

U1-B	
ADCO	15
ADCI	16
VBUS_DET	17
RESET	18
LCD_CS/12S_LRCK/1015	19
LCD_BSTB/12S_BCK/1014	20
LCD_D10/12S_D0/1017	21
LCD_DC/1018	22
LCD_SCK/12S_D1/1016	23
1029	24
1026	25
1027	26
GND	27

A9G-LS1

U1-C	
GPS_BP	28
GND	29
1028	30
1030	31
1025	32
MIC_N	33
MIC_P	34
USB_N	35
USB_P	36
BST_TXD	37
BST_RXD	38
SIM_D10	39
VSIM	40
SIM_CLK	41

A9G-LS1

COMPANY:		www.mirocast.com	
TITLE: A9G/A9 LogicSymbol			
CODE:	SIZE: B	DRAWING NO:	REV: V1.0
SCALE:		SHEET: 1 OF 1	

DRAWN:	Charlin	DATED:	2019/08/21
CHECKED:		DATED:	
QUALITY CONTROL:		DATED:	
RELEASED:		DATED:	