CACHE ATTACKS AND THEIR APPLICATIONS

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Introduction

The classical way of doing things

Step 1: somehow convince a target to visit the attacker's malicious website.

Step 2: involves some sort of browser vulnerability to exploit, one way or another.

What we want instead

Same benefits as with the older way

Independence from software bugs

Longevity of the exploit

The newer way of doing things:

Same benefits when it comes to spying

But it's not a flaw, it's hardware design!

Meet cache attacks

 Step 1: still convincing the target to visit the attacker's malicious website

Step 2: monitoring the CPU cache to snoop on the target

One step beyond: covert channels

Covert channels are simply unauthorized and unexpected channels of communication.

Covert channels based on cache attacks can be made cross-vm, thus even defeating virtual machines' seemingly perfect isolation.

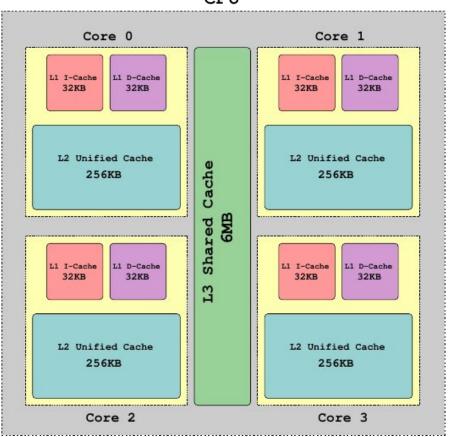


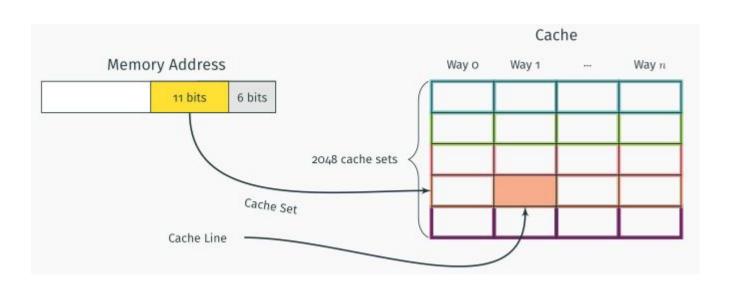
Cache attacks are a type of side-channel attacks.

A side-channel is basically any unaccounted for or disregarded leak of information.

Cache attacks obviously involve memory caches, or more precisely, monitoring memory caches

CPU





Summary

1. Cache-attacks: a JavaScript example

2. Engineering alternative timing sources

3. Errors in side-channels and how to fix them

1. Cache-attacks: a JavaScript example

Motivations

- Scalability.
- No physical access needed.
- Allows for profiling of basic users.
 - On social media.
 - On banking websites.

Prime+Probe

- Makes use of access time difference.
- Applied to one cache set.
- Works across CPU cores since the LLC is shared.

Prime+Probe

- Creating an eviction set for one or more relevant cache sets.
- Priming (filling) the cache set.
- Triggering the victim's operation.
- Probing the cache set again.

Creating an eviction set

- Variables mapped by the CPU to a set used by the victim.
- Fix an arbitrary address and brute force.
 - Optimisation 1: shrink the set by randomly removing elements.
 - Doptimisation 2: if physical addresses P1and P2 share a cache set, then for any value of Δ, P1⊕Δ and P2⊕Δ also share a cache set.

Priming and probing

- Replace each entry in the CPU cache with the eviction set.
- Measure Probe time precisely.
- Linked-list (ensures access before measurement).
- Randomly permuting elements (stride prefetching).
- Access from alternating directions (avoid too many cache misses).

Identifying interesting cache regions

- Correlate cache sets to code or data belonging to the victim.
- Machine learning
 - Derive meaning from cache set latency measurements.
 - Incentivize the victim to perform an action.

2. Timers and how to find them

Timers and how to find them

- W3C and browsers vendors eliminated fine-grained timers from JavaScript.
 - Wrong solution.
 - Other ways of finding / creating timers.

Recovering a high-resolution timer

- Observing clock edges.
 - Clock edges: time at which the timestamp is an exact multiple of its resolution.
 - Increment a counter between clock edges to get an higher resolution.
 - Attacks:
 - Clock interpolation.
 - Edge thresholding.

Clock interpolation

- Usage:
 - Busy wait for a clock edge.
 - Start the operation to time.
 - Busy wait for the next clock edge, incrementing the counter.
- Results: from a 100ms timer to a 15µs timer.

Alternative timing primitives

- Timeouts:
 - Use the timer of the browser with the setTimeout function.
 - Concurrent timer-based callback simulate a counting thread by incrementing a variable.
 - Microsoft browsers have another function called setImmediate that allows a resolution of up to 50µs.

Alternative timing primitives

- Other methods:
 - Message passing.
 - Message channel.
 - CSS animations.
 - SharedArrayBuffer.

Results of different timing primitives

	Free- running	A TOO TO ST	Grone s	Edge 38	70. 6.0.4	walk of
performance.now	1	$5\mathrm{\mu s}$	$5\mu\mathrm{s}$	$1\mathrm{\mu s}$	$100\mathrm{ms}$	$100\mathrm{ms}$
CSS animations setTimeout setImmediate postMessage Sub worker Broadcast Channel MessageChannel MessageChannel (W) SharedArrayBuffer		16 ms 4 ms - 45 μs 20 μs 145 μs 12 μs 75 μs 2 ns ³	$16 \mathrm{ms}$ $4 \mathrm{ms}$ $ 35 \mathrm{\mu s}$ $-^2$ $ 55 \mathrm{\mu s}$ $100 \mathrm{\mu s}$ $15 \mathrm{ns}^4$	16 ms 2 ms 50 μs 40 μs 50 μs - 20 μs 20 μs	16 ms 4 ms - 40 μs 15 μs 55 μs 20 μs 30 μs	125 ms 100 ms - 47 ms - 760 µs 45 ms 1120 µs 2 ns ³
Interpolation ¹ Edge thresholding ¹		$500\mathrm{ns}$ $2\mathrm{ns}$	$500\mathrm{ns}$ $15\mathrm{ns}$	$350\mathrm{ns}$ $10\mathrm{ns}$	15 μs 2 ns	6—1 1—1

3. Errors in side-channels and how to fix them

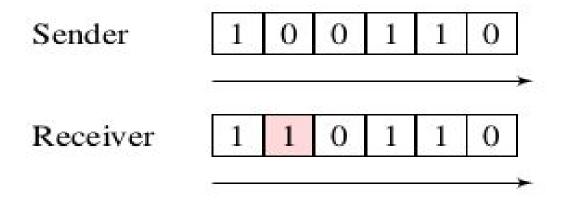
Errors in side-channels

Noise

Descheduled sender/receiver

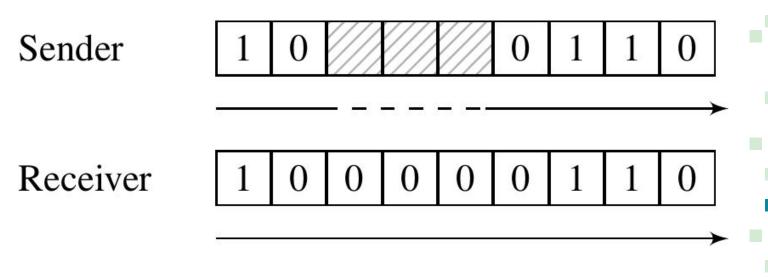
Errors in side-channels

Noise cause a substitution errors.



Errors in side-channel

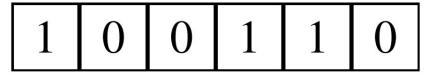
Sender descheduled create insertion error.



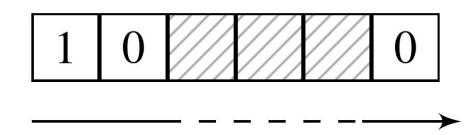
Errors in side-channel

Receiver descheduled create deletion error.

Sender



Receiver

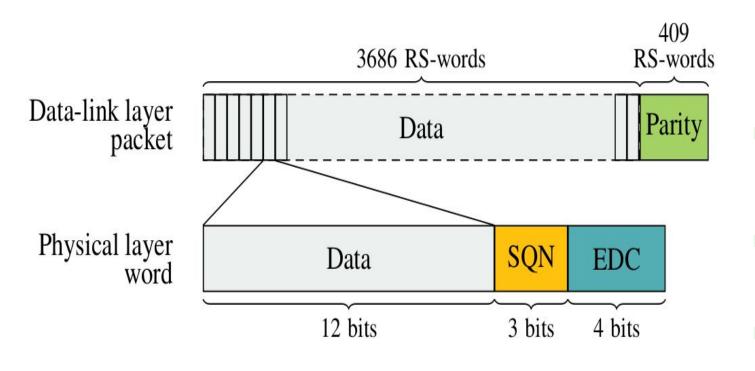


Robust covert-channels

Communication protocol.

- Physical layer.
- Data-link layer.

Robust cache covert-channels

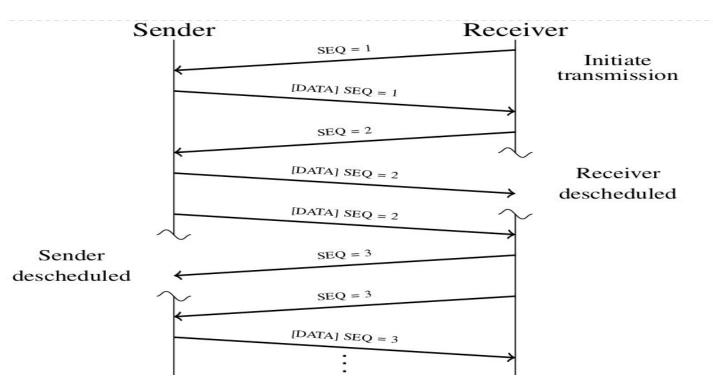


Robust cache covert-channels

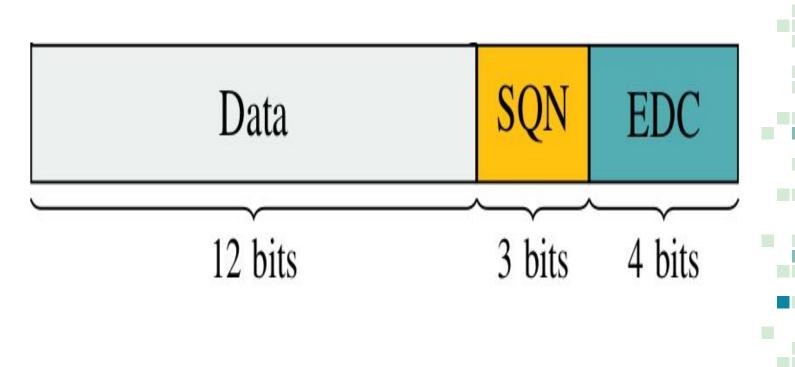
Substitution errors apply

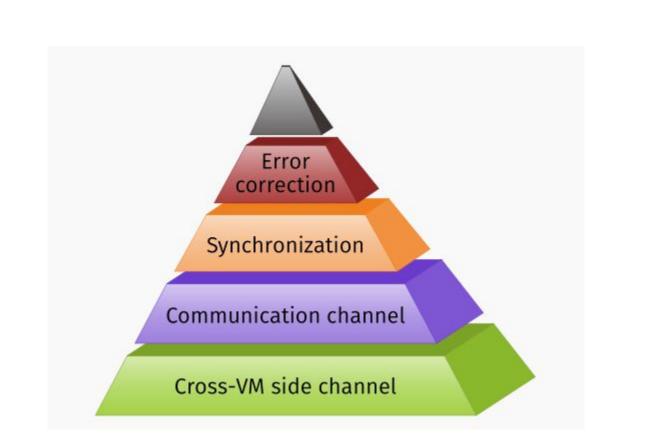
Encoded SQN

Deletion errors

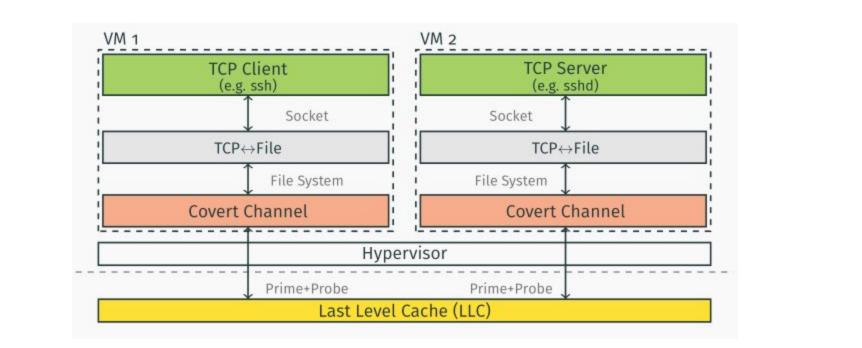


Insertion error









Conclusion

- Cache attacks are practical
- Cache-based covert-channels are also practical
- The covert channel can be made noise-free, thus reliable
- Noise is no protection against cache attacks
- Removing timer APIs is also no protection against cache attacks
- Promising attacks that may develop and yield creative stuff in the future

Sources

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Questions?