# Image processing applications are widely used nowadays which need to analyze and process a large number of images and video streams in real-time. This real-time constraint can be handled if the live camera feed is preprocessed right at the edge device i.e. an FPGA. The FPGA coupled with an image sensor is used to develop a camera that is adaptive and can preprocess the incoming live camera feed using high-level synthesis. Thus, this approach proposes to define a Software-Defined Camera that utilizes the software-hardware codesign functionality of the Zynq-7000 FPGA.

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