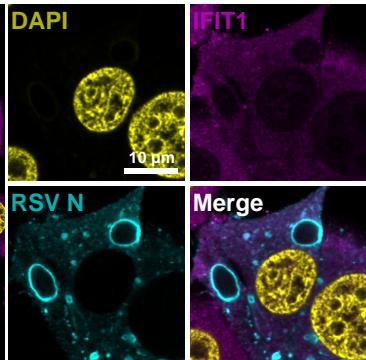
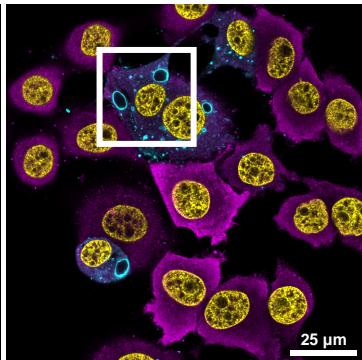
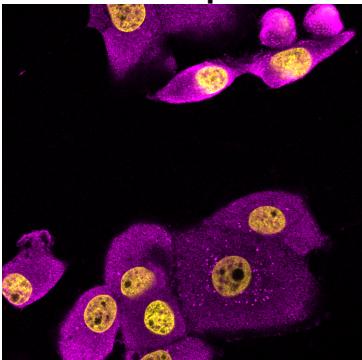
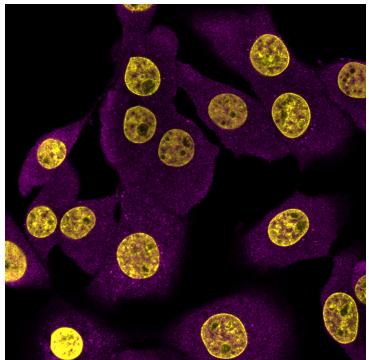


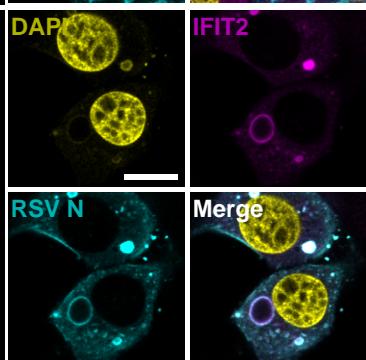
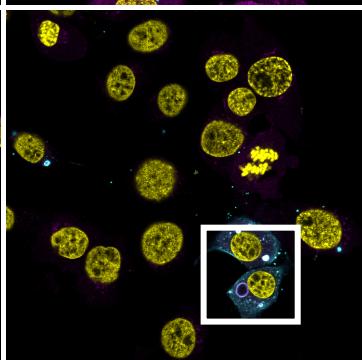
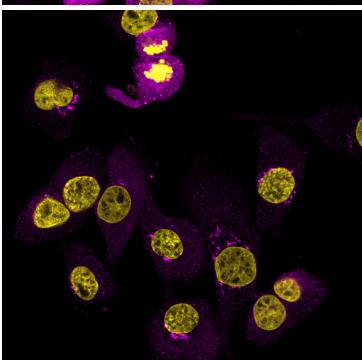
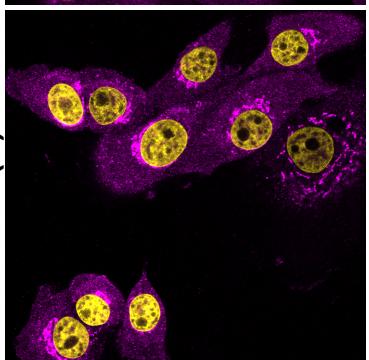
Mock**bIFN alpha****bRSV**

IFIT1



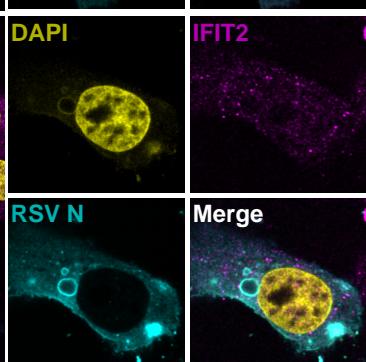
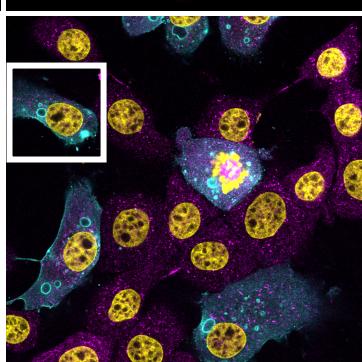
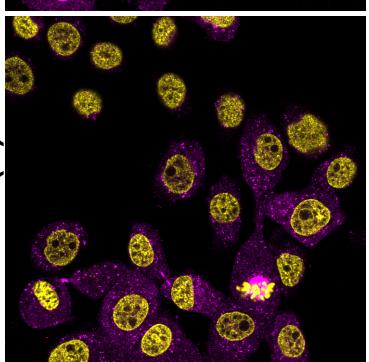
IFIT1

IFIT2(A)



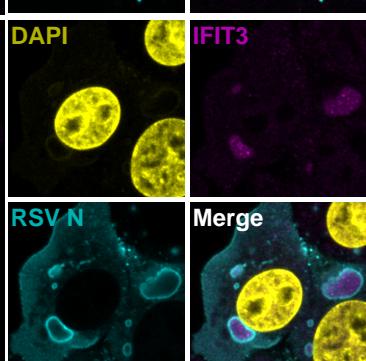
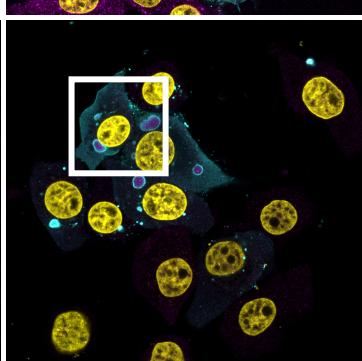
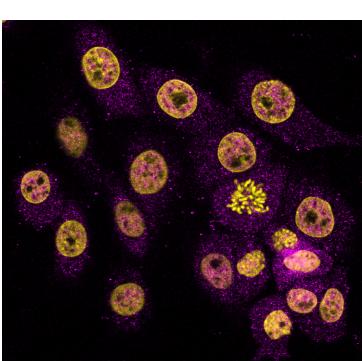
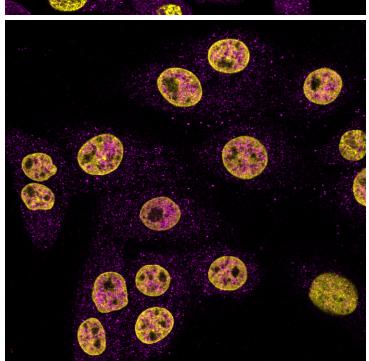
IFIT2

IFIT2(B)



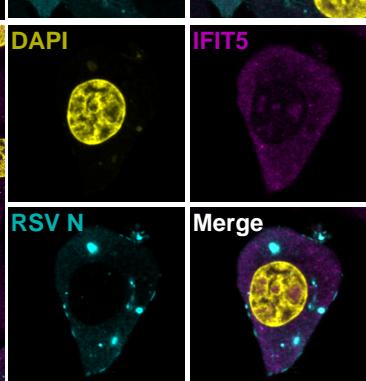
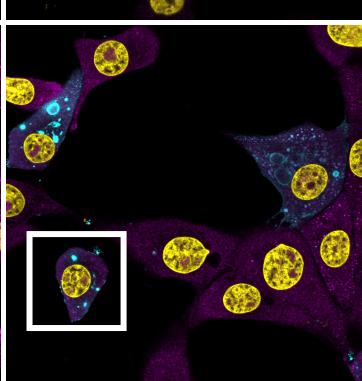
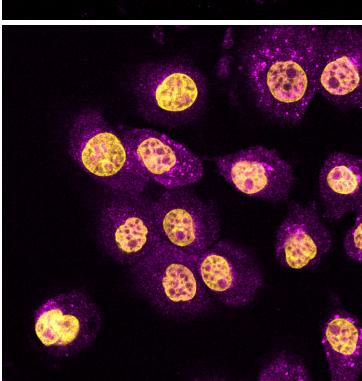
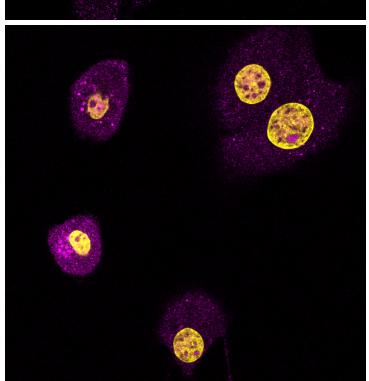
IFIT2

IFIT3



IFIT3

IFIT5



IFIT5