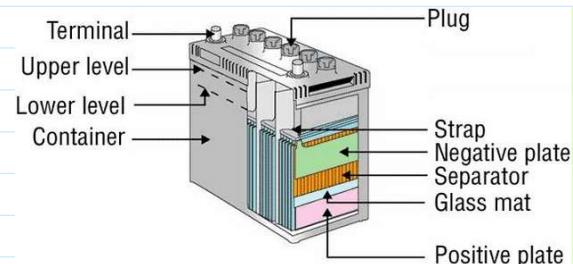
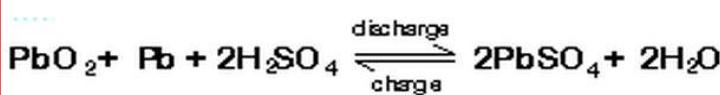


Lab3: Design a PWM charger circuit for a 12V, 7 Ah lead acid battery.

Valve Regulated Lead Acid (VRLA) battery



These are general voltage ranges for six-cell (12V) lead-acid batteries:

- Open-circuit (quiescent) at full charge: 12.6 V to 12.8 V (2.10-2.13V per cell)
- Open-circuit at full discharge: 11.8 V to 12.0 V
- Loaded at full discharge: 10.5 V. ←
- Continuous-preservation (float) charging: 13.4 V for gelled electrolyte; (13.5 V for AGM (absorbed glass mat) and 13.8 V for flooded cells)

Battery for this Lab:

(Some batteries in the lab are from B.B.)



NON-SPILLABLE SEALED RECHARGEABLE BATTERY
TY-12-7.5



★ SPECIFICATION		★ CHARACTERISTICS	
● NOMINAL VOLTAGE	: 12 V	● CAPACITY 25 °C / 77 °F	
● NOMINAL CAPACITY(20 hrs)	: 7.5 Ah	20 hr @ 0.375 A	: 7.5 Ah
● DIMENSIONS		10 hr @ 0.675 A	: 6.75 Ah
TOTAL HEIGHT	: 100 mm (3.94 inches)	5 hr @ 1.2 A	: 6 Ah
CONTAINER HEIGHT	: 94 mm (3.70 inches)	1 hr @ 4.5 A	: 4.5 Ah
LENGTH	: 151 mm (5.95 inches)	1 C @ 7.5 A	: 3.75 Ah
WIDTH	: 65 mm (2.56 inches)	● INTERNAL RESISTANCE (25°C , 77°F) : 25 mΩ	
WEIGHT	: APPROX 2.56 kg (5.64 lbs)	● CHARGING VOLTAGE (25°C , 77°F)	
● MAX DISCHARGE CURRENT : 75 A (5 sec)		STANDBY USE	: 2.275 ± 0.025 V/CELL
● MAX SHORT-DURATION DISCHARGE CURRENT : 225 A (0.1 sec)			: (-3.3mV / °C / CELL)
● STANDARD TERMINALS	: FO-02/FO-03	CYCLE USE	: 2.45 ± 0.05 V/CELL
● CONTAINER MATERIAL	: GENERAL GRADE ABS	● MAX CHARGING CURRENT	: (-5 mV / °C / CELL)

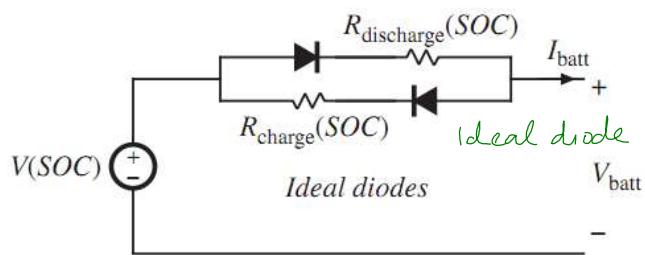
*Faster discharge
less output energy
(losses, heat)*

$$2.5V/\text{cell} \times 6 = 15V$$

$V_{in} \approx 15V$

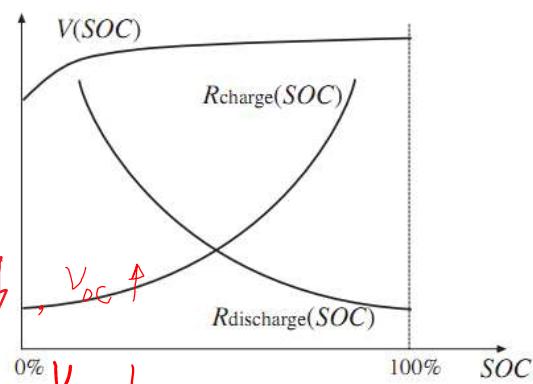
Lead acid battery model

Basic model

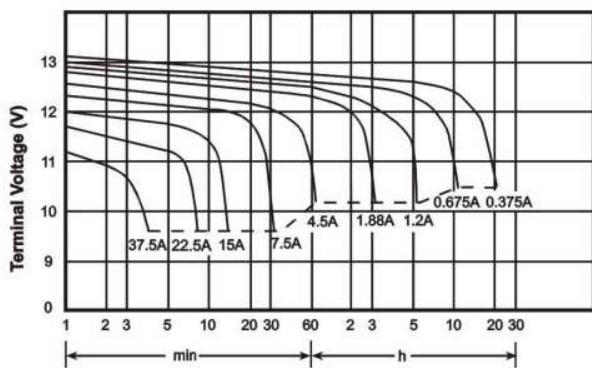


$SOC \uparrow, R_{charge} \uparrow, R_{discharge} \downarrow$
 $SOC \downarrow, R_{charge} \downarrow, R_{discharge} \uparrow, V_{oc} \downarrow$

Dependence of model parameters on battery state of charge (SOC)

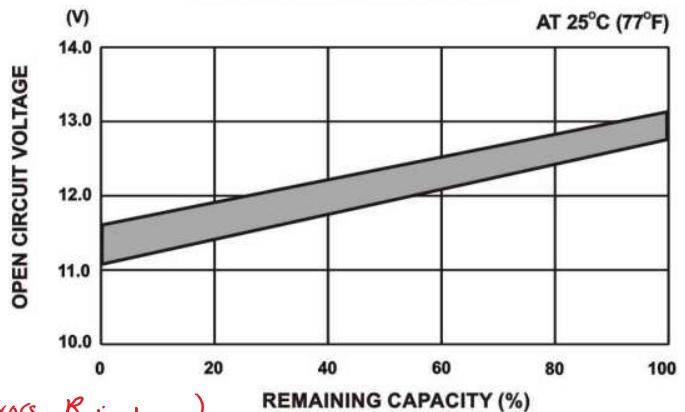


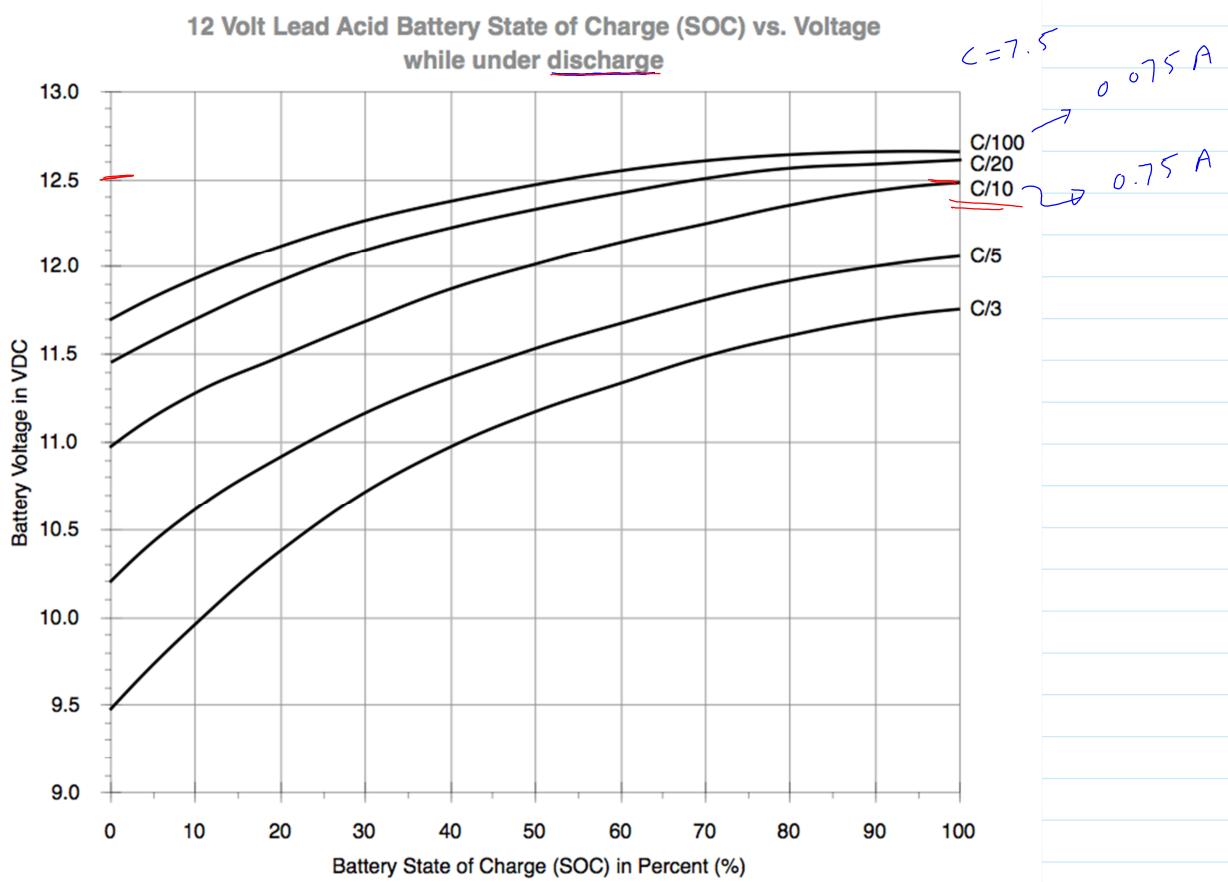
Discharge Characteristics 25 °C (77 °F)



Faster discharge less voltage (drop across $R_{discharge}$)

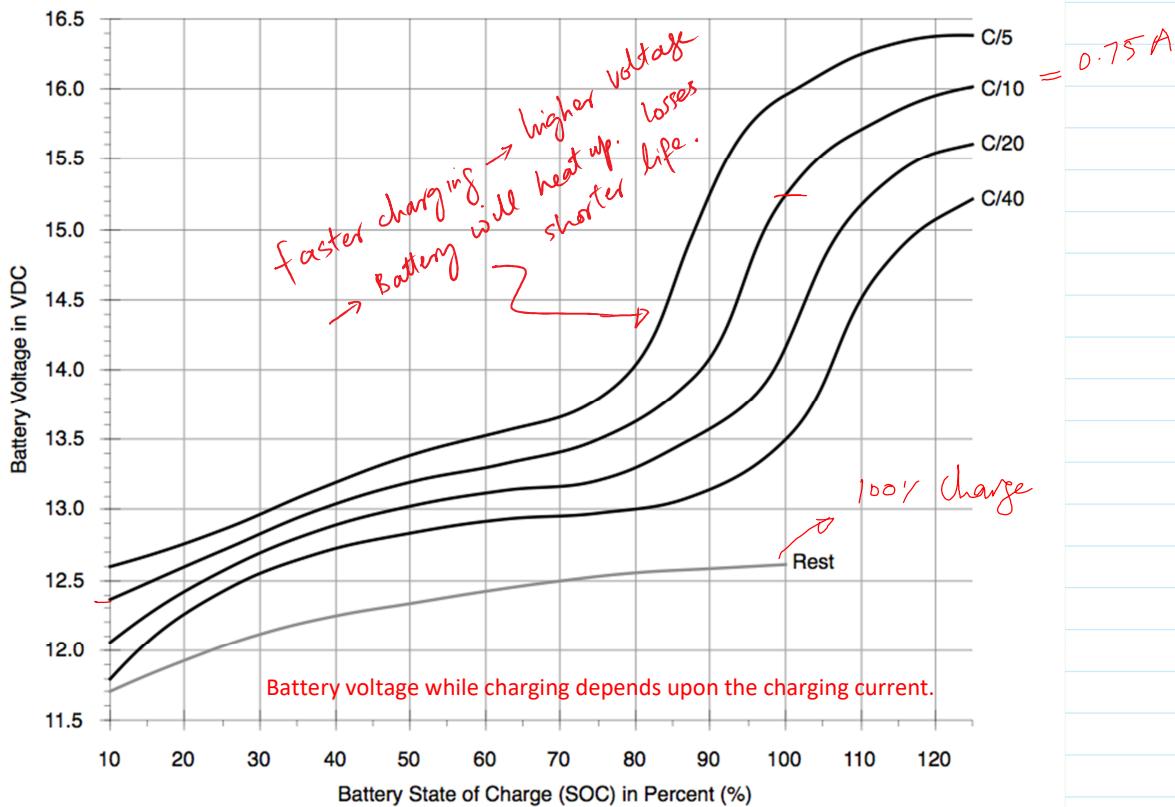
OPEN CIRCUIT VOLTAGE VS REMAINING CAPACITY



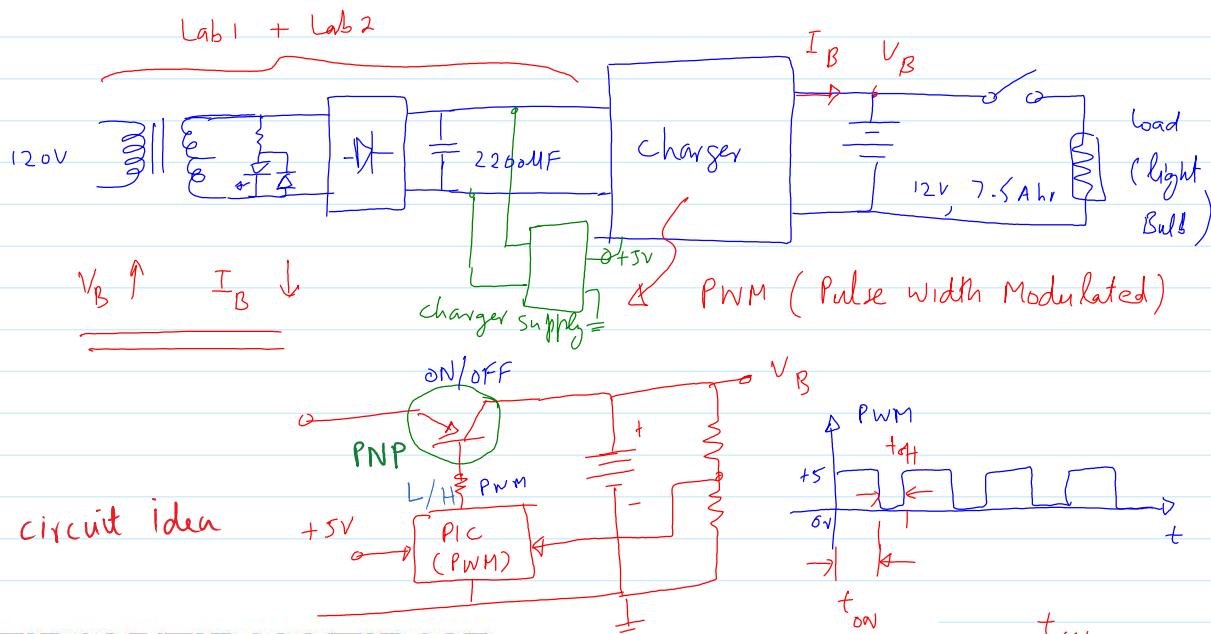


cycle use $V_{charge} = 2.5V/\text{cell} \Rightarrow 2.5 \times 6 = 15V$ (Maximum)

12 Volt Lead Acid Battery State of Charge (SOC) vs. Voltage while battery is under charge



charger Block diagram



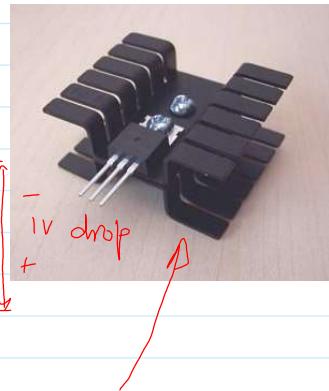
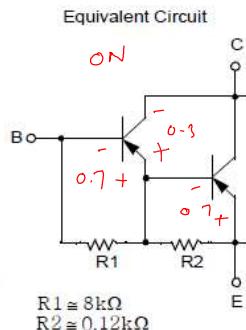
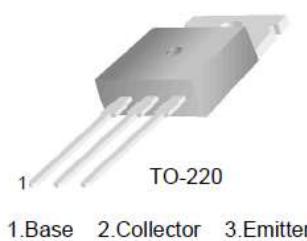
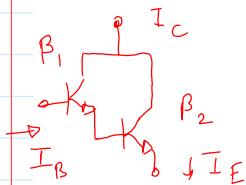
TIP125/TIP126/TIP127 PNP Epitaxial Darlington Transistor

- Medium Power Linear Switching Applications
- Complementary to TIP120/121/122

Darlington, High β

Equivalent Circuit



Darlington, High β 

Mount TIP125

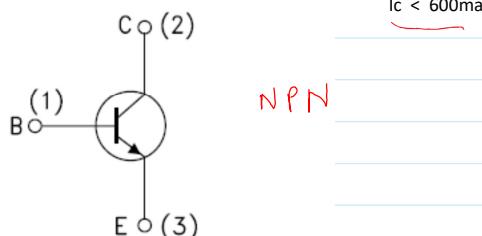
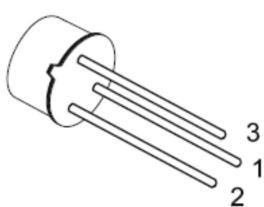
on a heat sink

Electrical Characteristics* $T_a=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
$V_{CEO(\text{sus})}$	Collector-Emitter Sustaining Voltage : TIP125 : TIP126 : TIP127	$I_C = -100\text{mA}, I_B = 0$	-60 -80 -120			V
I_{CEO}	Collector Cut-off Current : TIP125 : TIP126 : TIP127	$V_{CE} = -30\text{V}, I_B = 0$ $V_{CE} = -40\text{V}, I_B = 0$ $V_{CE} = -50\text{V}, I_B = 0$			-2	mA
I_{CBO}	Collector Cut-off Current : TIP125 : TIP126 : TIP127	$V_{CB} = -60\text{V}, I_E = 0$ $V_{CB} = -80\text{V}, I_E = 0$ $V_{CB} = -100\text{V}, I_E = 0$			-1	mA
I_{EBO}	Emitter Cut-off Current	$V_{BE} = -5\text{V}, I_C = 0$			-2	mA
h_{FE}	* DC Current Gain	$V_{CE} = -3\text{V}, I_C = 0.5\text{A}$ $V_{CE} = -3\text{V}, I_C = -3\text{A}$	1000 1000			
$V_{CE(\text{sat})}$	* Collector-Emitter Saturation Voltage	$I_C = -3\text{A}, I_B = -12\text{mA}$ $I_C = -5\text{A}, I_B = -20\text{mA}$			-2	V
$V_{BE(\text{on})}$	* Base-Emitter On Voltage	$V_{CE} = -3\text{V}, I_C = -3\text{A}$			-2.5	V
C_{ob}	Output Capacitance	$V_{CB} = -10\text{V}, I_E = 0, f = 0.1\text{MHz}$			300	pF

2N2222A

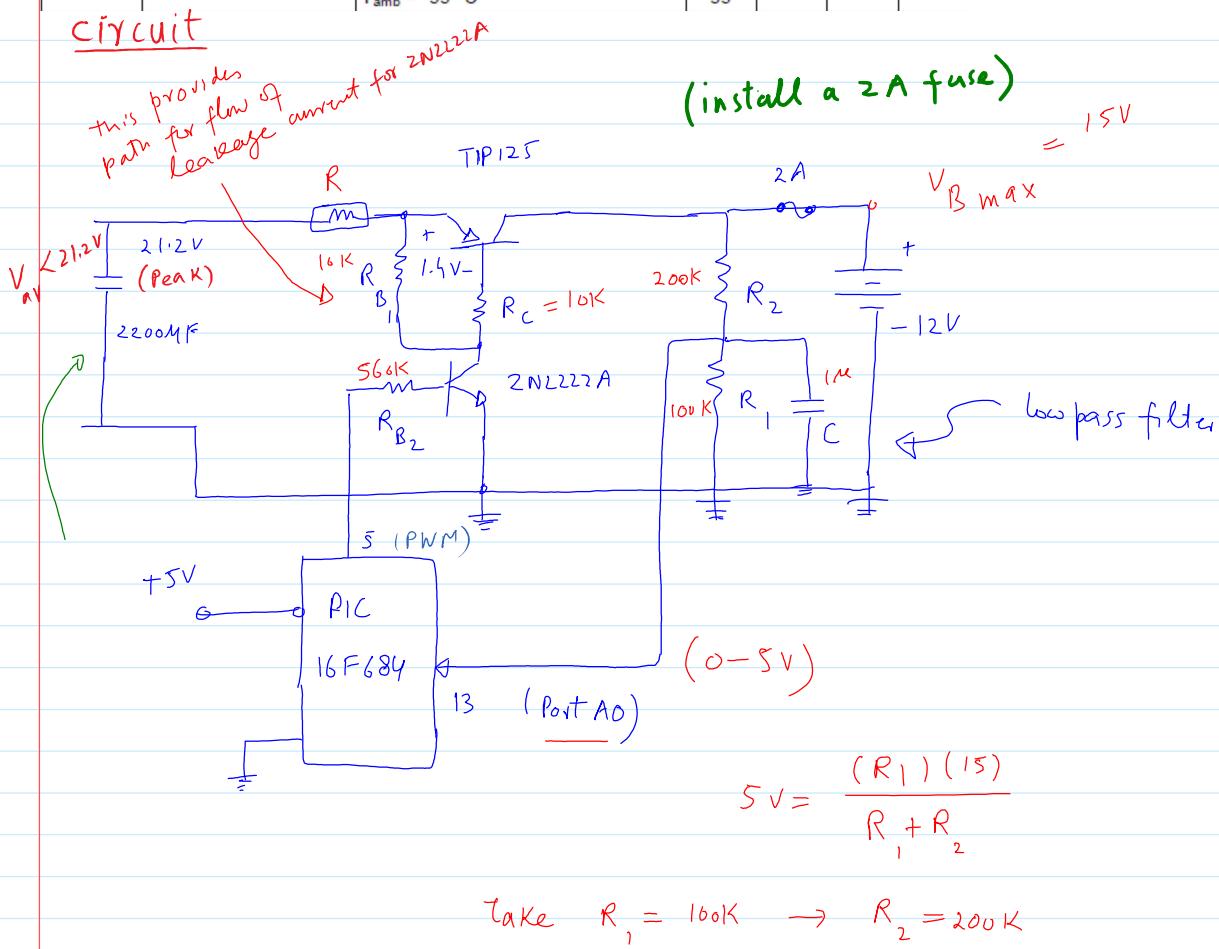
INTERNAL SCHEMATIC DIAGRAM



$V_{(BR)CEO}$	Collector-Base Breakdown Voltage ($I_E = 0$)	$I_C = 10 \mu\text{A}$	75			V
$V_{(BR)CEO^*}$	Collector-Emitter Breakdown Voltage ($I_B = 0$)	$I_C = 10 \text{ mA}$	40			V
$V_{(BR)EBO}$	Emitter-Base Breakdown Voltage ($I_C = 0$)	$I_E = 10 \mu\text{A}$	6			V
$V_{CE(\text{sat})^*}$	Collector-Emitter Saturation Voltage	$I_C = 150 \text{ mA}$ $I_B = 15 \text{ mA}$ $I_C = 500 \text{ mA}$ $I_B = 50 \text{ mA}$		0.3 1		V
$V_{BE(\text{sat})^*}$	Base-Emitter Saturation Voltage	$I_C = 150 \text{ mA}$ $I_B = 15 \text{ mA}$ $I_C = 500 \text{ mA}$ $I_B = 50 \text{ mA}$	0.6	1.2 2		V
h_{FE^*}	DC Current Gain	$I_C = 0.1 \text{ mA}$ $V_{CE} = 10 \text{ V}$ $I_C = 1 \text{ mA}$ $V_{CE} = 10 \text{ V}$ $I_C = 10 \text{ mA}$ $V_{CE} = 10 \text{ V}$ $I_C = 150 \text{ mA}$ $V_{CE} = 10 \text{ V}$ $I_C = 500 \text{ mA}$ $V_{CE} = 10 \text{ V}$ $I_C = 150 \text{ mA}$ $V_{CE} = 1 \text{ V}$ $I_C = 10 \text{ mA}$ $V_{CE} = 10 \text{ V}$	35 50 75 100 40 50	300		

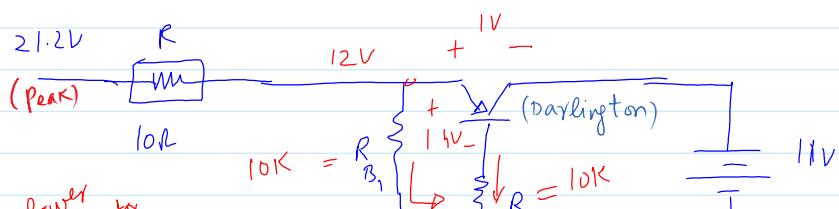
$V_{(BR)CBO}$	Collector-Base Breakdown Voltage ($I_E = 0$)	$I_C = 10 \mu A$	(75)		v
$V_{(BR)CEO}^*$	Collector-Emitter Breakdown Voltage ($I_B = 0$)	$I_C = 10 mA$	40		v
$V_{(BR)EBO}$	Emitter-Base Breakdown Voltage ($I_C = 0$)	$I_E = 10 \mu A$	6		v
$V_{CE(sat)}^*$	Collector-Emitter Saturation Voltage	$I_C = 150 mA$ $I_B = 15 mA$ $I_C = 500 mA$ $I_B = 50 mA$		0.3 1	v v
$V_{BE(sat)}^*$	Base-Emitter Saturation Voltage	$I_C = 150 mA$ $I_B = 15 mA$ $I_C = 500 mA$ $I_B = 50 mA$	0.6	1.2 2	v v
h_{FE}^*	DC Current Gain	$I_C = 0.1 mA$ $V_{CE} = 10 V$ $I_C = 1 mA$ $V_{CE} = 10 V$ $I_C = 10 mA$ $V_{CE} = 10 V$ $I_C = 150 mA$ $V_{CE} = 10 V$ $I_C = 500 mA$ $V_{CE} = 10 V$ $I_C = 150 mA$ $V_{CE} = 1 V$ $I_C = 10 mA$ $V_{CE} = 10 V$	35 50 75 100 40 50 35	300	

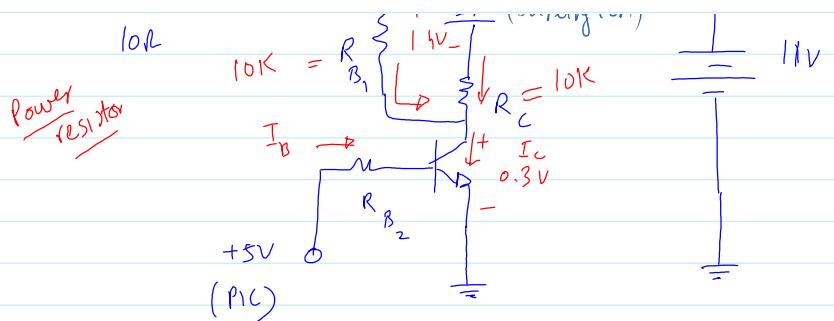
circuit



$$\text{Say } \tau = R_1 C = 0.1 \text{ sec} \Rightarrow R_1 = 100K \Rightarrow C = 1\text{mF}$$

Say Battery is discharged $\rightarrow V_B = 11V \rightarrow$ connected to the charger





$$I_C = \frac{12V - 1.4 - 0.3V}{R_C} + \frac{12V - 0.3}{R_{B_1}} = \frac{10.3}{10k} + \frac{11.7}{10k}$$

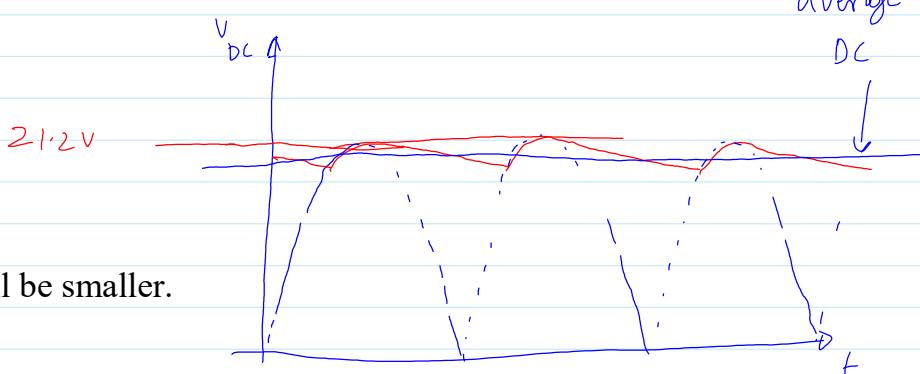
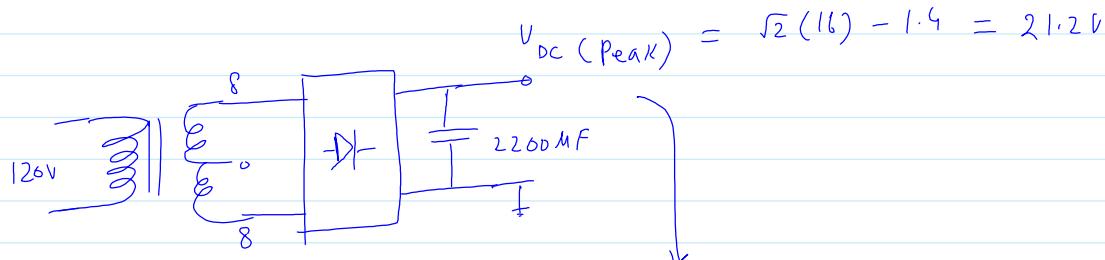
$$I_C = 1.03 \text{ mA} + 1.17 \text{ mA} = 2.2 \text{ mA}$$

I_B darlington = 1.03 mA , $\beta = 1000$, $I_C = 1.03 \text{ A}$ (max) (Actual I_C may be smaller)

$$I_B = \frac{I_C}{\beta} = \frac{2.2 \text{ mA}}{200} = 11 \text{ mA}$$

10VA transformer
 $V = 12 \text{ V}$ $I = \frac{10}{12} = 0.83 \text{ A}$

$$R_{B_2} = \frac{5 - 0.7}{11 \text{ mA}} = 390 \text{ k}\Omega$$



Average voltage will be smaller.

$$12 \text{ V battery, } 10 \text{ VA transformer} \rightarrow I_{DC} = \frac{10}{12} = 0.833 \text{ A}$$

Say $I_{DC} (\text{max}) \approx \frac{1}{2} \text{ A}$

... the model is ... $21.2 \text{ V} - 12 \text{ V} = 9.2 \text{ V}$ (Actual drop)

① $V_{DC}(\max) =$

Maximum drop across the resistor $R = 21.2V - 12V = 9.2V$ (Actual drop will be smaller)



$$R = \frac{9.2V}{1A} = 9.2\Omega \quad \text{? } 10\Omega \quad ?$$

A PWM output Example for PIC16F684 (It will not work as such)

program PWM_rc2_an0

dim dc1 as byte

main:

TRISA=\$01

cmccon0=0 'comparators off

ANSEL=\$01

ADCON0=\$80 'right justified

ADCON1=\$70 'set clock frequency

TRISC = 0

PORTE=\$FF 'initial value

PWM1_Init(1000) ' initializes PWM module, freq = 1kHz.

PWM1_Start ' starts PWM

While true

dc1=word(ADC_read(0) >> 2) 'read adc and kick two LSBs out

Delay_ms(100) 'change duty every 100ms

PWM1_Set_Duty(dc1) ' changes duty ratio

wend

end.

$$V_{DC} = 0.9 V_{AC} - 1.4V$$

$$V_{DC} = 0.9 \times 16 - 1.4 = 13V$$

Drop across the resistor = 1V

$$R = \frac{1V}{0.833A} = 1.2\Omega$$

(Resistor could be removed or reduced)

Program PIC16F684
using PICKit1

$(255 - K \cdot dc1) ?? \quad V_B \uparrow, \text{ PWM duty ratio} \downarrow$

$11V < V_B < 15V, 100\% (255) < \text{PWM duty} < 0\% (0), \text{ voltage divider is } 1/3$

$$V_B = 11V \rightarrow V_{ADC} = \frac{11}{3} = 3.66V \quad , \quad V_B = 15V \rightarrow V_{ADC} = \frac{15}{3} = 5V$$

Requirement: $3.66V \rightarrow dc1 = 255 \quad \text{and} \quad 5V \rightarrow dc1 = 0$ (Proportional controller)

$$\frac{dc1 - 255}{0 - 255} = \frac{V_{ADC} - 3.66}{5 - 3.66} \Rightarrow dc1 = 255 - \frac{255}{1.34} (V_{ADC} - 3.66)$$

where $dc1 = 951.5 - 190.3 V_{ADC}$ $\Rightarrow V_{ADC} = \frac{5 (\text{data1})}{1023}$

\Rightarrow Write your own code in MikroBasic. use float variables.

Testing:

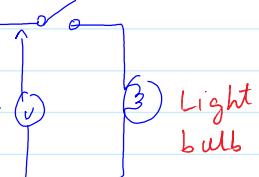
#1 Plot Battery discharge curve

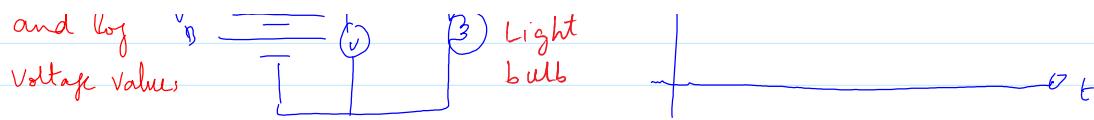
charge battery to 13.4V using a power supply with current set to less than 1A.

then discharge

and log V_B

Voltage values

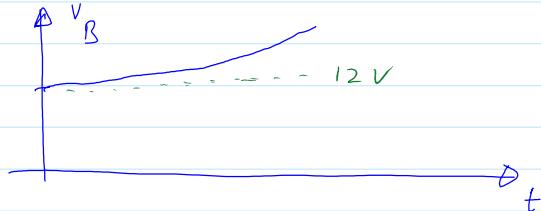




Record v_B when battery is being discharged.

#2 Plot battery charging curve

Connect your charger circuit to the battery and record v_B as the battery is being charged



#3 Record waveforms in your circuit while battery is charging.

→ rectifier output, PIC out, v_B , Darlington v_B

#4 Find PWM charger η

$$\eta = \frac{V_o I_o}{V_{in} I_{in}} \text{ or } \eta = \frac{\text{dc}}{\text{ac}}$$

(it may be a low number)

#5 Find charger η without resistor R.

Submit report on the course website in pdf format