Technical Design Report

Parallax

A simple data acquisition system

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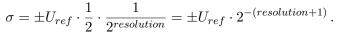
1 Hardware Design

1.1 Introduction

This chapter explains the hardware details of the measurement system, aiming to provide a clear understanding of why and how the hardware was designed. In principle the purpose of the measurement hardware is to map analog voltages from a wide input range to a smaller input range, which then can be measured using the PR2040 Microcontroller of the Raspberry Pi Pico Microcontroller board.

1.1.1 Measuring analog voltages

The core component of the data acquisition is the PR2040 Microcontroller which includes built-in Analog-to-Digital Converters(ADC). These ADCs convert electronic voltages ranging from Ground (0 V) to a reference voltage U_{ref} into a digital number defined by the resolution which is depicted in bits. The resolution for all the ADCs in the RP2040 IC is set to 12 bits, meaning that the digital result from an ADC is in between 0 and $2^{12} = 4096$. Within this conversion the mapping from the analog input voltage to a corresponding digital number should be proportional but has small uncertainties which are defined by the resolution as seen in Fig. 1.1. This picture also clearly shows that a value acquired by an ADC has an uncertainty of half the voltage to which the least significant bit (LSB) corresponds. This can be calculated as:



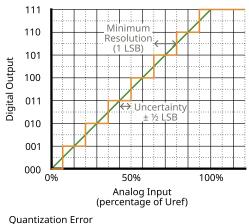




Figure 1.1: Conversion from an analog voltage do a digital number with an ADC

In reality there are multiple reasons for small short time fluctuations in the voltage which are greater than the quantization uncertainty of an ADC. Therefore the propper uncertainty of an

ADC in an electronic application can be measured with a calibration. The data sheet explains that four ADC channels (ADC0 to ADC3) are connected to GPIO Pins and the fifth ADC channel (ADC4) is connected to an internal temperature sensor. Notably the fourth channel ADC3 can be used to measure the VSYS voltage of the Raspberry Pi Pico board. Each channel is linked to a single Analog-to-Digital Converter (ADC) through an internal switch, allowing the selection of one channel at a time. The shared ADC operates with a maximum sampling rate of 500 kilosamples per second (KSps). This sampling rate can be used for measuring voltages on a single channel at the full sampling speed (simplex). Alternatively, it can simultaneously measure two channels by periodically switching between them, effectively dividing the sampling rate to 250 KSps per channel. The accuracy and the sampling rate are sufficient to use this as the core of a simple oscilloscope which acquires periodically ADC values and streams the data to another device for further processing.

An ADC can only measure voltages between 0 V and U_{ref} but we want to build an oscilloscope that can measure positive and negative voltages $\pm U_{max}$ within a range of p.ex. $U_{max} > 10 \text{ V}$. It is the task of an analog front-end to map voltages from $-U_{max}$ to $+U_{max}$ into an analog voltage which is suitable for the ADC. Furthermore the range of the input voltage should be selectable and there should be some kind of over-voltage protection on the signal inputs. This allows to measure for higher voltages with less precision or lower voltages with higher precision.

1.1.2 Prerequisites

In order to discuss the structure of the analog front-end and the corresponding voltage ranges for the oscilloscope some basic knowledge in electronics engineering is assumed. This contains the following topics:

- The definition of Charge, Voltage and Current
- The definition of the electrical resistance, Ohms law and resistors as electrical components
- Capacitance and capacitors as electrical components
- The usage of a Diode and a Z-Diode
- A mathematical understanding of Reactance and Impedance, this will also be explained briefly when it comes up

1.2 Voltage Divider

One of them most important circuits in electronics is the voltage divider.