**Configurare registre MIPS32 Pipeline – varianta 2**

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| --- | --- | --- | --- |
| **IF/ID** | **ID/EX** | **EX/MEM** | **MEM/WB** |
| Instruction\_IF\_ID (32) | PC\_ID\_EX (32) | BranchAddress\_EX\_MEM (32) | MemData\_MEM\_WB (32) |
| PC\_IF\_ID (32) | RD1\_ID\_EX (32) | Zero\_EX\_MEM (1) | ALURes\_MEM\_WB (32) |
|  | RD2\_ID\_EX (32) | Gtz\_EX\_MEM (1) | rd\_MEM\_WB (5) |
|  | Ext\_imm\_ID\_EX (32) | ALURes\_EX\_MEM (32) | MemtoReg\_MEM\_WB (1) |
|  | SA\_ID\_EX (5) | RD2\_EX\_MEM (32) | RegWrite\_MEM\_WB (1) |
|  | Func\_ID\_EX (6) | rd\_EX\_MEM (5) |  |
|  | rt\_ID\_EX (5) | MemtoReg\_EX\_MEM (1) |  |
|  | rd\_ID\_EX (5) | RegWrite\_EX\_MEM (1) |  |
|  | MemtoReg\_ID\_EX (1) | MemWrite\_EX\_MEM (1) |  |
|  | RegWrite\_ID\_EX (1) | Branch\_EX\_MEM (1) |  |
|  | MemWrite\_ID\_EX (1) | Br\_ne\_EX\_MEM (1) |  |
|  | Branch\_ID\_EX (1) | Br\_gtz\_EX\_MEM (1) |  |
|  | Br\_ne\_ID\_EX (1) |  |  |
|  | Br\_gtz\_ID\_EX (1) |  |  |
|  | ALUSrc\_ID\_EX (1) |  |  |
|  | ALUOp\_ID\_EX (2) |  |  |
|  | RegDst\_ID\_EX (1) |  |  |
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