USB 2.0, Type C
One Plug to Rule Them All
Until the USB-IF introduces the Type-D

128K EEPROM

ESP WiFi Receiver Module

Re locate TX RX TP
Redo footprint of 3.3V rail chip
Space compents out; caps near U2/U1
Move reset TP

Remove buttons all together...
Change 15k to 10k
Lower voltage from 5.2 to 5.05
change 36k to 54

Change TLC5929 to TLC5928
Formally change the 2.7K resistors to 3.3

Connect IO1 or reset to PB3 via nmos

Expressif Systems ESP-WROOM-02

change .1 c

µC Power

STM32F405RGT7

STM32F4x5RGTy
Designed for STM32F405RGTy

405: vout1 en to pb2, shift others up

STM32F405RGT7

Abracon ASE-12.000MHZ-LC-T

Abracon ASEK-32.768KHZ-LRT

Green (H) & Blue (M) LED Drivers

TLC5929RGET

USBPD Negotiation Logic
5V, 500mA (Programming); 18W MIN for Normal Operation
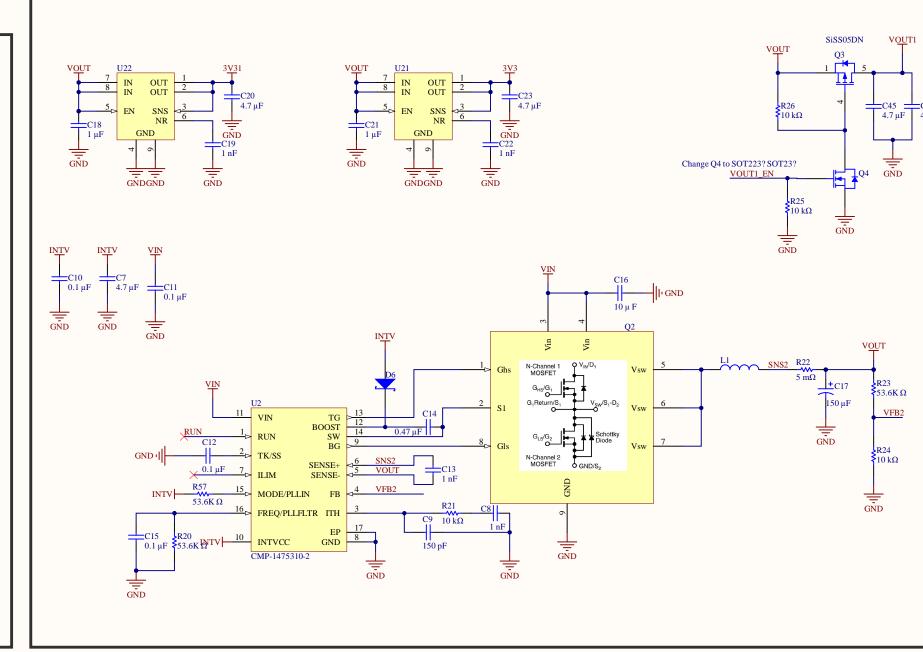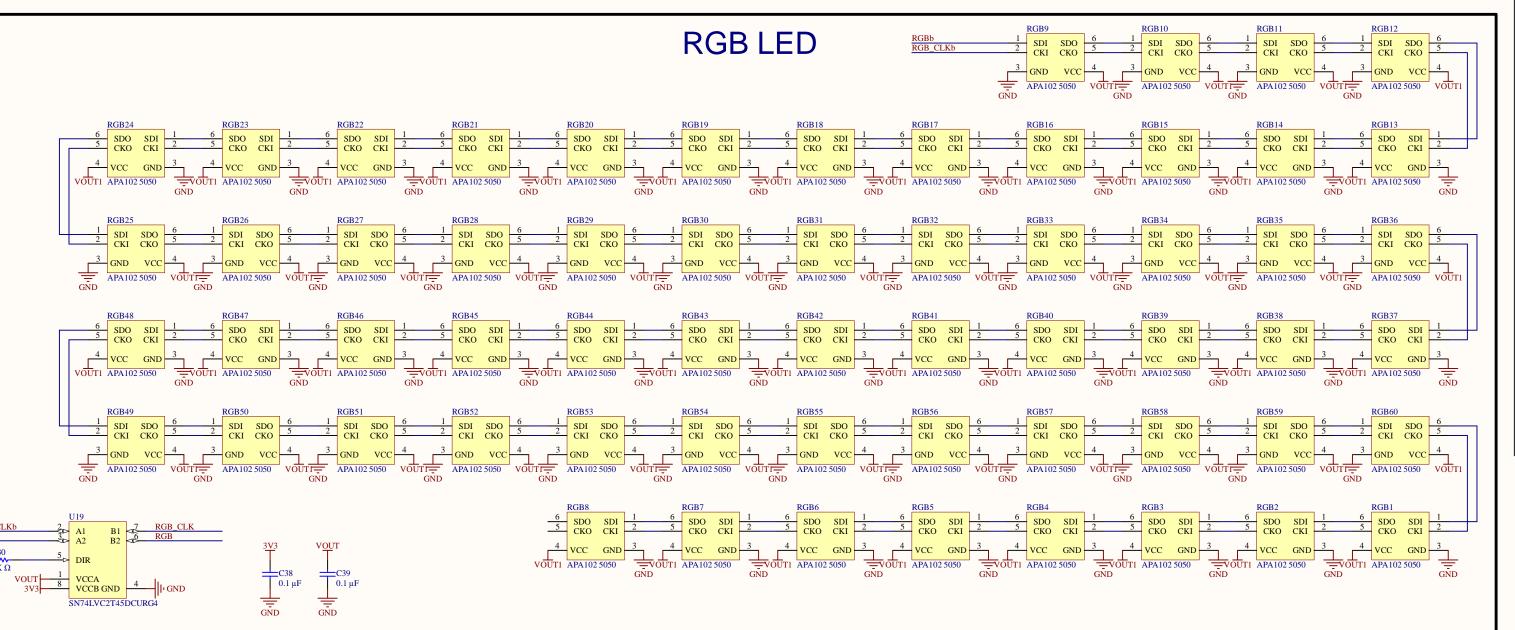### Address: 0x08

CYPD3177-24LQXQ

Pin 5 -> 1A
Pin 6 -> 0.75A
Pin 1 -> 9V
Pin 2 -> 20V

Explore using ST part again

Change Q4 to SOT2237 SOT237

CMP-1475310-2

RGB LED

APA102 5050

SN74LVC2T45DCURG4

Case For and against an int board V6 are...

https://www.onsemi.com/pub/Collateral/AND9135-D.PDF

SiSS05DN

VOUT1_EN

Nidec Copal CL-SB-13A-11T

Nidec Copal CL-SB-13A-11T