UNIVERSITY OF SCIENCE AND TECHNOLOGY OF SOUTHERN PHILIPPINES
C.M. Recto Avenue, Lapasan, Cagayan de Oro City

COLLEGE OF ENGINEERING AND ARCHITECTURE
Department of Computer Engineering
Digital Signal Processing

GUIDELINES FOR PERFORMANCE INNOVATIVE TASK

Middle Term

1. Write a review paper on the current study, methodology considerations, and potential solutions on any of the topics under digital signal processing. The topics you've chosen in DSP must be of use for machine learning (which you'll be implementing for your Final PIT).
2. Read recent journal papers (at least five) published for past five (5 years, 2020-2025) and summarize the findings to produce your own review paper with the following contents:
   - **Introduction** to your chosen specific topic. It can be any subtopic under your chosen topic (*e.g., Cepstral Methods for Feature Reduction: A Literature Review*).
   - **Methodology** of your reviewed journals specifying their process and approach in handling the problem.
   - **Challenges and issues** on implementation of the study. Consider the reason why they conducted their study.
   - **Solution** to the challenges identified in the previous item.
   - **Conclusion**
   - **References** must strictly (follow IEEE formatting).
3. Use IEEE double column conference paper formatting for your paper. Click here for guidelines and downloadable sample .tex format. Use the LaTeXTemplate. **DO NOT** use the Microsoft Word format. For instructions on how to use, click the **LaTeX Template Instructions** in the site.
4. Kindly ensure that you cite the references in your write up. Any figures, equations, and tables must be cited. Your paper must contain a minimum of five (5) pages, excluding the references page.
5. Your paper will be graded based on the rubrics attached in the syllabus.
6. Submit your paper in **.pdf** format through Google Classroom. Only **one** from your group must submit the paper, the other **two** will turn in a blank submission.
7. Submit also your paper in **printed form** with paper clip at the Computer Engineering Department Office.
8. Deadline: **October 15, 2025** at 5:00PM. Late submissions without valid reason and incorrect formatting will **not** be accepted.