RSLogix Micro Project Report



Processor Information

Processor Type: Bul.1763 MicroLogix 1100 Series B

Processor Name: UNTITLED

Total Memory Used: 278 Instruction Words Used - 349 Data Table Words Used

Total Memory Left: 6378 Instruction Words Left

Program Files: 3

Data Files: 10

Program ID: bc27

Bul.1763 MicroLogix 1100 Series B

Channel Configuration

```
CHANNEL 0 (SYSTEM) - Driver: DF1 Full Duplex
  CHANNEL 0 (SYSTEM) - Driver: DF1 Full Duplex Edit Resource/Owner Timeout: 60 CHANNEL 0 (SYSTEM) - Driver: DF1 Full Duplex Passthru Link ID: 1
  CHANNEL 0 (SYSTEM) - Driver: DF1 Full Duplex Write Protected: No
  CHANNEL 0 (SYSTEM) - Driver: DF1 Full Duplex Comms Servicing Selection: Yes
  CHANNEL 0 (SYSTEM) - Driver: DF1 Full Duplex Message Servicing Selection: Yes
  CHANNEL 0 (SYSTEM) - Driver: DF1 Full Duplex 1st AWA Append Character: \d
  CHANNEL 0 (SYSTEM) - Driver: DF1 Full Duplex 2nd AWA Append Character: \a
  Source ID: 1 (decimal)
  Baud: 19200
  Parity: NONE
  Control Line : No Handshaking
  Error Detection: CRC
  Embedded Responses: Auto Detect
  Duplicate Packet Detect: Yes
  ACK Timeout (x20 ms): 50
  NAK Retries: 3
  ENQ Retries: 3
CHANNEL 1 (SYSTEM) - Driver: Ethernet
  CHANNEL 1 (SYSTEM) - Driver: Ethernet Edit Resource/Owner Timeout: 60
  CHANNEL 1 (SYSTEM) - Driver: Ethernet Passthru Link ID: 1
  CHANNEL 1 (SYSTEM) - Driver: Ethernet Write Protected: No
  CHANNEL 1 (SYSTEM) - Driver: Ethernet Comms Servicing Selection: Yes
  CHANNEL 1 (SYSTEM) - Driver: Ethernet Message Servicing Selection: Yes
  Hardware Address: 00:00:00:00:00:00
  IP Address: 0.0.0.0
  Subnet Mask: 0.0.0.0
  Gateway Address: 0.0.0.0
  Msg Connection Timeout (x 1mS):
  Msg Reply Timeout (x mS): 3000
  Inactivity Timeout (x Min): 30
  Bootp Enable: Yes
  Dhcp Enable No
  SNMP Enable: No
  HTTP Enable: Yes
  Auto Negotiate Enable: Yes
  Port Speed Enable: 10/100 Mbps Full Duplex/Half Duplex
  Contact:
```

Location:

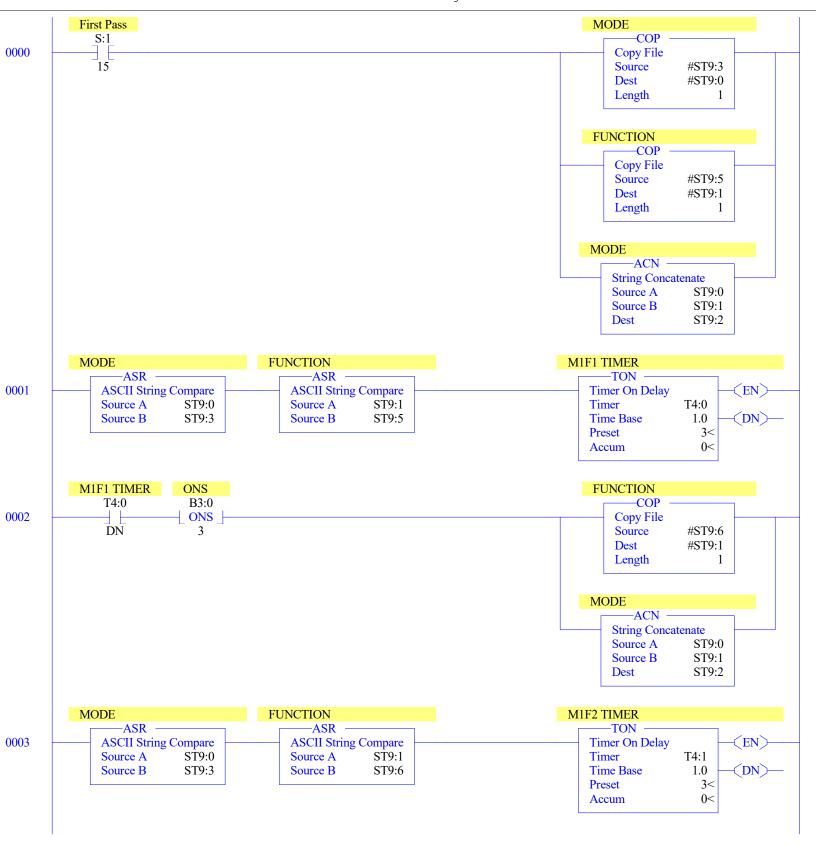
Program File List

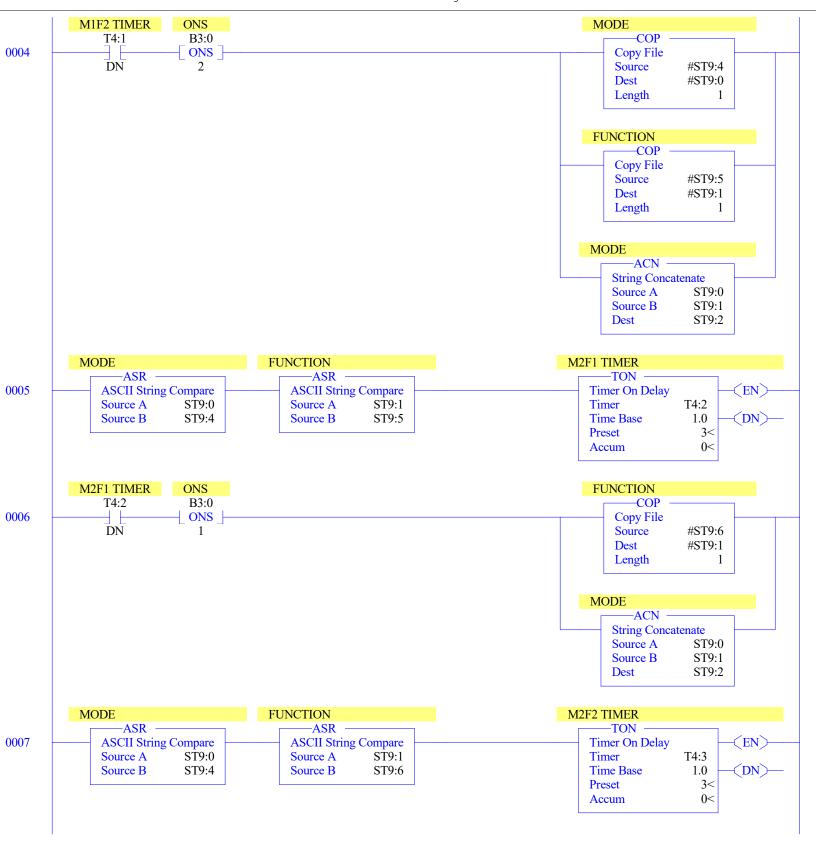
Name	Number	Туре	Rungs	Debug	Bytes
[SYSTEM]	0	SYS	0	No	0
-	1	SYS	0	No	0
MAIN	2	LADDER	14	No	715

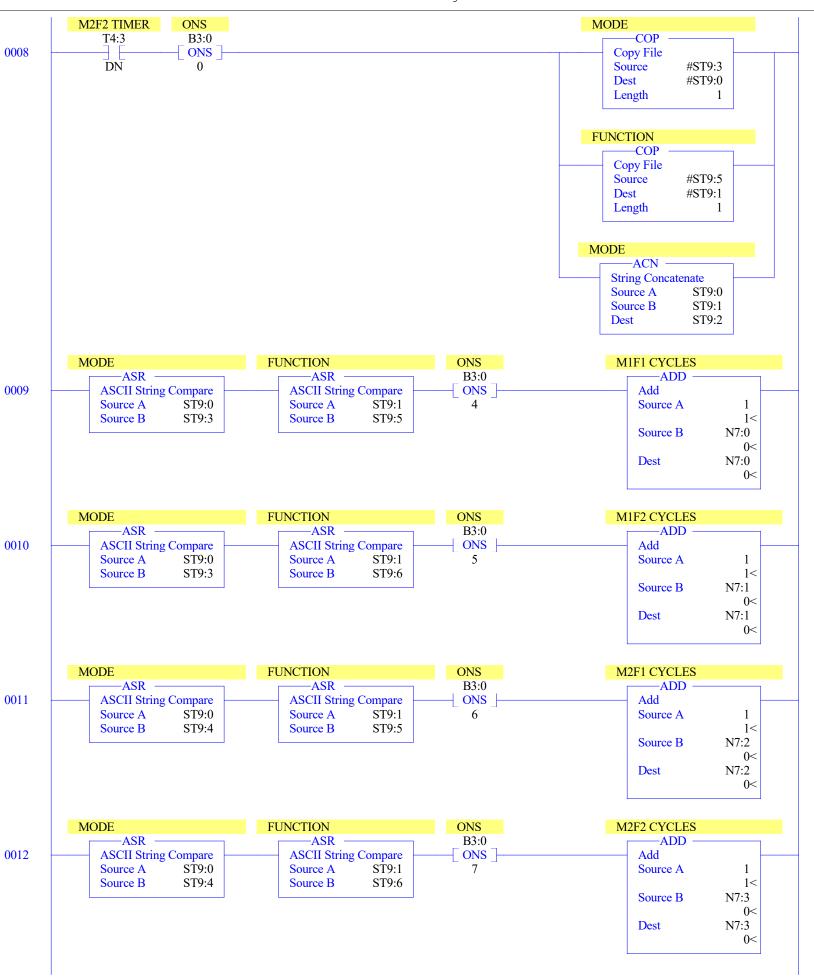
Data File List

Name	Number	Type	Scope	Debug	Words	Elements	Last	
OUTPUT	0	O	Global	No	12	4	D:3	
INPUT	1	I	Global	No	18	6	:5	
STATUS	2	S	Global	No	0	66	S:65	
BINARY	3	В	Global	No	1	1	33:0	
TIMER	4	T	Global	No	12	4	Г4:3	
COUNTER	5	C	Global	No	3	1	C5:0	
CONTROL	6	R	Global	No	3	1	R6:0	
INTEGER	7	N	Global	No	4	4	N7:3	
FLOAT	8	F	Global	No	2	1	F8:0	
STRINGS	9	ST	Global	No	294	7	ST9:6	

LAD 2 - MAIN --- Total Rungs in File = 14







0013

-(END)

Data File OO (bin) -- OUTPUT

Offset	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0		
0:0.0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Bul.1763	MicroLogix 1100 Series B
0:0.1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Bul.1763	MicroLogix 1100 Series B
0:0.2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Bul.1763	MicroLogix 1100 Series B
0:0.3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Bul.1763	MicroLogix 1100 Series B

Data File I1 (bin) -- INPUT

Offset	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0		
I:0.0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Bul.1763	MicroLogix 1100 Series B
I:0.1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Bul.1763	MicroLogix 1100 Series B
I:0.2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Bul.1763	MicroLogix 1100 Series B
I:0.3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Bul.1763	MicroLogix 1100 Series B
I:0.4	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Bul.1763	MicroLogix 1100 Series B-Analog
I:0.5	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Bul.1763	MicroLogix 1100 Series B-Analog

Data File S2 (hex) -- STATUS

```
Main
```

```
Processor Mode S:1/0 - S:1/4 = Remote Program Mode
On Power up Go To Run (Mode Behavior) S:1/12 = 0
First Pass S:1/15 = No
Free Running Clock S:4 = 0000-0000-0000-0000
Proc
OS Catalog Number S:57 = 1100
                                        User Program Type S:63 = 8001h
OS Series S:58 = A
                                        Compiler Revision Number S:64 =
OS FRS S:59 =
Processor Catalog Number S:60 =
Processor Series S:61 = A
Processor FRN S:62 =
Scan Times
Maximum (x10 ms) S:22 = 0
Watchdog (x10 ms) S:3 (high byte) = 10
Last 100 uSec Scan Time S:35 = 0
Scan Toggle Bit S:33/9 = 0
Math
Math Overflow Selected S:2/14 = 0
                                            Math Register (lo word) S:13 = 0
Overflow Trap S:5/0 = 0
                                             Math Register (high word) S:14-S:13 = 0
Carry S:0/0 = 0
                                             Math Register (32 Bit) S:14-S:13 = 0
Overflow S:0/1 = 0
Zero Bit S:0/2 = 0
Sign Bit S:0/3 = 0
Chan 0
Processor Mode S:1/0- S:1/4 = Remote Program Mode
Node Address S:15 (low byte) = 0
                                 Outgoing Msg Cmd Pending S:33/2 = 0
Baud Rate S:15 (high byte) = ?
Channel Mode S:33/3 = 0
Comms Active S:33/4 = 0
Incoming Cmd Pending S:33/0 = 0
Msg Reply Pending S:33/1 = 0
Debug
Suspend Code S:7 = 0
Suspend File S:8 = 0
Errors
Fault Override At Power Up S:1/8 = 0
                                             Fault Routine S:29 = 0
Startup Protection Fault S:1/9 = 0
                                             Major Error S:6 = 0h
Major Error Halt S:1/13 = 0
Overflow Trap S:5/0 = 0
                                             Error Description:
Control Register Error S:5/2 = 0
Major Error Executing User Fault Rtn. S:5/3 = 0
Battery Low S:5/11 = 0
Input Filter Selection Modified S:5/13 = 0
ASCII String Manipulation error S:5/15 = 0
Protection
Deny Future Access S:1/14 = No
Data File Overwrite Protection Lost S:36/10 = False
Mem Module
Memory Module Loaded On Boot S:5/8 = 0
Password Mismatch S:5/9 = 0
Load Memory Module On Memory Error S:1/10 = 0
```

Page 1

Load Memory Module Always S:1/11 = 0

Program Compare S:2/9 = 0

On Power up Go To Run (Mode Behavior) S:1/12 = 0

Data File Overwrite Protection Lost S:36/10 = 0

Forces

Forces Enabled S:1/5 = Yes Forces Installed S:1/6 = No Data File B3 (bin) -- BINARY

Offset 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0 (Symbol) Description

B3:0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0

Data File T4 -- TIMER

Offset	EN	ТТ	DN	BASE	PRE	ACC	(Symbol) Description
T4:0	0	0	0	1.0 sec	3	0	M1F1 TIMER
T4:1	0	0	0	1.0 sec	3	0	M1F2 TIMER
T4:2	0	0	0	1.0 sec	3	0	M2F1 TIMER
T4:3	0	0	0	1.0 sec	3	0	M2F2 TIMER

Data File C5 -- COUNTER

Offset CU CD DN OV UN UA PRE ACC (Symbol) Description
C5:0 0 0 0 0 0 0 0 0

Data File R6 -- CONTROL

Offset EN EU DN EM ER UL IN FD LEN POS (Symbol) Description R6:0 0 0 0 0 0 0 0 0

Offset 0 1 2 3 4 5 6 7 8 9

N7:0 0 0 0 0

Data File F8 -- FLOAT

Offset 0 1 2 3 4

F8:0 0

Data File ST9 -- STRINGS

Offset	LEN String Text	(Symbol) Description	
ST9:0	0		MODE
ST9:1	0		FUNCTIO
ST9:2	0		MODE+FU
ST9:3	6 Mode-1		M1
ST9:4	6 Mode-2		M2
ST9:5	2 F1		F1
ST9:6	2 F2		F2
1			

Address/Symbol Database

1979 1979	Second S									
13-271		Address	Symbol	Scope	Description	Sym Group	Dev.	Code	ABV	BLW
13.07.2	03074	B3:0/0			ONS					
13.07.2	03074	B3:0/1			ONS					
23.070 ORS	03075 088									
1997 1998	1997 1998									
NOTE OF THE PROPERTY OF THE PR	NOTE OF THE PROPERTY OF THE PR									
23006 DAS	23006 DAS									
## 1970 ONL ## 1971 ONL ## 1971 ONL ## 1971 ONL ## 1971 ONL ## 1972 ONL ## 1972 ONL ## 1972 ONL ## 1973 ONL ## 1974 ONL ## 197	## 1970 ONL ## 1971 ONL ## 1971 ONL ## 1971 ONL ## 1971 ONL ## 1972 ONL ## 1972 ONL ## 1972 ONL ## 1973 ONL ## 1974 ONL ## 197									
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MOTO MATERIAL STATE OF TABLE MOTO MATERIAL STATE MOTO MATERIAL STATE MOTO MATERIAL STATE MOT	MOTO MATERIAL STATE OF TABLE MOTO MATERIAL STATE MOTO MATERIAL STATE MOTO MATERIAL STATE MOT									
More controls More c	More controls More c									
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Arthmetic Flags Processor Atthmetic Energy Flag Processor Atthmetic Starty Flag Processor Atthmetic Starty Flag Processor Atthmetic Starty Flag Processor Mode Status/ Control Processor Mode Status/ Proce	Arthmetic Flags Processor Atthmetic Engry Flag Processor Atthmetic Engry Flag Processor Atthmetic Engry Flag Processor Stode Status Control Processor Mode S									
Processor Arthmetic Convertion Processor Arthmetic Stage Tlag Processor Arthmetic Stage Tlag Processor Arthmetic Stage Tlag Processor Arthmetic Stage Tlag Processor Hold Bit 0 Processor Hold Bit 1 P	Processor Artiments Carry Flag 10/12 Cocessor Artiments Discretive Flag 10/13 Cocessor Artiments Sign Flag 10/14 Processor Holds Bit 0 10/14 Pr									
Processor Arithmetic ConderTowy Overflow Flag 1070 Processor Mode Status (Control 1071 Processor Mode Status (Control 1	Processor Artithmetic UnderFlow Flow Flow Flow Flow Flow Flow Flow				<u> </u>					
Fromesor Arthmetic Sero Flag # Processor Arthmetic Sero Flag # Processor Arthmetic Sero Flag # Processor Mode Mit 0 Control # Processor Mode Mit 0 # Processor Mit 0 # Processor Mit 0 # Processor Mode Mit 0 # Processor Mit 0 # Processor Mode Mit 0 # Processor Mode Mit 0 # Processor Mit 0	Processor Arthmetic Sero Flag SIJ/3 Processor Arthmetic Sero Flag SIJ/3 Processor Mode Not October SIJ/4 Processor Mode Not I SIJ/4 Processor Mode Not I SIJ/5 Processor Mode Not I SIJ/5 Processor Mode Not I SIJ/5 Processor Mode Not I SIJ/6 Processor Mode Not I SIJ				Processor Arithmetic Carry Flag					
Processor Arithmetic Sign Fleg Processor Mode Status / Octrol Processor Mode Status / Octrol	Processor Arithmetic Sagn Fleg Processor Mode Status / Octrol	S:0/1			Processor Arithmetic Underflow/ Overflow Flag					
Processor Mode Statury Control Processor Mode Bit 0 Processor Mode Bit 0 Processor Mode Bit 2 Processor Mode Bit 2 Processor Mode Bit 3 Procesor Mode Bit 3 Processor Mode Bit 3 Processor Mode Bit 3	Sall Processor Mode Satury Control Processor Mode Dit U Processor Mode D	S:0/2			Processor Arithmetic Zero Flag					
Frocessor Node Bit 0 Processor Node Bit 1 Processor Node Bit 3 Processor Node Bit 3 Processor Node Bit 4 Procesor Node Bit 4 Procesor Node Bit 4 Procesor No	Processor Mode Mit 1 Processor Mode Mit 2 Processor Mode Mit 3 Processor Mode Mit 4 Processor Mit 4 Processor Mode Mit 4 Processor Mit 4 Processor Mit 4 Processor Mit 4 Processor Mit 5 Processor	S:0/3			Processor Arithmetic Sign Flag					
Signature of Processor Wode Bit 1 Processor Wode Bit 2 Processor Wode Bit 2 Processor Wode Bit 3 Processor Wode Bit 4 Process French Forces Enabled 11/4 Proces Enabled 11/4 Process Present 11/4 P	Processor Mode Bit 1 Processor Mode Bit 2 Processor Mode Bit 2 Processor Mode Bit 3 Processor Mode Bit 3 Processor Mode Bit 4 Processor Processor Mode Bit 4 Processor Processor Mode Bit 4 Processor Processor Bit 4 Processor Processor Bit 4 Processor Processor Bit 4 P	S:1			Processor Mode Status/ Control					
### ### ### ### #### #### ############	# Processor Mode Bit 2 Processor Mode Bit 3 Processor Mode Bit 4 Processor Mode Bit 3 Processor Mode Bit 4 Processor Mode Bit 4 Processor Mode Bit 5 Process Present Process Pre	S:1/0			Processor Mode Bit 0					
Side	Processor Mode Bit 3 Processor Mode Bit 4 Processor Beauty Processor Mode Bit 8 Pault Override at Proverup Satisfied Bit 1 Doad Memory Models Always Satisfied Bit 1 Doad Memory Models Bit 1 Doad Bit 1	S:1/1			Processor Mode Bit 1					
SIL/4 Processor Mode Bit 4 Process Pleasent Process Pleas	### Processor Mode Bit 4 Process Tresert Proce	S:1/2			Processor Mode Bit 2					
SIL/4 Processor Mode Bit 4 Process Pleasent Process Pleas	### Processor Mode Bit 4 Process Tresert Proce									
SIL/6 Porces Enabled Parces Present SIL/6 Porces Present SIL/6 Porces Present SIL/7 Comma Active Sil/9 Startup Portection Fault SIL/10 Load Memory Module Always SIL/10 Load Memory Module Always SIL/11 Load Memory Module Always SIL/11 Load Memory Module Always SIL/12 Load Memory Module Always SIL/13 Wajor Error Haited SIL/14 Porces SIL/14 Porces SIL/15 Pirst Pass SIL/16 Pirst	### STOCKS Pabled Forces Packers									
Site	131/6 Forces Fresent 131/7 Comman Active at Downrup 131/7 Pault Override at Downrup 131/10 Load Kemory Module on Memory Error 131/11 Load Kemory Module and Num 131/11 Load Kemory Module and Num 131/12 Load Kemory Module and Num 131/13 Major Error Maited 131/14 Major Error Maited 131/15 Pirst Pass 131/15 Sirst Pass 131/15 Sirst Pass 131/16 Sir Fending 131/17 Sir Fending 13									
Sit/7 Comms Active Fault Overide at Powerup Sit/8 Fault Overide at Powerup Sit/9 Statut protection Fault Load Memory Module Allways Sit/11 Load Memory Module Allways Sit/12 Load Memory Module Allways Sit/13 Major Fror Falted Access Denied Sit/14 Access Denied S	11/7 Comms Active 11/8 Paul to Overlide at Powerup 11/8 Paul to Startup Protection Fault 11/8 Paul to Overlide at Powerup 11/8 Paul									
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Said Load Memory Module on Memory Error					<u> -</u>					
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Side										
Major From Halted Access Denied From Halted From Hal	### SITO ** Halted ** Access Denied ** ### SIT Pending ** ### SIT Pend									
	### SITURE									
Size	Size									
STI Pending	SIZ Pending SiZ/1 STI Exacuting SiZ/2 STI Exacuting SiZ/3 STI Exacuting SiZ/4 STI Exacuting SiZ/5 SIZ Exacuting SiZ/6 Si	S:1/14			Access Denied					
SIT SIT	STI Enabled STI Executing Index Addressing File Range Saved with Debug Single Step	S:1/15			First Pass					
SIZ/A	STI Executing Index Addressing File Range	S:2/0			STI Pending					
	Saved with Debug Single Step	S:2/1			STI Enabled					
Saved with Debug Single Step	Saved with Debug Single Step	S:2/2			STI Executing					
Size Bit	### Size	S:2/3			Index Addressing File Range					
Siz/16	Siz/16	S:2/4			Saved with Debug Single Step					
Siz/6	Siz/6	S:2/5								
St2/17	### Style="font-size: 150%; as a command Pending ### St	S:2/6								
Strict	Size									
State	Current Scan Time									
Simple S	Sistro									
Sis	Sis 10									
Sis 2	Sis 2									
Sis Sis Major Err Detected Executing UserFault Routine	### ### ### ### ### ### ### ### ### ##									
M0-M1 Referenced on Disabled Slot	### Si5/4 Money Module Boot ### Si5/9 Memory Module Boot ### Si5/9 Memory Module Boot ### Si5/10 STI Overflow ### Si5/11 Battery Low ### Si5/12 Suspend Code ### Si5/13 Suspend Code ### Si5/14 Math Register ### Si1/1									
Sistemark Sist	### Sis.5/8 Memory Module Password Mismatch ### Sis.5/10 STI Overflow ### Sis.5/11 Battery Low ### Sis.6 Major Error Fault Code ### Suspend Code ### Suspend Code ### Suspend File ### Sis.9 Active Nodes ### Sis.10 Active Nodes ### Sis.11 I/O Slot Enables ### Sis.12 I/O Slot Enables ### Sis.13 Math Register ### Sis.14 Math Register ### Sis.15 Node Address/ Baud Rate ### Sis.15 Node Address/ Baud Rate ### Sis.16 Debug Single Step Rung ### Sis.17 Debug Single Step File ### Sis.18 Debug Single Step File ### Sis.19 Debug Single Step Freakpoint Rung ### Sis.20 Debug Fault/ Powerdown Rung ### Sis.21 Debug Fault/ Powerdown File ### Sis.22 Maximum Observed Scan Time ### Sis.23 Average Scan Time ### Sis.24 Index Register ### Sis.25 I/O Interrupt Pending ### Sis.26 I/O Interrupt Pending ### Sis.27 I/O Interrupt Enabled ### Sis.28 I/O Interrupt Enabled ### Sis.33 STI File Number ### Sis.33 Extended Proc Status Control Word ### Sis.33 Extended Proc Status Control Word ### Sis.34 Incoming Command Pending									
Sisylo	Memory Module Password Mismatch St.5/10 STI Overflow									
STI Overflow	### STI Overflow #### STI Overflow ### STI Overflow #### STI Overflow ###									
Sife	### Still Battery Low ### Suspend Code ### Suspend Code ### Suspend File #									
S:6 Major Érror Fault Code S:7 Suspend Code S:8 Suspend File S:9 Active Nodes S:10 Active Nodes S:11 I/O Slot Enables S:12 I/O Slot Enables S:12 Math Register S:13 Math Register S:14 Math Register S:15 Node Address/ Baud Rate Debug Single Step Rung S:16 Debug Single Step File S:17 Debug Single Step File S:18 Debug Single Step Breakpoint Rung S:19 Debug Single Step Breakpoint File S:20 Debug Fault/ Powerdown Rung S:21 Debug Fault/ Powerdown File S:22 Maximum Observed Scan Time S:23 Average Scan Time S:24 Index Register S:25 I/O Interrupt Pending S:26 I/O Interrupt Pending S:27 I/O Interrupt Enabled S:28 I/O Interrupt Enabled S:29 User Fault Routine File Number S:31 STI Site Number S:32 Extended Proc Status Control Word S:33/O Encoming Command Pending	Major Error Fault Code S:7 Suspend Code S:8 Suspend File S:9 Active Nodes S:10 Active Nodes S:11 I/O Slot Enables S:12 I/O Slot Enables S:12 Math Register S:13 Math Register S:14 Math Register S:15 Node Address/ Baud Rate Debug Single Step Rung S:16 Debug Single Step File S:17 Debug Single Step File S:18 Debug Single Step Breakpoint Rung S:19 Debug Single Step Breakpoint File S:20 Debug Fault/ Powerdown Rung S:21 Debug Fault/ Powerdown File S:22 Maximum Observed Scan Time S:23 Average Scan Time S:24 Index Register S:25 I/O Interrupt Pending S:26 I/O Interrupt Pending S:27 I/O Interrupt Enabled S:28 I/O Interrupt Enabled S:29 User Fault Routine File Number S:30 STI Setpoint S:31 STI File Number S:33 Extended Proc Status Control Word S:33 Extended Proc Status Control Word S:33 Extended Proc Status Control Word S:33 December Status Control Word Stat									
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S:8 Suspend File S:9 Active Nodes S:10 Active Nodes S:11 I/O Slot Enables S:12 I/O Slot Enables S:13 Math Register S:14 Math Register S:15 Node Address/ Baud Rate S:16 Debug Single Step Rung S:17 Debug Single Step File S:18 Debug Single Step File S:19 Debug Single Step Breakpoint Rung S:19 Debug Single Step Breakpoint File S:20 Debug Fault/ Powerdown Rung S:21 Debug Fault/ Powerdown File S:22 Maximum Observed Scan Time S:23 Average Scan Time S:24 Index Register S:25 I/O Interrupt Pending S:26 I/O Interrupt Pending S:27 I/O Interrupt Enabled S:28 I/O Interrupt Enabled S:29 User Fault Routine File Number S:30 STI Setpoint S:31 STI File Number S:32 Extended Proc Status Control Word S:33/O Incoming Command Pending	Sign Active Nodes Sign Active Nodes Sign Active Nodes Sign Active Nodes Sign I/O Slot Enables Sign I/O Slot Enables Sign Math Register Math Register Node Address/ Baud Rate Sign Node Address									
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S:11	I/O Slot Enables I/O Slot En									
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S:33/0 Incoming Command Pending	S:33/0 Incoming Command Pending	S:33								
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Address/Symbol Database

Address	Symbol	Scope	Description	Sym Group	Dev. Code	ABV	BLW
S:33/2 S:33/3 S:33/4 S:33/5 S:33/6 S:33/7 S:33/10 S:33/11 S:33/12 S:33/11 S:33/12 S:33/14 S:33/15 S:33/15 S:33/16 S:33/16 S:33/16 S:33/17 S:33/17 S:33/17 S:33/17 S:33/17 S:33/17 S:33/17 S:33/17 S:33/17 S:34/17 S:34/17 S:34/17 S:34/17 S:36/8 S:36/9 S:36/9 S:36/9 S:36/9 S:36/9 S:36/9 S:36/9 S:36/9 S:37 S:38 S:38/9 S:59/8 S:59/8 S:59/8 S:59/8 S:59/8 S:59/8 S:61/8 S:62/8 S:63/8 S:64/8 S:65/8 S:70/8 S:77/8 S:78/8 S:79/8 S:70/8 S:70/8 S:70/8 S:70/8 S:70/8 S:70/8 S:70/8 S:70/8 S:7	Symbol	Scope	Outgoing Message Command Pending Selection Status User/DF1 Communicat Servicing Selection Message Servicing Selection Channel 0 Message Servicing Selection Channel 1 Interrupt Latency Control Flag Scan Toggle Flag Discrete Input Interrupt Reconfigur Flag Online Edit Status Scan Time Timebase Selection DTR Control Bit DTR Force Bit Pass-thru Disabled Pass-Thru Disabled Flag DH+ Active Node Table Enable Flag Floating Point Math Flag Disable,Fl Last 1 ms Scan Time Extended Minor Error Bits DII Lost STI Lost Memory Module Data File Overwrite Protection Clock Calendar Year Clock Calendar Month Clock Calendar Month Clock Calendar Minutes Clock Calendar Minutes Clock Calendar Minutes Clock Calendar Minutes Clock Calendar Flag DII Interrupt Time I/O Event Interrupt File Number Discrete Input Interrupt- Slot Number Discrete Input Interrupt- Solt Number Discrete Input Interrupt- Compare Value Processor Catalog Number Discrete Input Interrupt- Accumulat Reserved/ Clock Calendar Day of the Week Last DII Scan Time Maximum Observed DII Scan Time Operating System Catalog Number Discrete Input Interrupt- Accumulat Reserved/ Clock Calendar Day of the Week Last DII Scan Time Maximum Observed DII Scan Time Operating System FRN Processor Series Processor Revision User Program Functional Index User RAM Size Channel 0 Active Nodes DH+	Sym Group	Dev. Code	ABV	BLW
ST9:6 T4:0			F2 M1F1 TIMER				
T4:0/DN T4:1 T4:1/DN			M1F2 TIMER				
T4:2 T4:2/DN			M2F1 TIMER				

Address/Symbol Database

Address Symbol Scope Description Sym Group Dev. Code ABV BLW
T4:3 M2F2 TIMER

Instruction Comment Database

Address Instruction Description

Group_Name Description