

**ARM<sup>®</sup> Cortex<sup>®</sup>-M**  
**32-bit Microcontroller**

**Mini58 Series CMSIS BSP**  
**Revision History**

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### Revision 3.00.002 (Released 2016-06-15)

1. Updated CMSIS to v4.5.0.
2. Added sample codes including ADC\_PWMTrigger, SPI\_MasterFIFOmode and SPI\_SlaveFIFOmode.
3. Minor bug fixes.

### Revision 3.00.001 (Released 2015-06-05)

1. Regenerated register and bit field definitions in Mini58Series.h based on Mini58 Technical Reference Manual Rev 1.0.
2. Removed FMC\_ReadDID(), FMC\_DisableAPUpdate(), FMC\_DisableSPUpdate(), FMC\_SetBootSource(), FMC\_DisableConfigUpdate(), FMC\_DisableLDUpdate(), FMC\_EnableAPUpdate(), FMC\_EnableSPUpdate(), FMC\_EnableConfigUpdate(), FMC\_EnableLDUpdate() functions in fmc.c.
3. Renamed TIMER\_TOGGLE\_TMXEXT to TIMER\_TOGGLE\_TMX\_EXT in timer.h.
4. Updated all IAR samples to set Reset\_Handler() as entry point.
5. Updated FMC\_WriteConfig() to check input parameter in fmc.c.
6. Updated TIMER\_Open() and TIMER\_Delay() to support the time-out calculation while timer clock is faster than 0x1FFFFFF Hz in timer.c.
7. Updated UART\_Open() and UART\_SetLine\_Config() to support using PLL as UART clock source in uart.c.
8. Updated UART\_SelectIrDAMode() to support using PLL and HIRC as UART clock source in uart.c.
9. Updated WDT\_Open() to support reset delay parameter in wdt.c.
10. Updated Timer\_Wakeup sample code to unlock protect register before entering Power-down mode.
11. Added ACMP\_SET\_FILTER() macro in acmp.h.
12. Added ADC\_SeqModeEnable() and ADC\_SeqModeTriggerSrc() functions in adc.c.
13. Added CLK\_EnableSysTick() and CLK\_DisableSysTick() functions in clk.c.
14. Added FMC\_GetVectorPageAddr() function in fmc.c.
15. Added PWM\_SET\_CMCRD(), PWM\_ENABLE\_ASYMMETRIC\_MODE(), and PWM\_ENABLE\_PCAEN() macros in pwm.h.
16. Added PWM\_EnableCenterInt(), PWM\_DisableCenterInt(), PWM\_ClearCenterIntFlag(), PWM\_GetCenterIntFlag(), PWM\_EnableRiseInt(), PWM\_DisableRiseInt(), PWM\_ClearRiseIntFlag(), and PWM\_GetRiseIntFlag() function in pwm.c.
17. Added SPI\_GET\_TX\_FIFO\_FULL\_FLAG() macro in spi.h.
18. Added PWM\_MaskAlign sample.
19. Fixed ACMP\_SET\_NEG\_SRC() and ACMP\_ENABLE\_RISING\_EDGE\_TRIGGER() implementation error in acmp.h.
20. Fixed I2C\_GetBusClockFreq() implementation error in i2c.c.
21. Fixed I2C\_CLEAR\_WAKEUP\_FLAG(), I2C\_DISABLE\_CLOCK\_STRETCH() and I2C\_ENABLE\_CLOCK\_STRETCH() implementation error in i2c.h.
22. Fixed TIMER\_CAPTURE\_RISING\_\* definition error in timer.h.
23. Fixed TIMER\_EnableCaptureDebounce() and TIMER\_DisableCaptureDebounce() implementation error in timer.c.
24. Fixed PWM\_ENABLE\_OUTPUT\_INVERTER() implementation error in pwm.h.
25. Fixed PWM\_GetADCTriggerFlag() and PWM\_ConfigOutputChannel() implementation error in pwm.c.
26. Fixed SPI\_SET\_xSB\_FIRST() implementation error in spi.h.
27. Fixed UART\_CLEAR\_RTS(), UART\_EnableFlowCtrl(), UART\_ClearIntFlag() implementation error in uart.c and uart.h.

**Revision 3.00.000** (Released 2015-2-27)

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1. Initial release.

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