

5	4	3	2	1
Date		Description		
2018/01/23		Initial		
2018/02/07		1. move out flash, 32K XTAL, C24		
2018/06/15		1. Change to A1 version		
2018/09/10		1. C26,C27 C28 DNI 2. Add C68, C69 L3 CN1 3. remove R3 R6 4. C14 change to 4.7uF 5. Add pi circuit on pin19,pin20		
2018/10/29		1. Add Flash, 32K XTAL, RST, EN circuit		
2018/11/08		1. remove C18,R82,R87,R88 (Flash part) 2. remove C20,C16, change C21 to 4,7uF 3. remove C18, change C72 to 10uF 4. remove D1,D2,C28,C26,C27 5. Change 32.768 XTAL 6. Change C70,C71 to 5.1pF		
2018/12/03		1. remove C67 (Buck FB)		
2018/12/22		1. Change TP3 circuit		
<div> <div> <div>H1</div> <div>TEST-1-R</div> </div> <div> <div>H2</div> <div>TEST-1-R</div> </div> <div> <div>H3</div> <div>TEST-1-R</div> </div> <div> <div>H4</div> <div>TEST-1-R</div> </div> <div> <div>H5</div> <div>TEST-1-R</div> </div> <div> <div>H6</div> <div>TEST-1-R</div> </div> </div>		<div> </div> <div> </div>		
<div> <div>OpuLinks</div> <div>旺凌科技</div> </div>		<div> <div>Title</div> <div>History</div> <div>Size A</div> <div>Document Number &lt;Doc&gt;</div> <div>Rev 1.2E</div> <div>Date: Saturday, December 22, 2018</div> <div>Sheet 1 of 2</div> </div>		
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