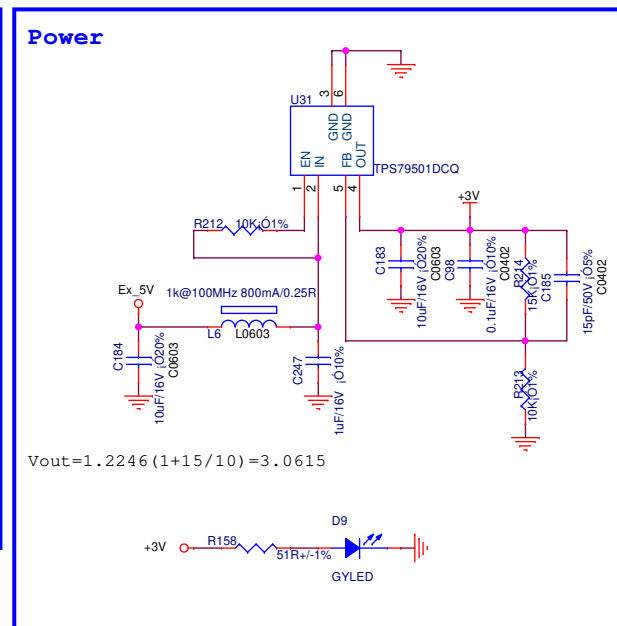
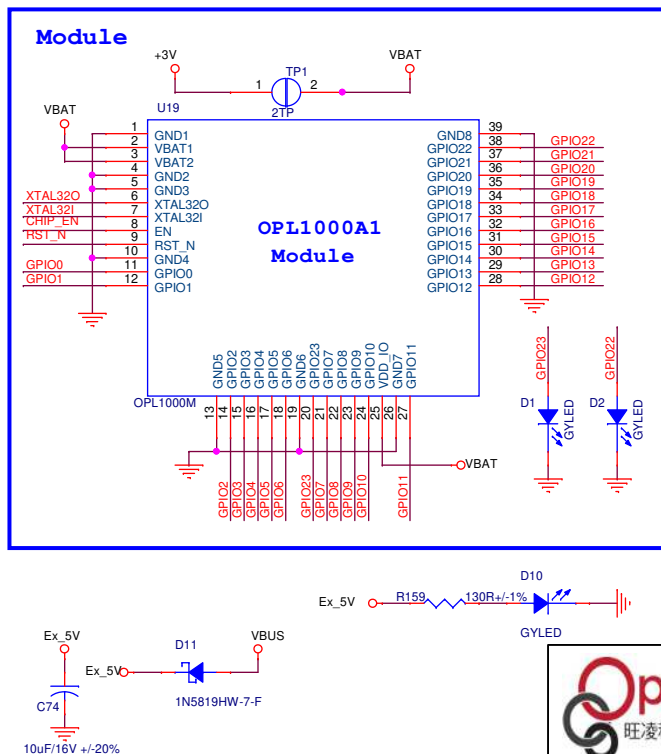
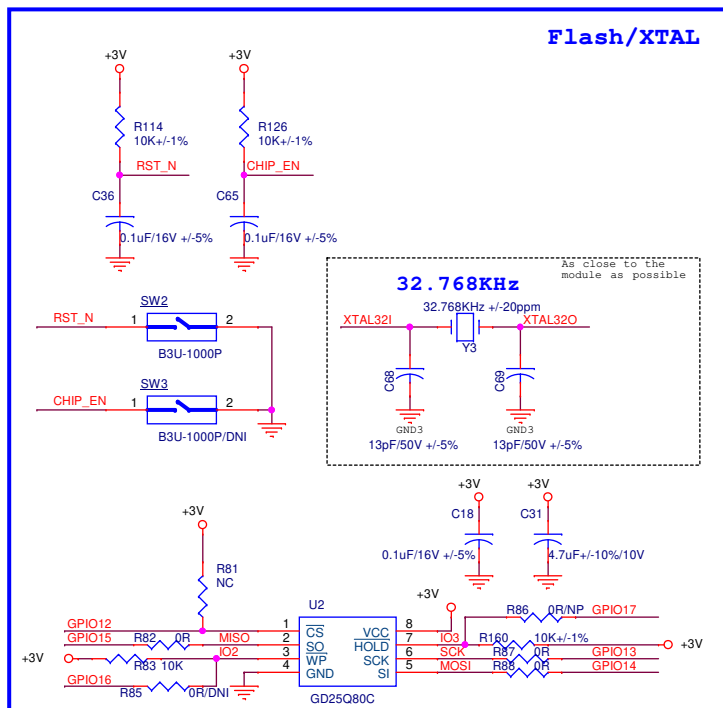
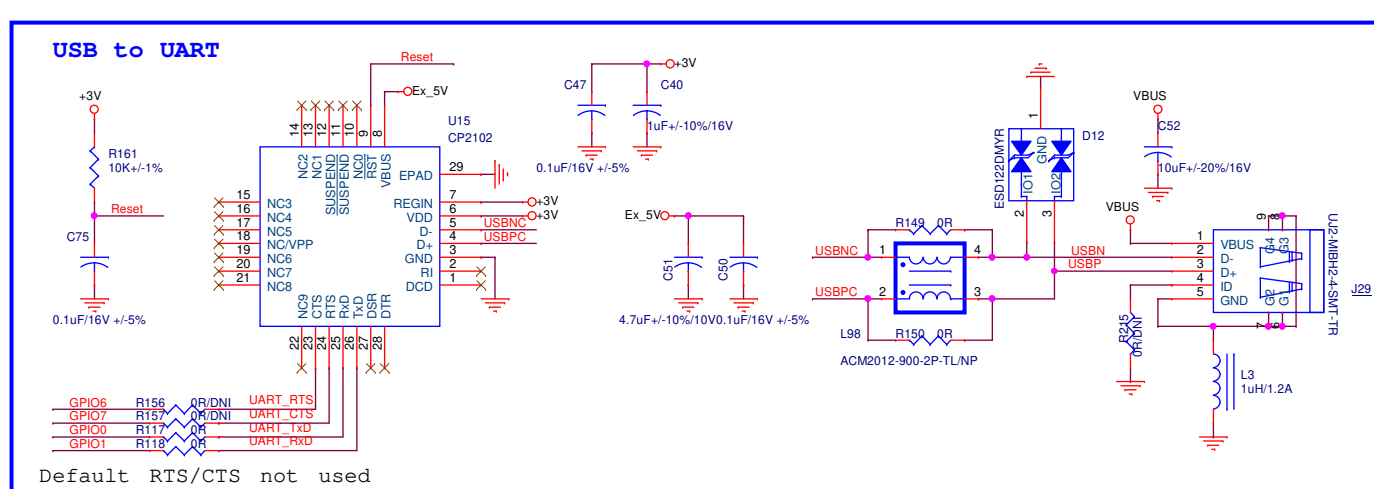
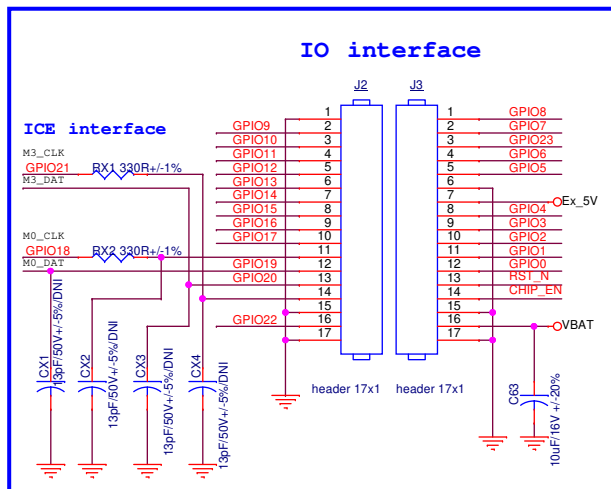


OPL1000 A1 DevKit

Date	Description
2018/06/21	1. Add Rx1,Rx2,CX1,CX2,CX3,CX4 for ICE interface issue 2. Add TP1
2018/08/02	1. R85,R86 DNI 2. R83 Load 3. VBUS change to EX_5V, move out SW4 4. VBUS connect to EX_5V via diode 5. change USB ESD protect diode(D12) 6. change USB data path from GPIO8/9 to GPIO 0/1 7. Add D1, D2
2018/10/02	1. Change USB connector Layout only 2. SW3 DNI, 'CHIP_EN' default pull high



Title		
History		
Size A	Document Number <Doc>	Rev V1.2
Date:	Wednesday, October 10, 2018	Sheet 1 of 2



OPL1000_A1_DevKit			
Size B	Document Number		Rev 1.2
Date:	Wednesday, October 10, 2018	Sheet 2 of 2	