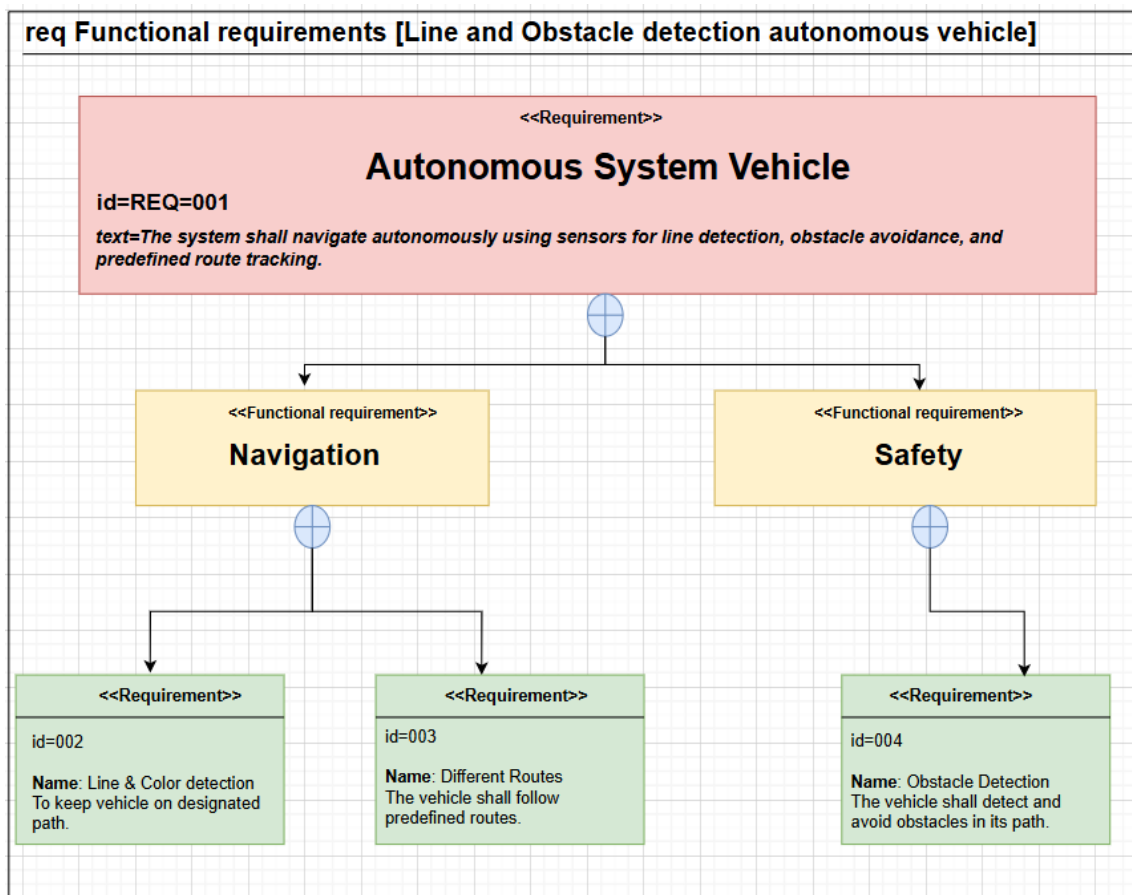


Prototyping and System Engineering Pre-final SysML

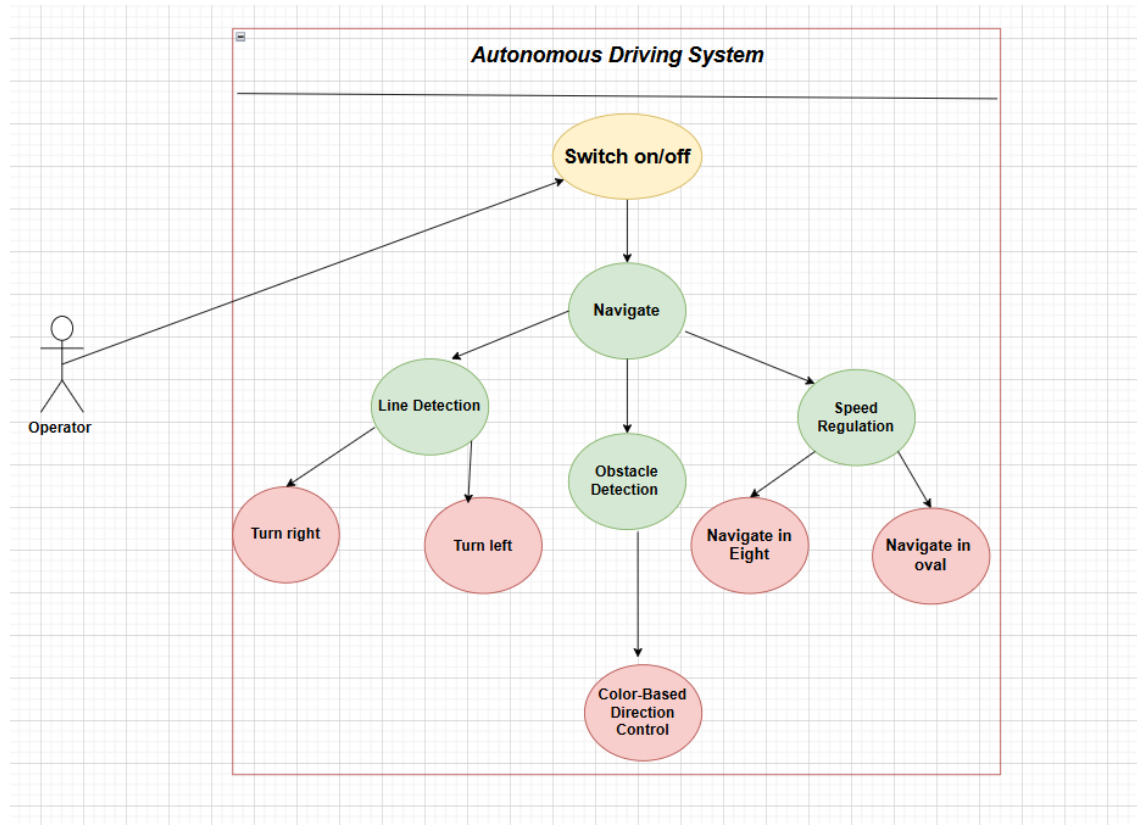
Group C1: Mohamed Abdo, Md Ruhul, Md Rahman, Muhammad Aamir

Requirement diagram:

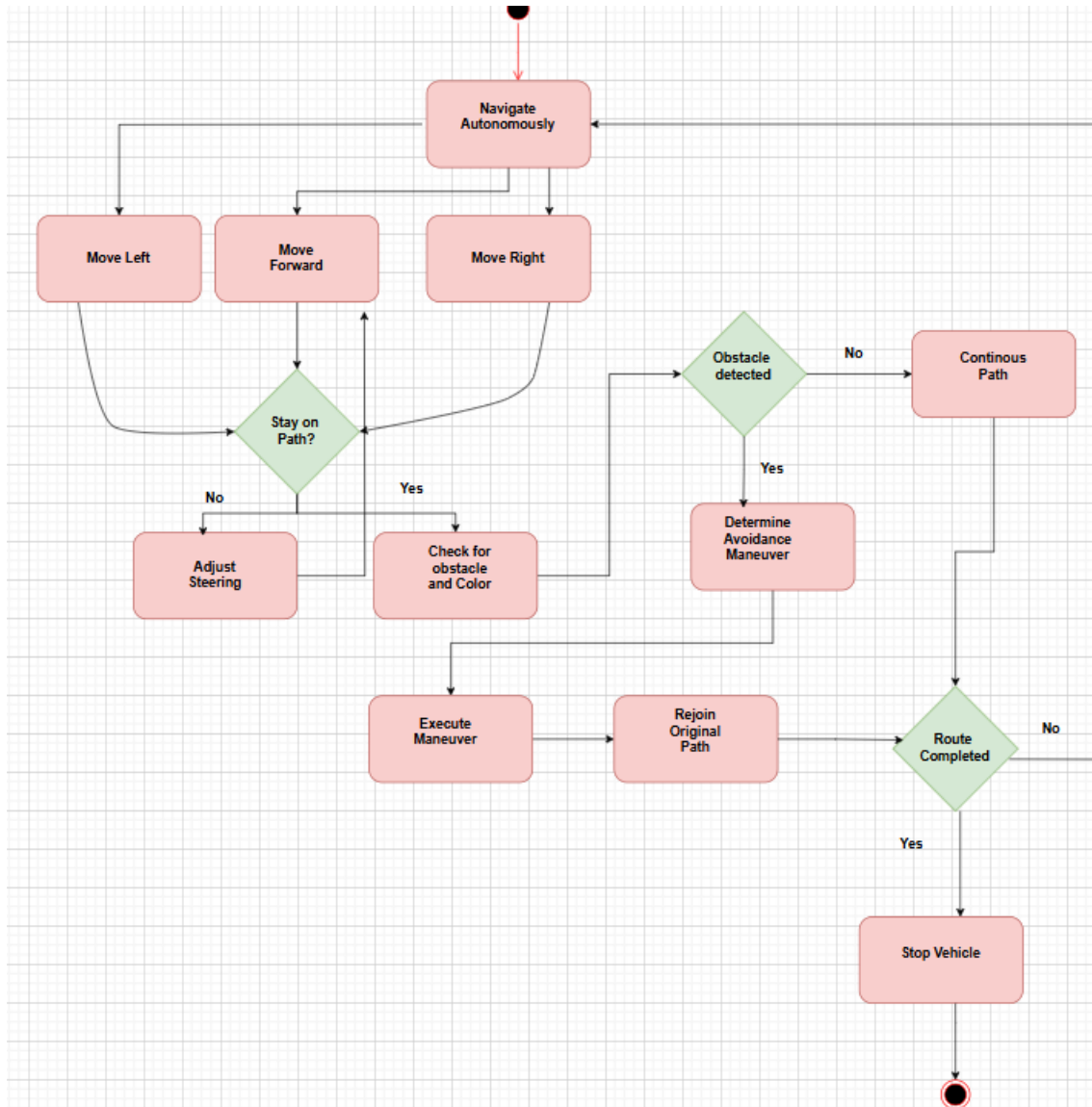


Behaviour Diagrams:

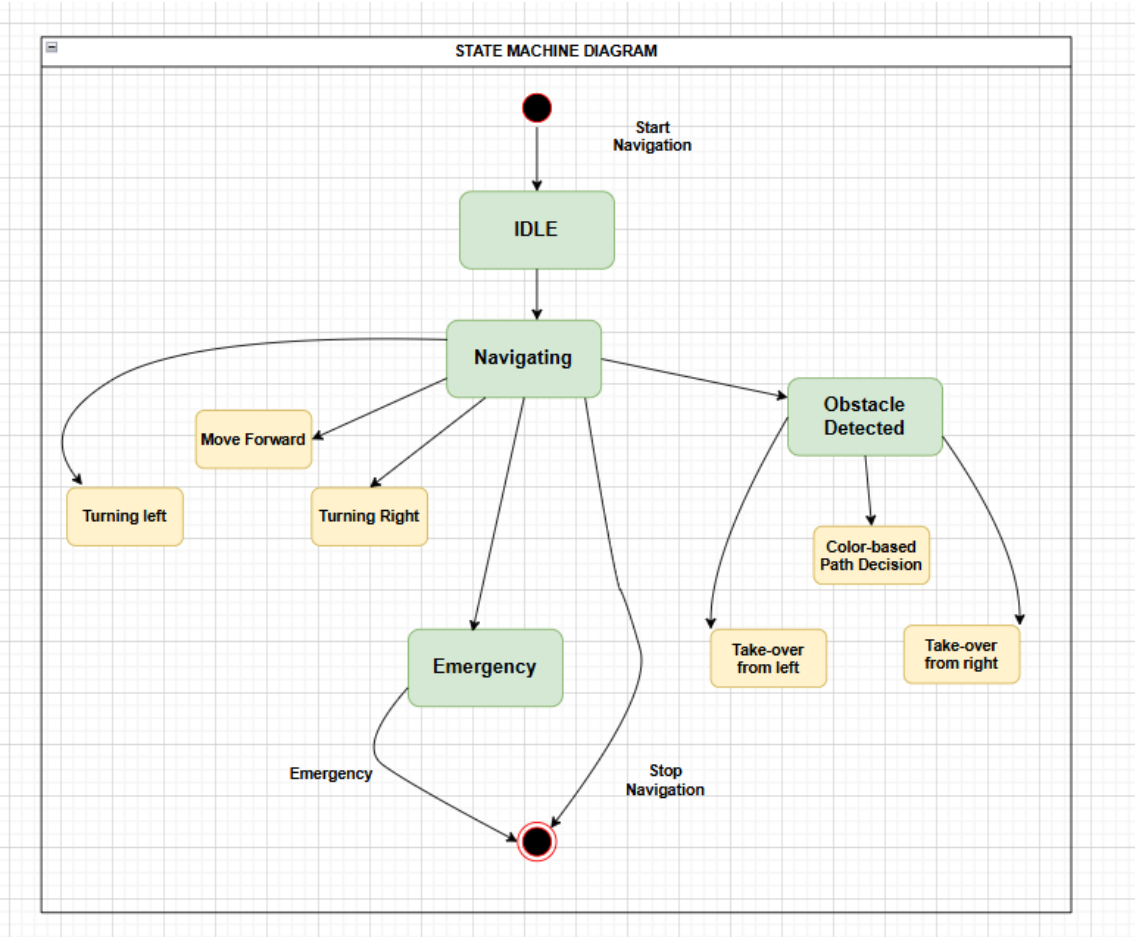
Use Case:



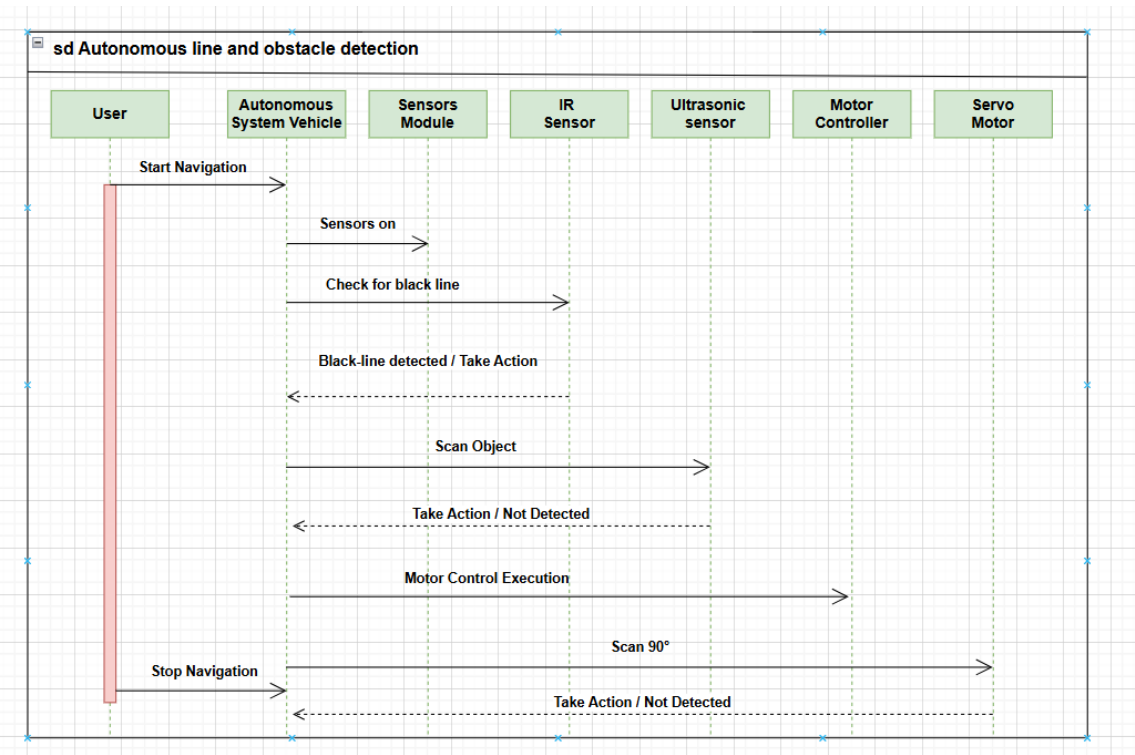
Activity Diagram:



State Machine:

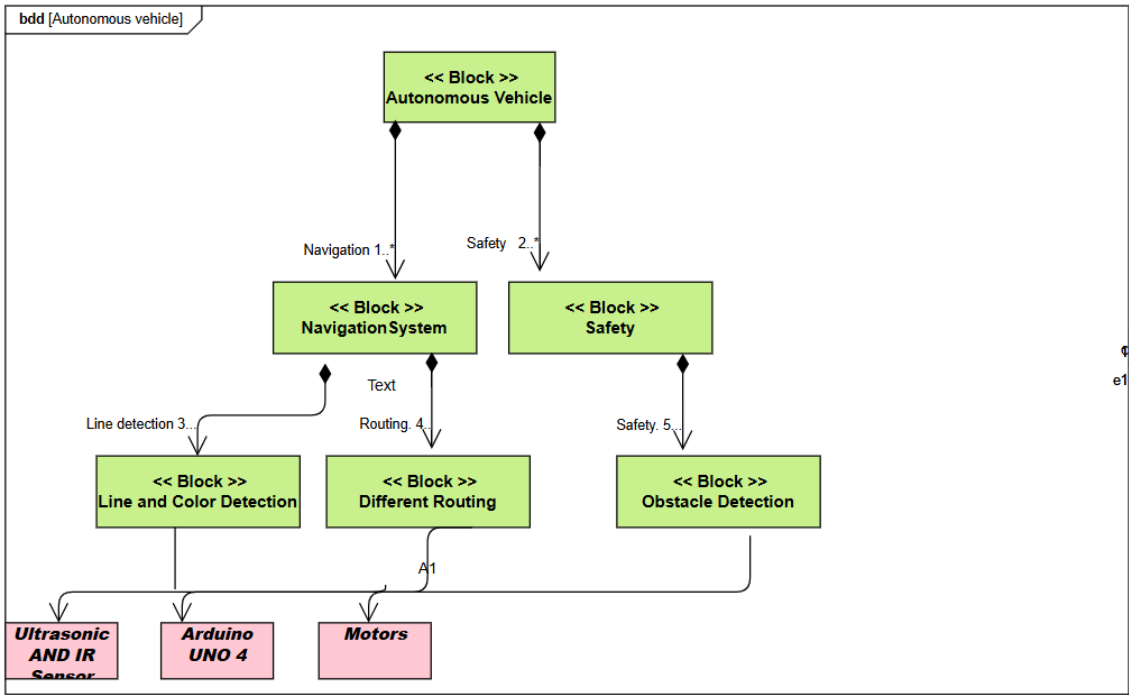


Sequence Diagram:

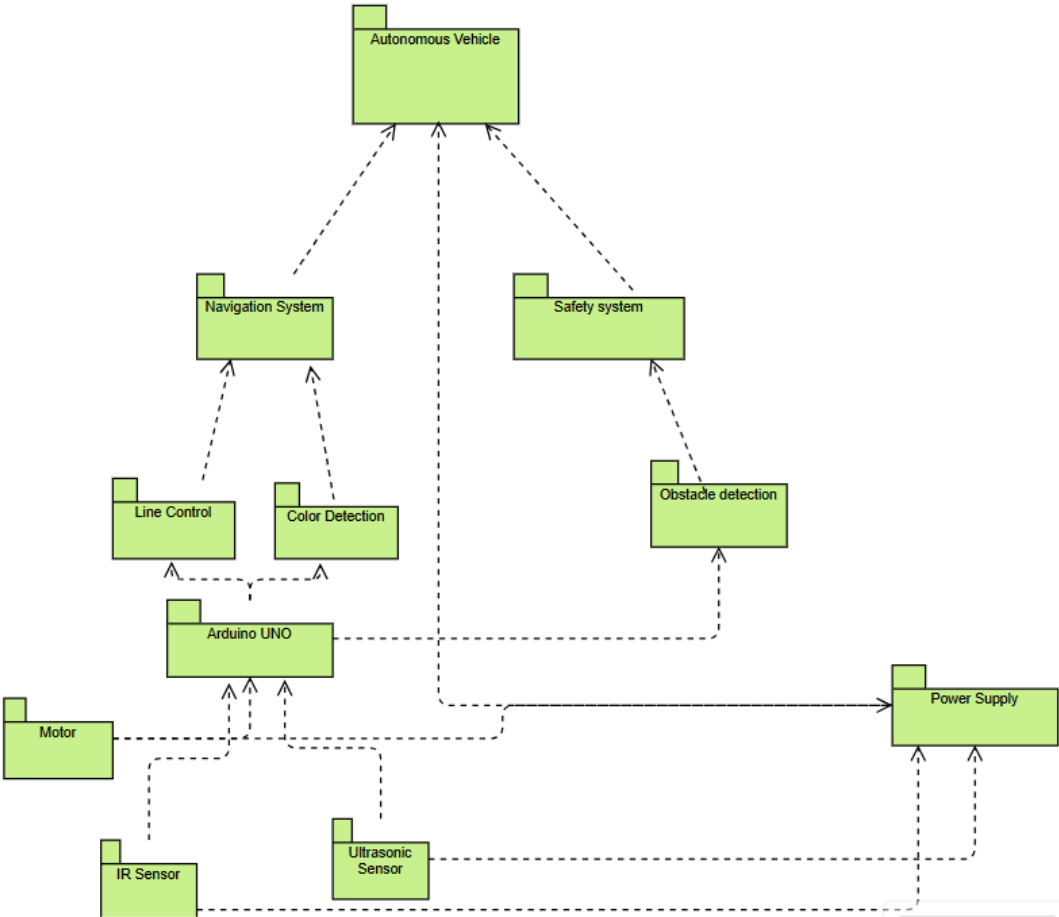


Structure Diagrams:

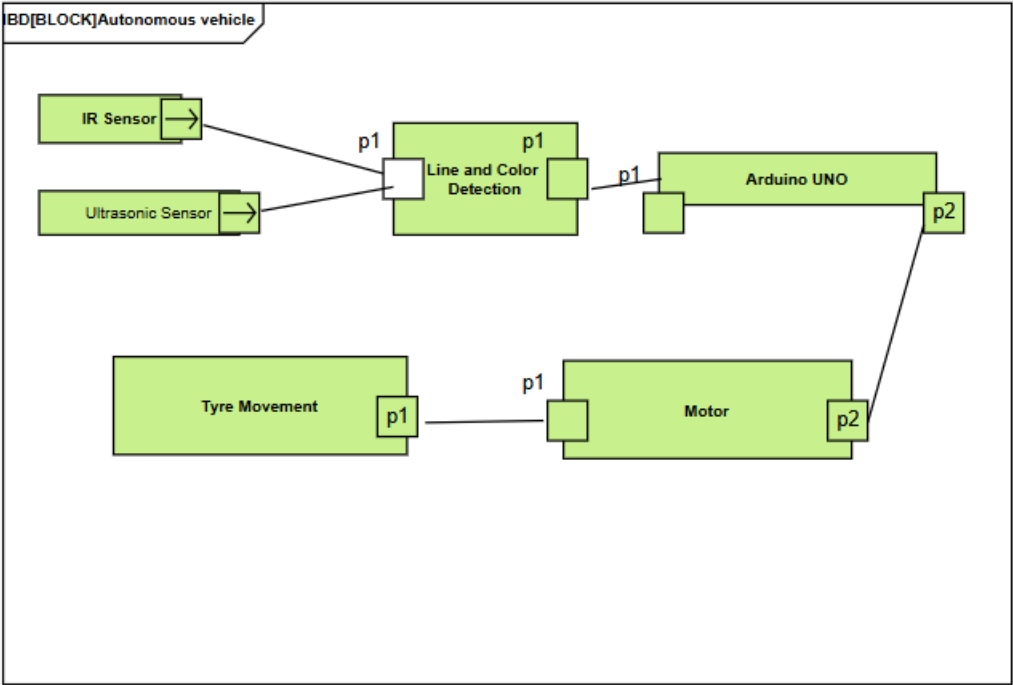
Block definition diagram:



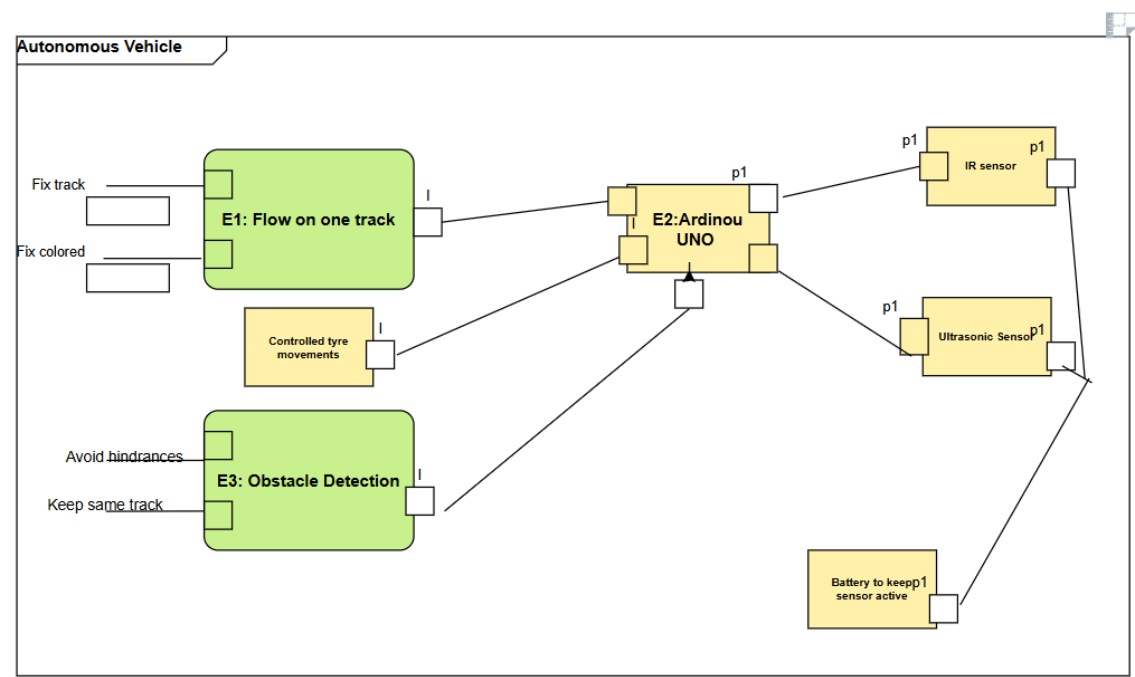
Package diagram:



Internal structure diagram:



Parametric diagram:



***Sincere thanks for your guidance
through this project***