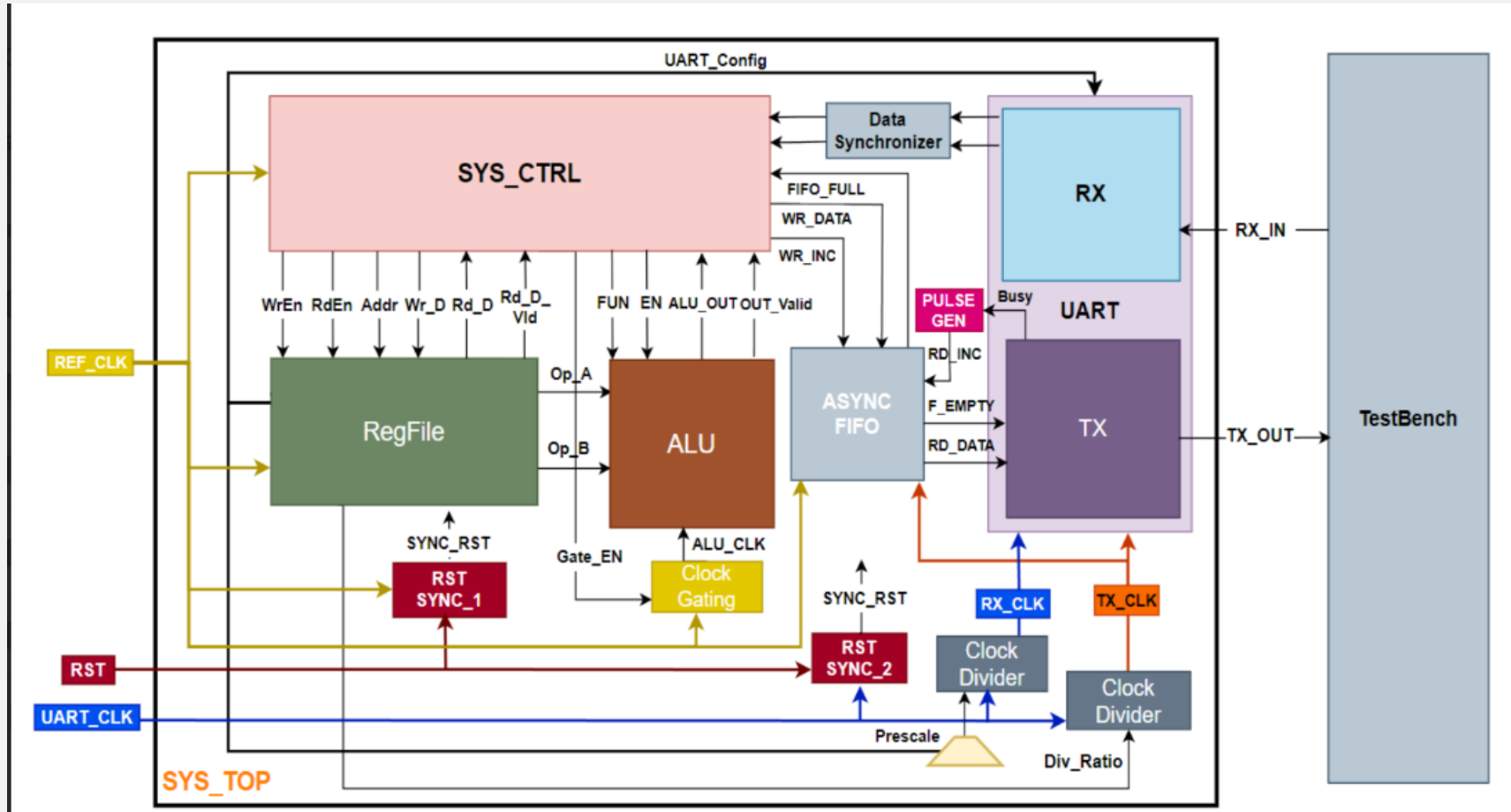


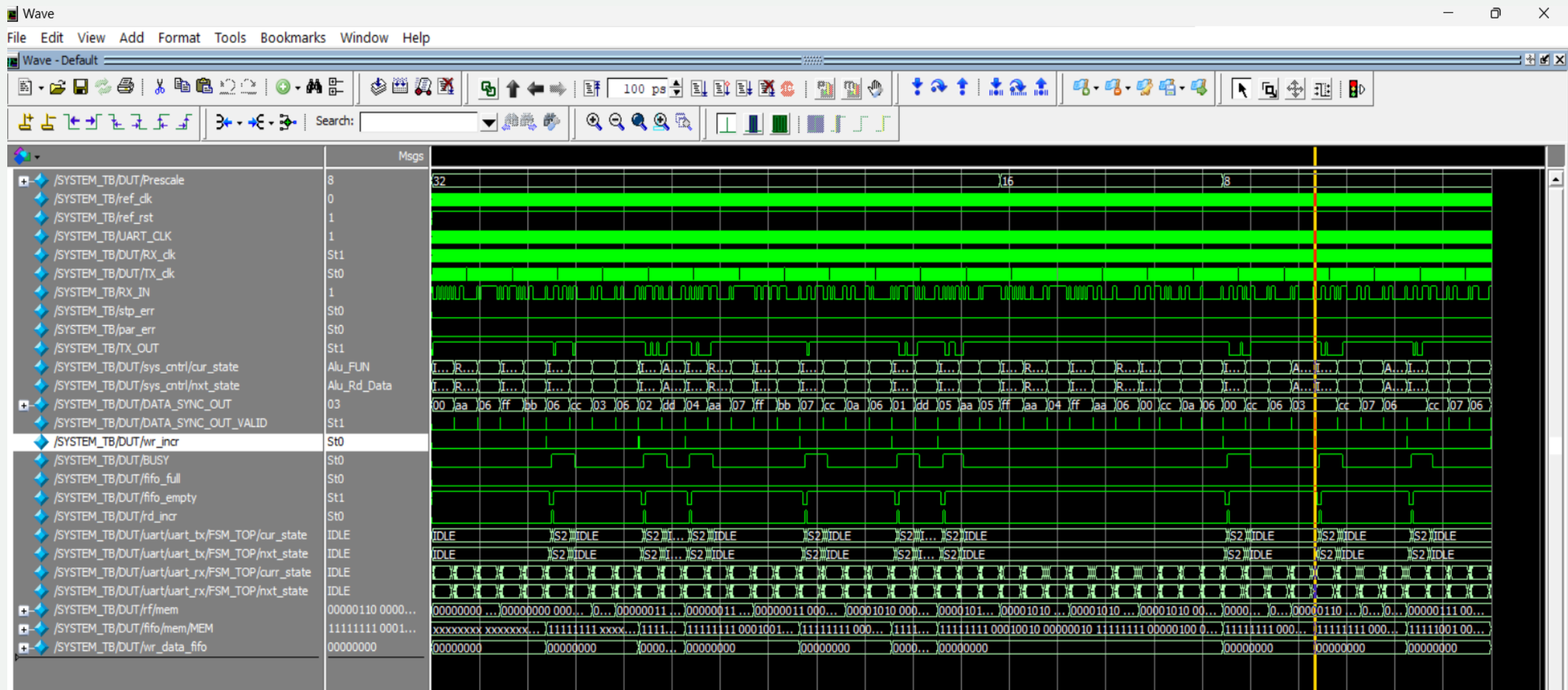
Mohamed Osama Elkholy

Final System

Overview

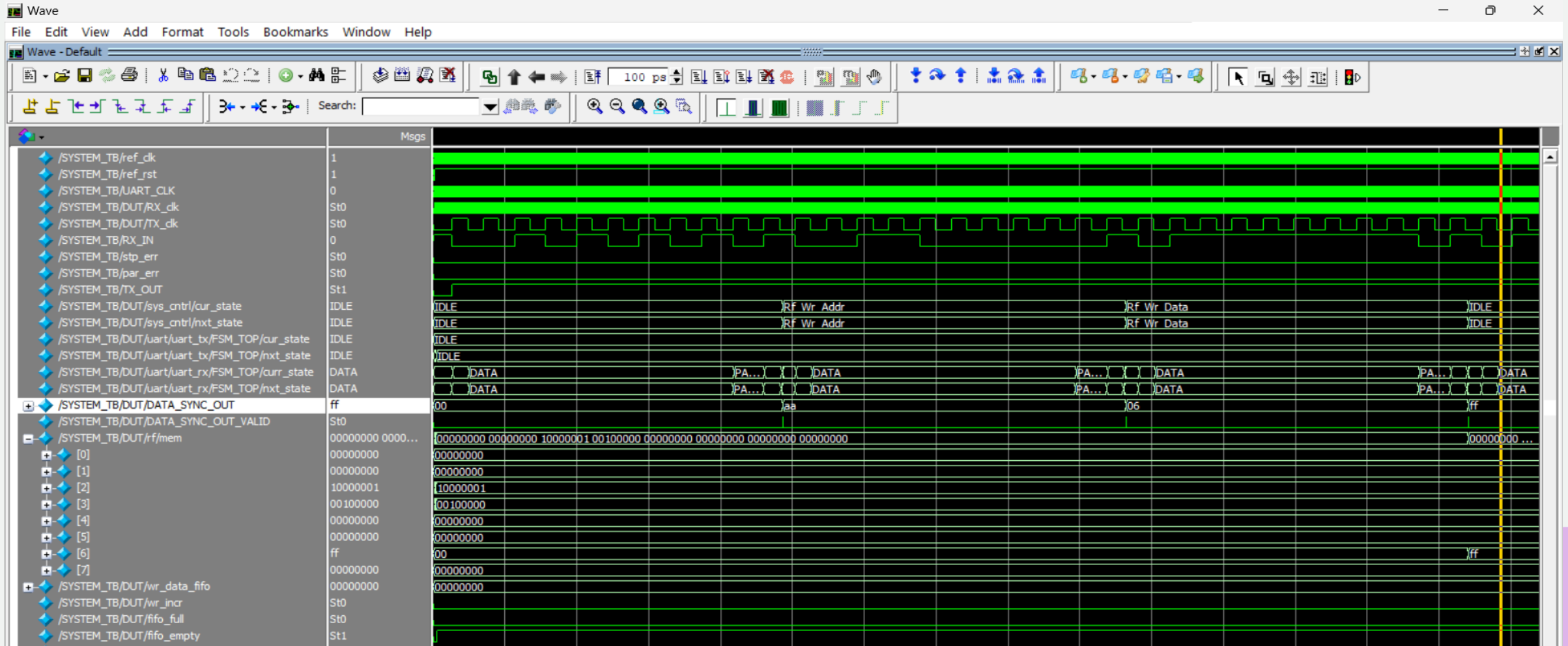


Overview

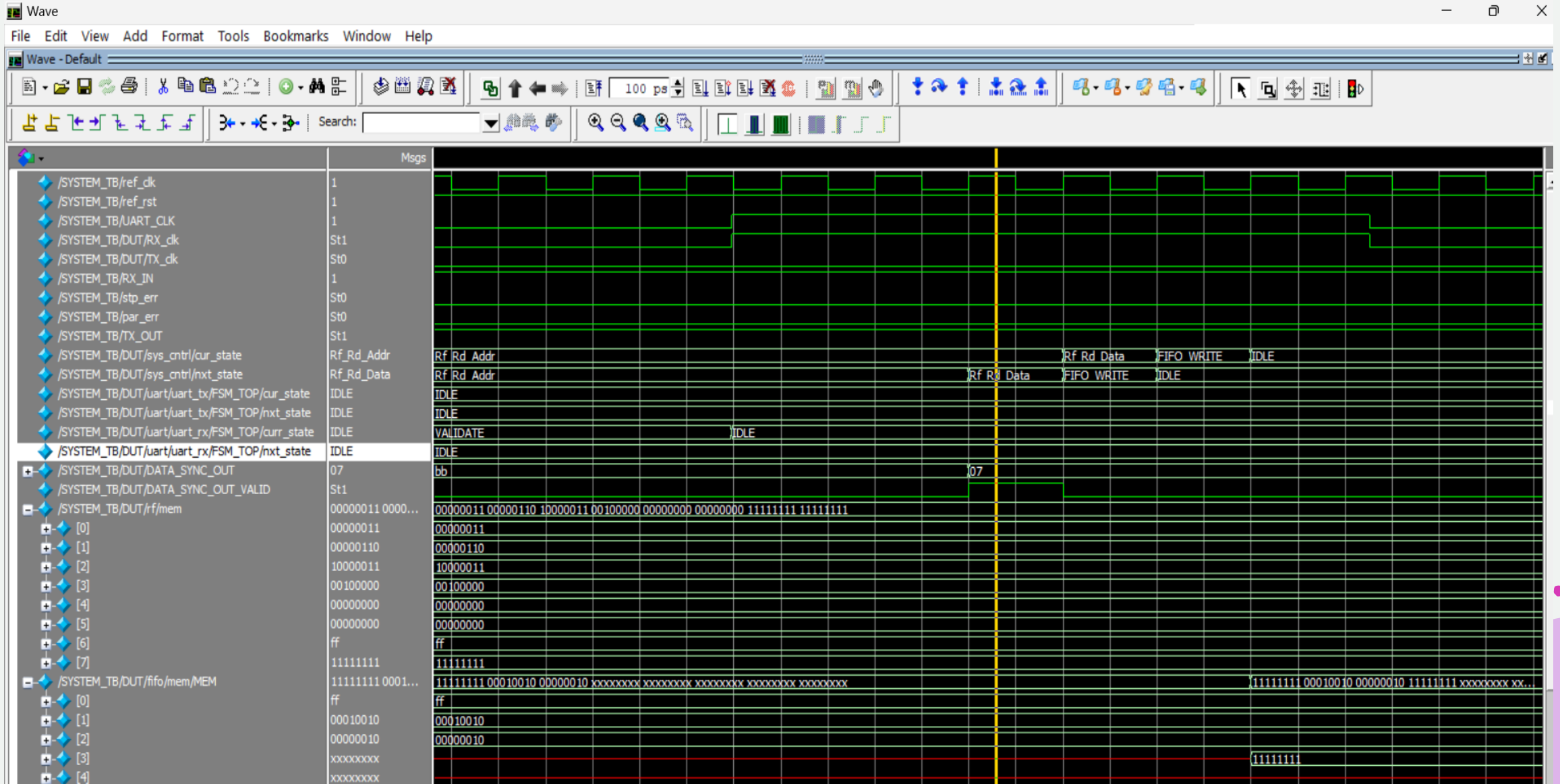


Four commands tested at prescale = 32

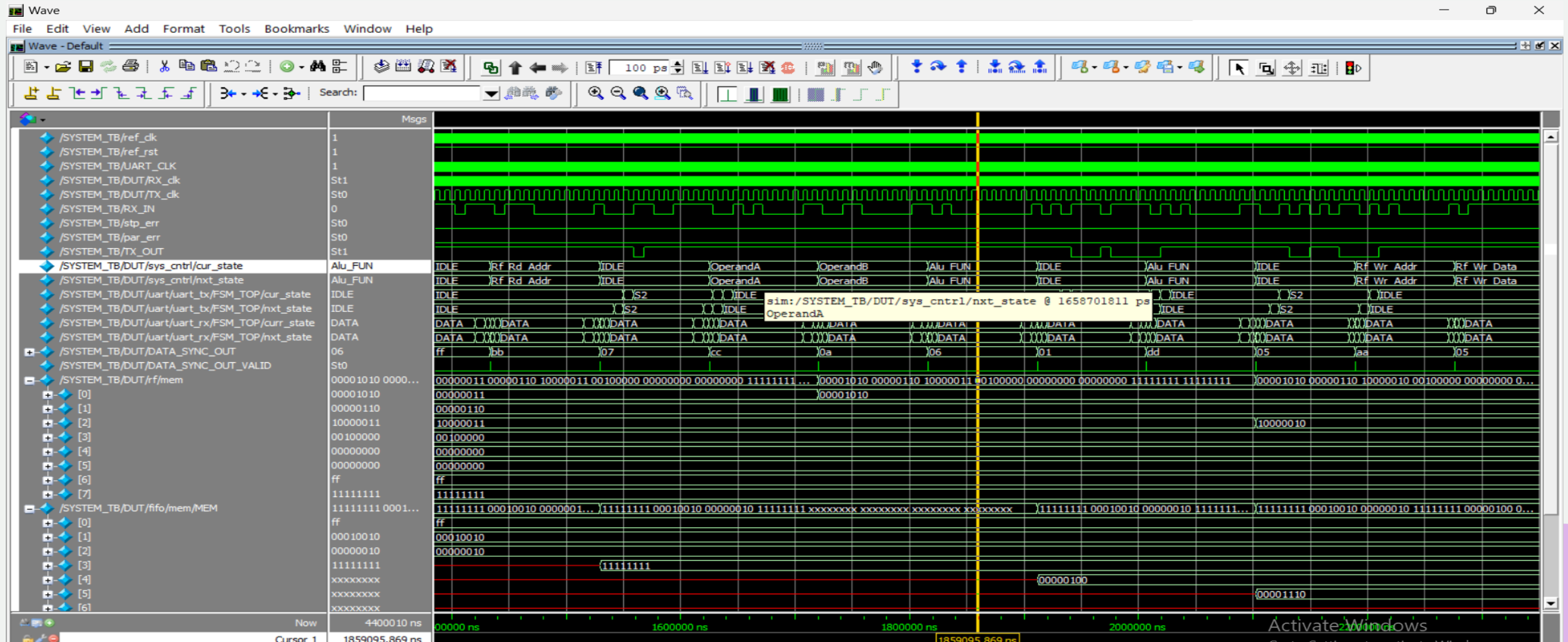
Register File Write Command



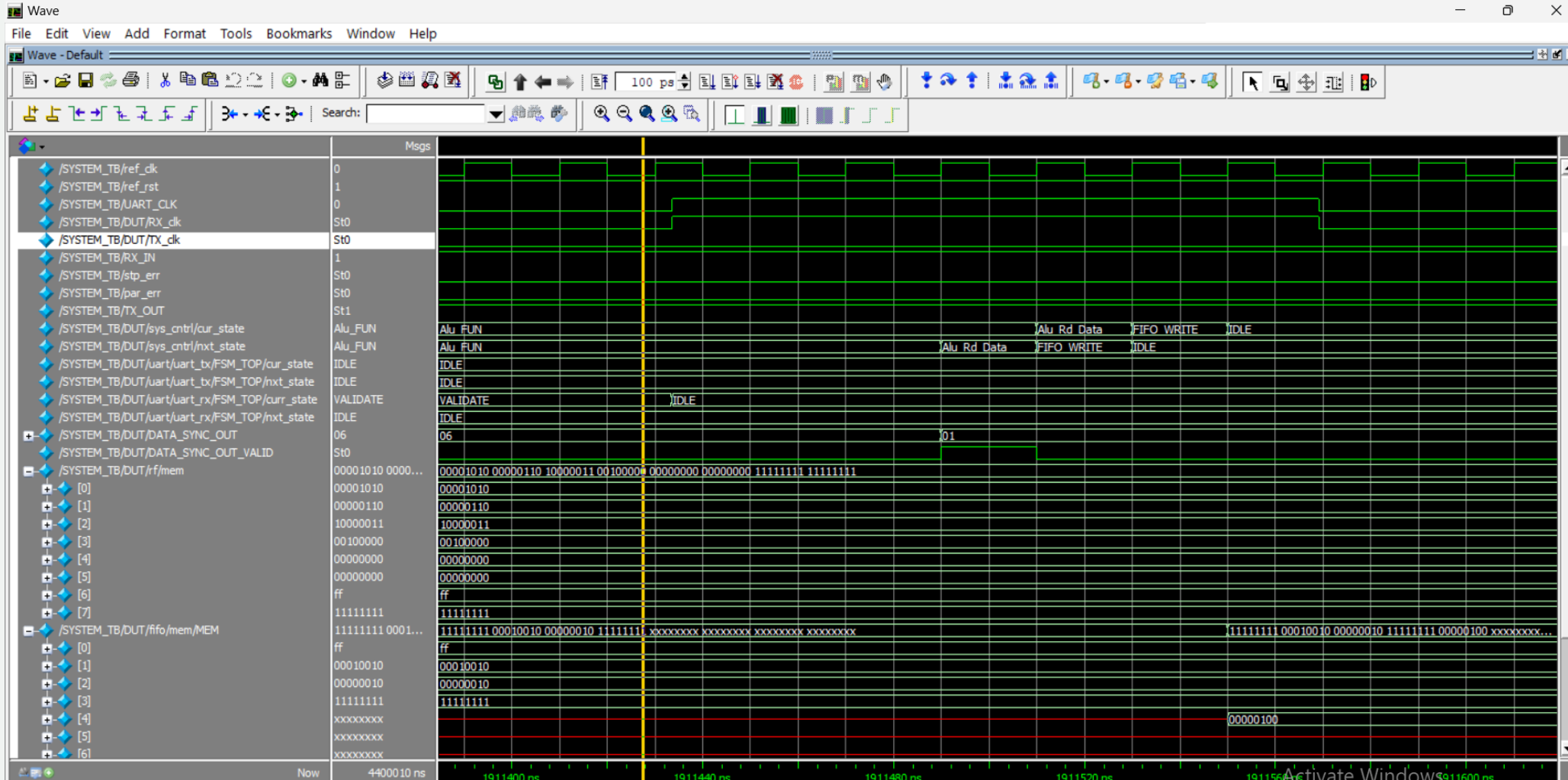
Register File Read Command



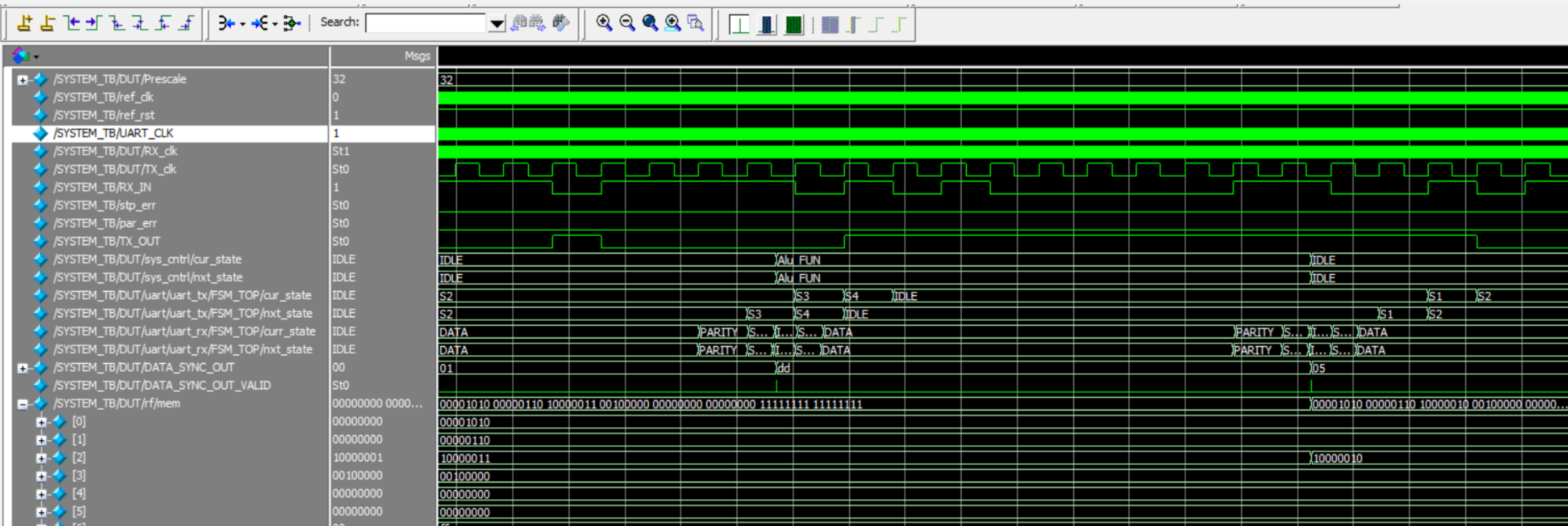
ALU Operation Command



Alu operation Write in fifo

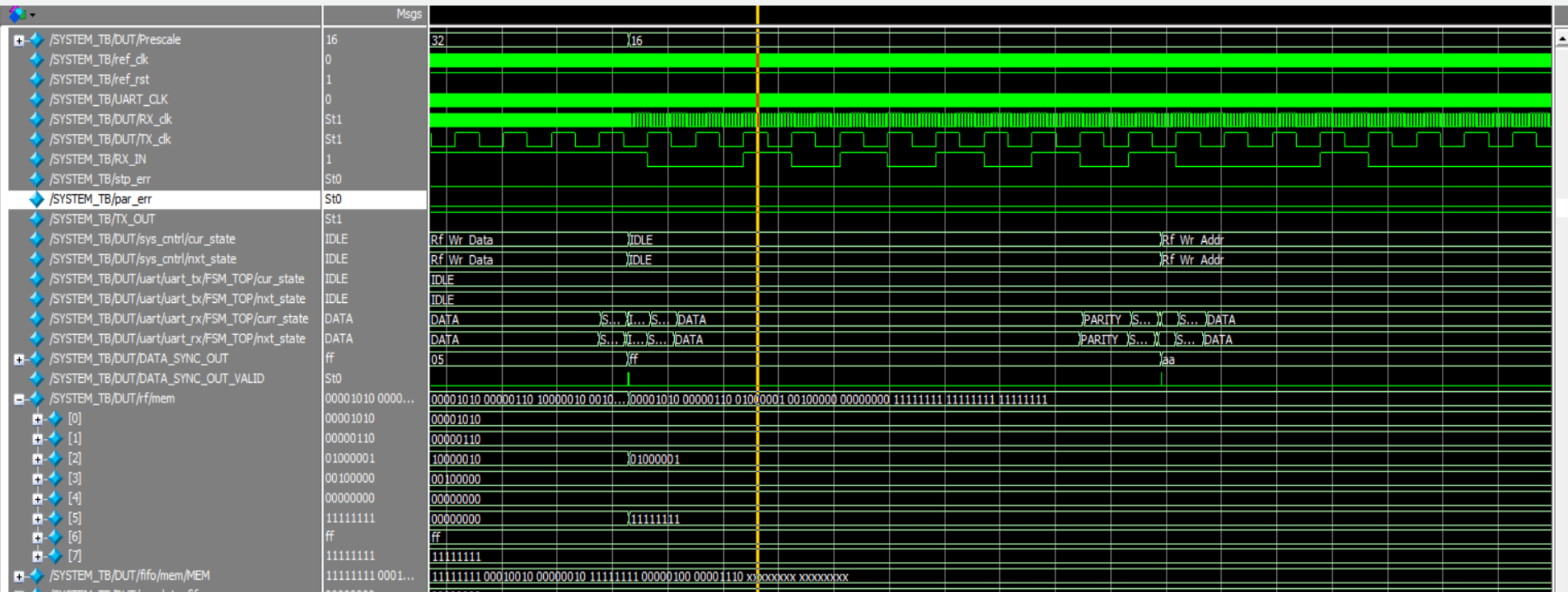


ALU operation (no operands)



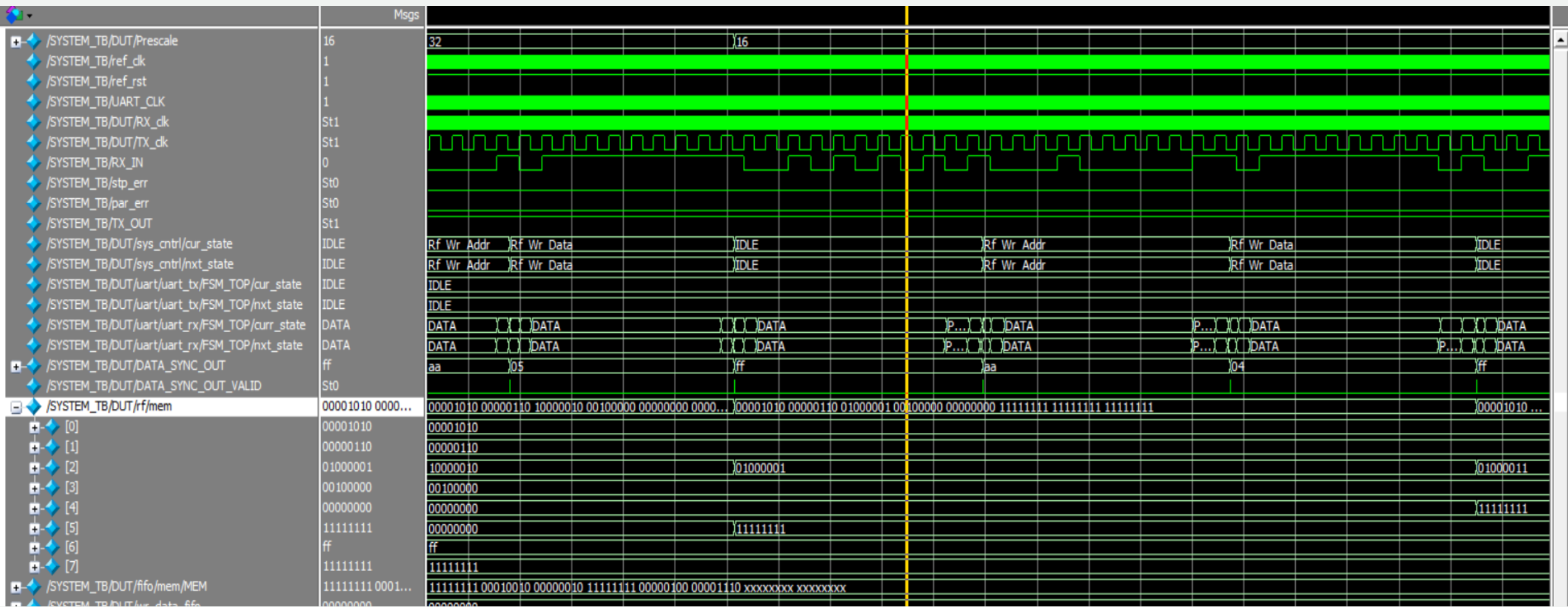
Prescale = 16

Write Command



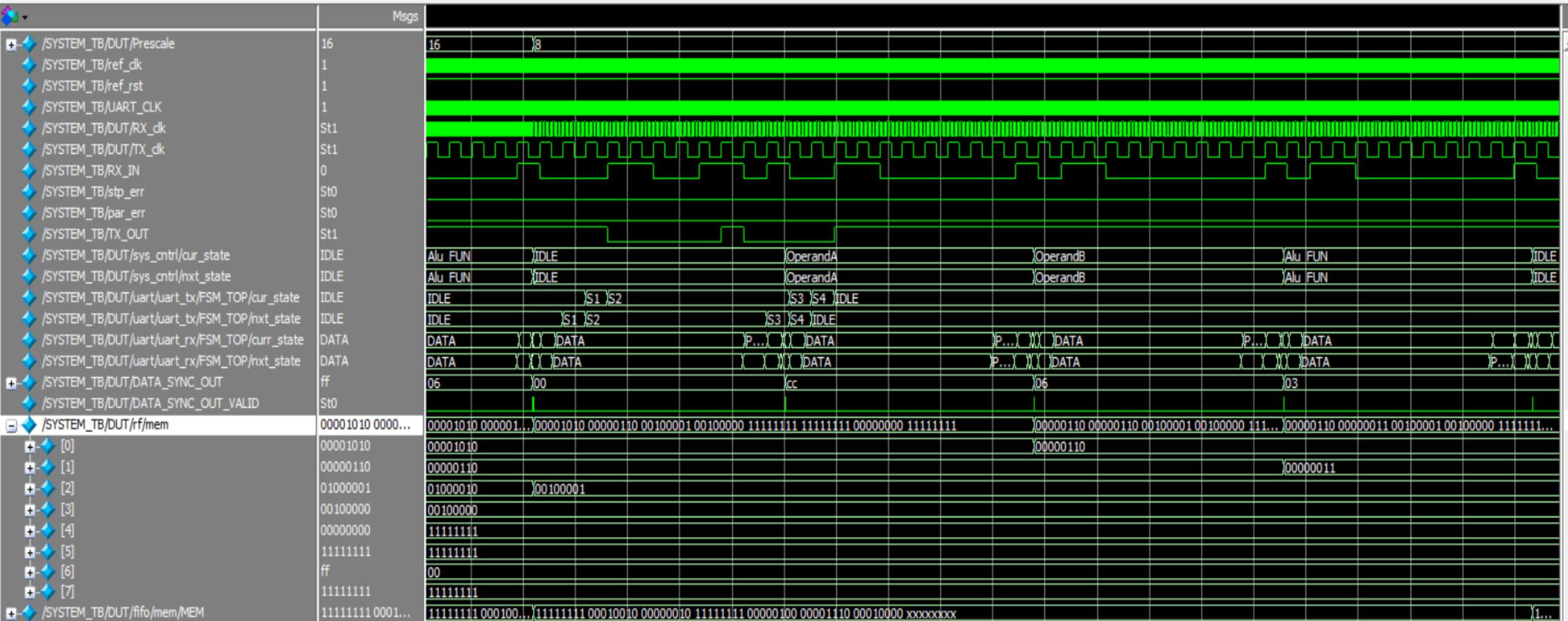
Prescale = 16

Write Command



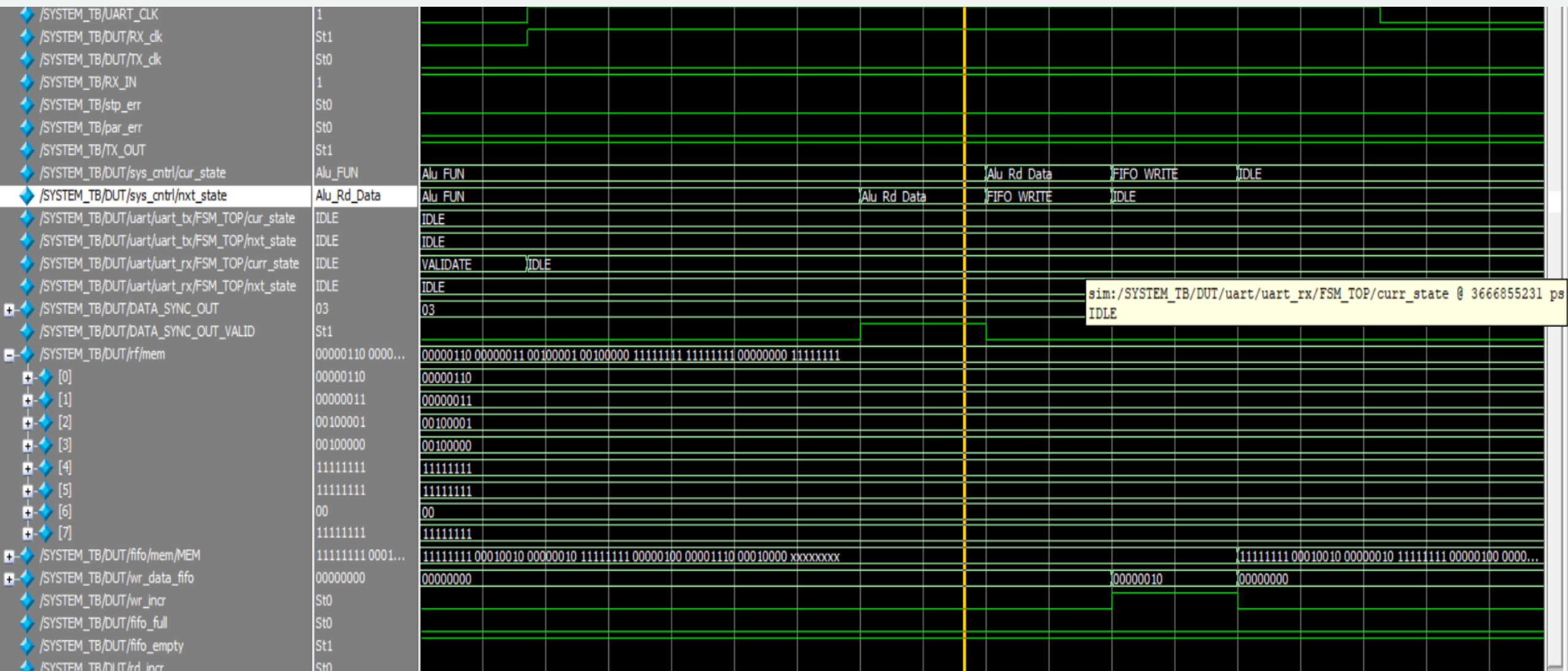
Prescale = 8

ALU Command

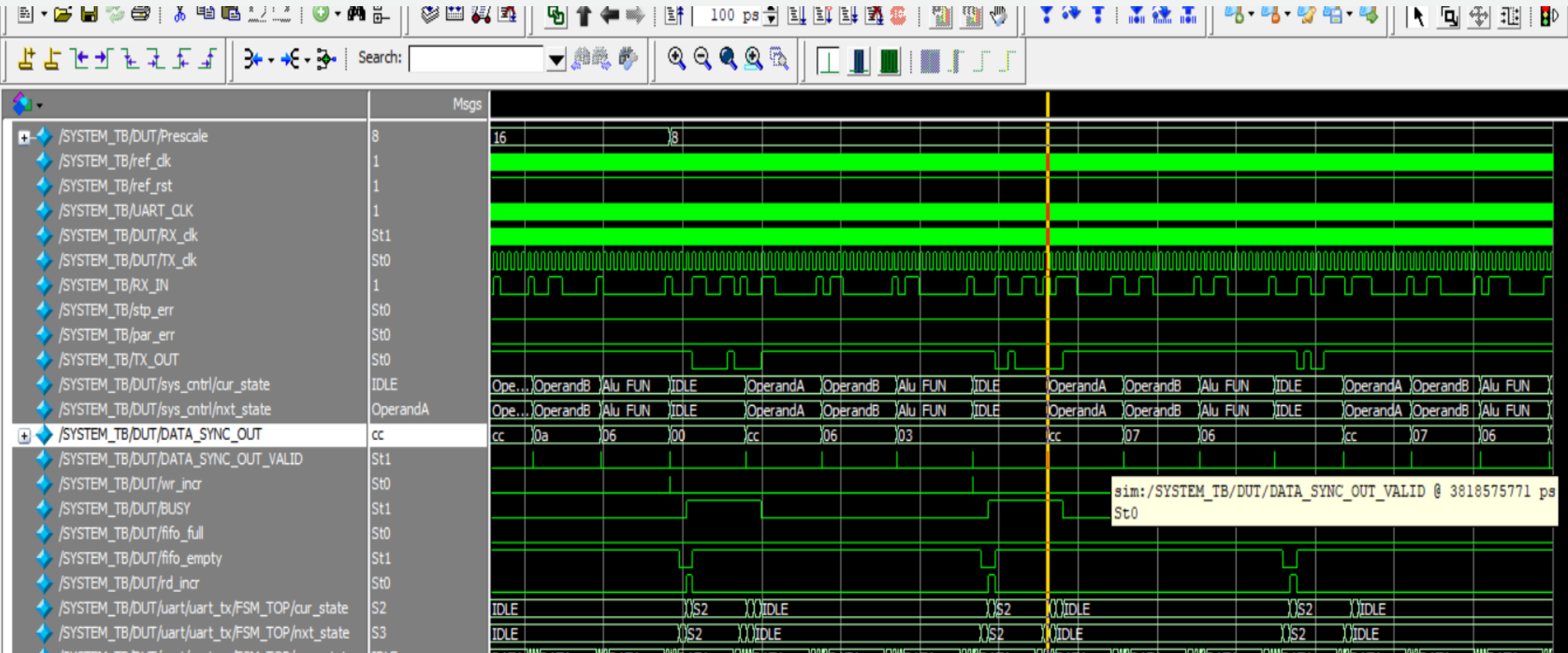


Prescale = 8

ALU Command



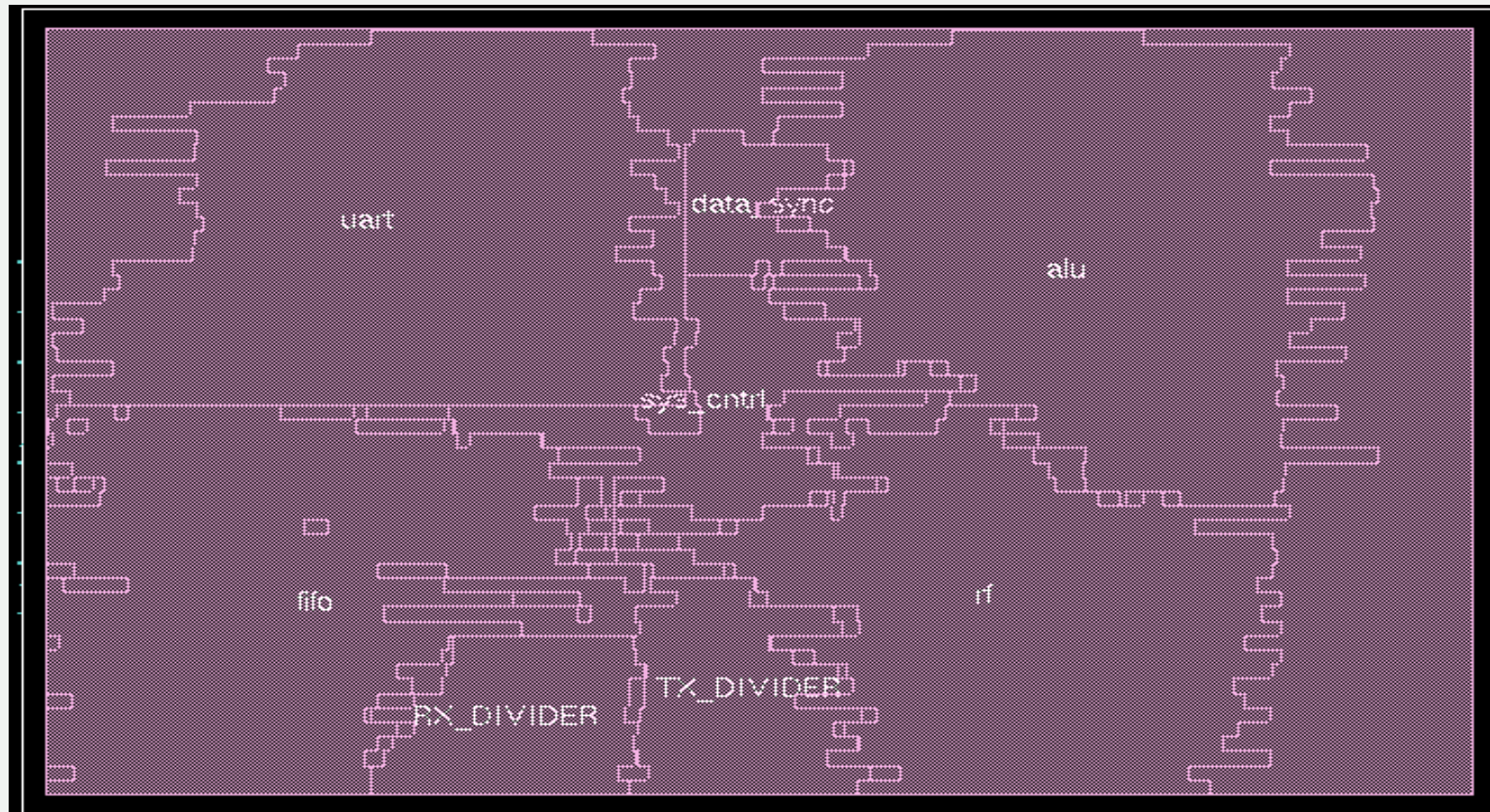
Important Signals



Transcript

```
# write in regfile done
# Read CMD done & write in fifo done
# ALU CMD DONE & write in fifo done (sub succeeded)
# ALU CMD DONE & write in fifo done (or succeeded)
# ***** Prescale=32 Parity Disabled *****
# write in regfile done
# ***** Prescale=32 Parity Enabled par_type Even *****
# write in regfile done
# ***** Prescale=16 Parity Enabled par_type ODD *****
# write in regfile done
# ***** Prescale=16 Parity Disabled *****
# ALU CMD DONE & write in fifo done (add succeeded)
# ***** Prescale= 8 Parity Enabled par_type Even *****
# write in fifo failed , found x and expected 2
# ***** Prescale= 8 Parity Enabled par_type ODD *****
# ALU CMD DONE & write in fifo done (NAND succeeded)
# ***** Prescale= 8 Parity Disabled *****
# ALU CMD DONE & write in fifo done (NOR succeeded)
# Break in Module SYSTEM_TB at SYSTEM_TB.v line 431
# Simulation Breakpoint: Break in Module SYSTEM_TB at SYSTEM_TB.v line 431
# MACRO ./do.txt PAUSED at line 13
# A time value could not be extracted from the current line
```


Placement



Routing

