

<i>Document Name</i>	Requirements of DIO Driver (SRS)
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## 1- Scope of document

This document specifies requirements on the module DIO Driver.

### *1.1 - Constraints*

First scope for specification of requirements on basic software modules are systems which are not safety relevant. For this reason, safety requirements are assigned to medium priority.

## 2- Requirement structure

Each module specific chapter contains a short functional description of the Basic Software Module. Requirements of the same kind within each chapter are grouped under the following headlines (where applicable)

### *2.1 Functional Requirements:*

- Configuration (which elements of the module need to be configurable)
- Initialization
- Normal Operation
- Shutdown Operation
- Fault Operation

### *2.2 Non-Functional Requirements:*

- Timing Requirements
- Resource Usage
- Usability
- Output for other WPs

### 3- Acronyms and abbreviations

<i>Acronyms and abbreviations</i>	<b>Description</b>
DIO channel	Represents a single general-purpose digital input/output pin
DIO Port	Represents several DIO channels that are grouped by hardware (typically controlled by one hardware register). Example: Port A (8 bit)
STD-High	Represent the Bit Value High = 1
STD-Low	Represent the Bit Value Low = 0
Physical Level (Input)	Two states possible: LOW/HIGH. A bit value '0' represents a LOW, a bit value '1' represents a HIGH.
Physical Level (Output)	Two states possible: LOW/HIGH. A bit value '0' represents a LOW, a bit value '1' represents a HIGH.
U8	Unsigned Character Data Type of size (8-Bits)

### 4- Functional Overview

The DIO driver provides port and channel based read and write access to the internal general purpose I/O ports. The read and write behavior is unbuffered. The basic behavior of this driver is synchronous.

<i>Expression</i>	<i>Explanation</i>
<b>DIO Channel</b>	Represents a single general-purpose digital input/output pin
<b>DIO Port</b>	Represents multiple DIO channels that are grouped by hardware and accessible synchronously (typically controlled by one hardware register). Example: Port A (8 bit)

## 5- Functional Specification

### 5.1- Functional Requirements

#### 5.1.1- Configuration and Initialization

Symbolic names shall be configured

<b>Type</b>	Valid
<b>Description</b>	The DIO driver shall allow the static configuration of the following symbolic names: <ul style="list-style-type: none"> <li>• DIO channel names</li> <li>• DIO port names</li> </ul>
<b>Rationale</b>	Provide human readable symbolic names for DIO channels
<b>Use Case</b>	None
<b>Dependencies</b>	None

#### 5.1.1- Normal operation

<b>Function Name</b>	MCAL_DIO_u8_SetPortDirection
<b>Description</b>	The DIO Driver shall provide a service for setting the direction of a DIO Port by writing data word to the assigned port (Input/Output), without changing physical level. (Ex. DDRA)
<b>Rationale</b>	Basic functionality
<b>Dependencies</b>	General write behavior
<b>Use Case</b>	Write access to an entire DIO port

<b>Function Name</b>	MCAL_DIO_u8_SetPinDirection
<b>Description</b>	The DIO Driver shall provide a service for setting the direction of a single DIO channel by writing the value to the assigned pin (Input/Output) , without changing physical level(High/Low).
<b>Rationale</b>	Efficient handling of single DIO channels
<b>Dependencies</b>	General write behavior using data shift
<b>Use Case</b>	Write access to a particular DIO channel (port pin)

<b>Function Name</b>	MCAL_DIO_u8_SetPortValue
<b>Description</b>	The DIO Driver shall provide a service for setting the value of a DIO Port by writing data word to the assigned port by changing physical level (High / Low). (Ex. PORTA)
<b>Rationale</b>	Basic functionality
<b>Dependencies</b>	General write behavior
<b>Use Case</b>	Write access to an entire DIO port

<b>Function Name</b>	MCAL_DIO_u8_SetPinValue
<b>Description</b>	The DIO Driver shall provide a service for setting the value of a single DIO channel by writing the data to the assigned pin by changing physical level (High / Low).
<b>Rationale</b>	Efficient handling of single DIO channels
<b>Dependencies</b>	General write behavior using data shift
<b>Use Case</b>	Write access to a particular DIO channel (port pin)

<b>Function Name</b>	MCAL_DIO_u8_GetPinValue
<b>Description</b>	The DIO Driver shall provide a service for reading one bit value of an assigned DIO channel (specific port pin). The operation shall be unbuffered. There shall be no influence to the output functionality of the port.
<b>Rationale</b>	Basic functionality
<b>Dependencies</b>	General read/write behavior
<b>Use Case</b>	Read access to an entire DIO port

<b>Function Name</b>	MCAL_DIO_u8_TogglePinValue
<b>Description</b>	The DIO Driver shall provide a service to flip (change state from 1 to 0 or from 0 to 1) one bit of an assigned DIO channel (specific port pin)
<b>Rationale</b>	Write access to a particular DIO channel using shift data.
<b>Dependencies</b>	General read/write behavior
<b>Use Case</b>	Write access to an entire DIO port

## 5.2 *Non-Functional Requirements*

<b>Type</b>	Valid
<b>Description</b>	All re-entrant functions of the DIO Driver shall perform the following access actions in an atomic way <ul style="list-style-type: none"><li>• DIO ports</li><li>• DIO channels</li></ul>
<b>rationale</b>	Avoid data integrity problems within concurrent access of DIO Driver API functions
<b>Use Case</b>	A specific microcontroller (or a specific compiler) does not provide atomic access to single port pins. For that reason, the implementation has to use read-modify-write operations on the whole port. Concurrent access to pins of the same port will lead to data integrity problems if concurrent access is not blocked
<b>Dependencies</b>	None
<b>Supporting Material</b>	None