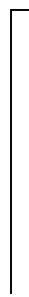




OUT DATA

A thin black vertical line with a small arrowhead pointing upwards at the top.

from outside the processor



In Data











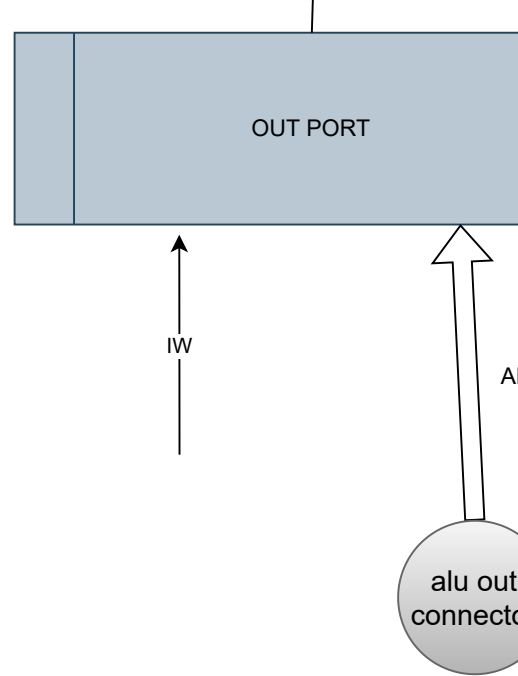








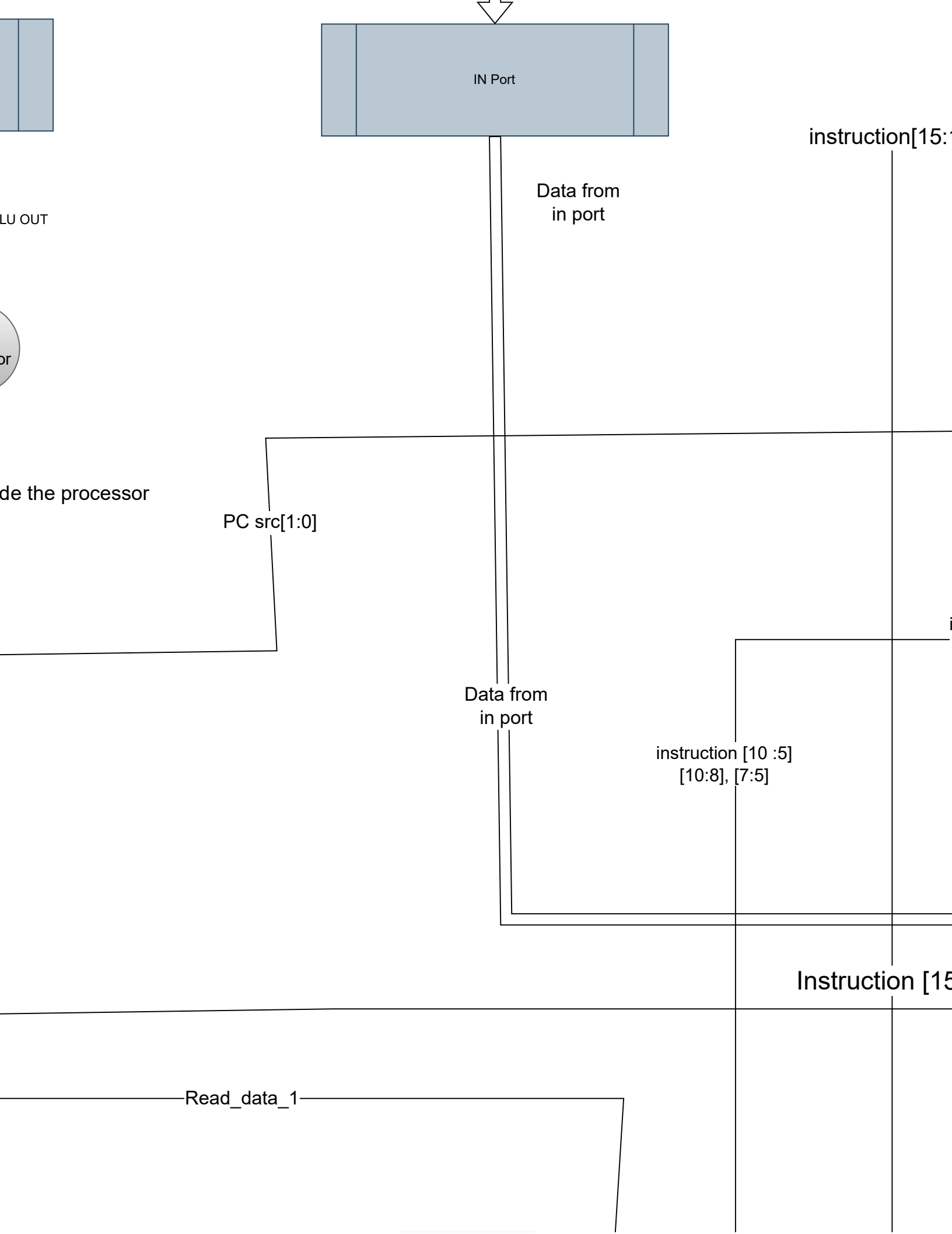
Read\_data\_1

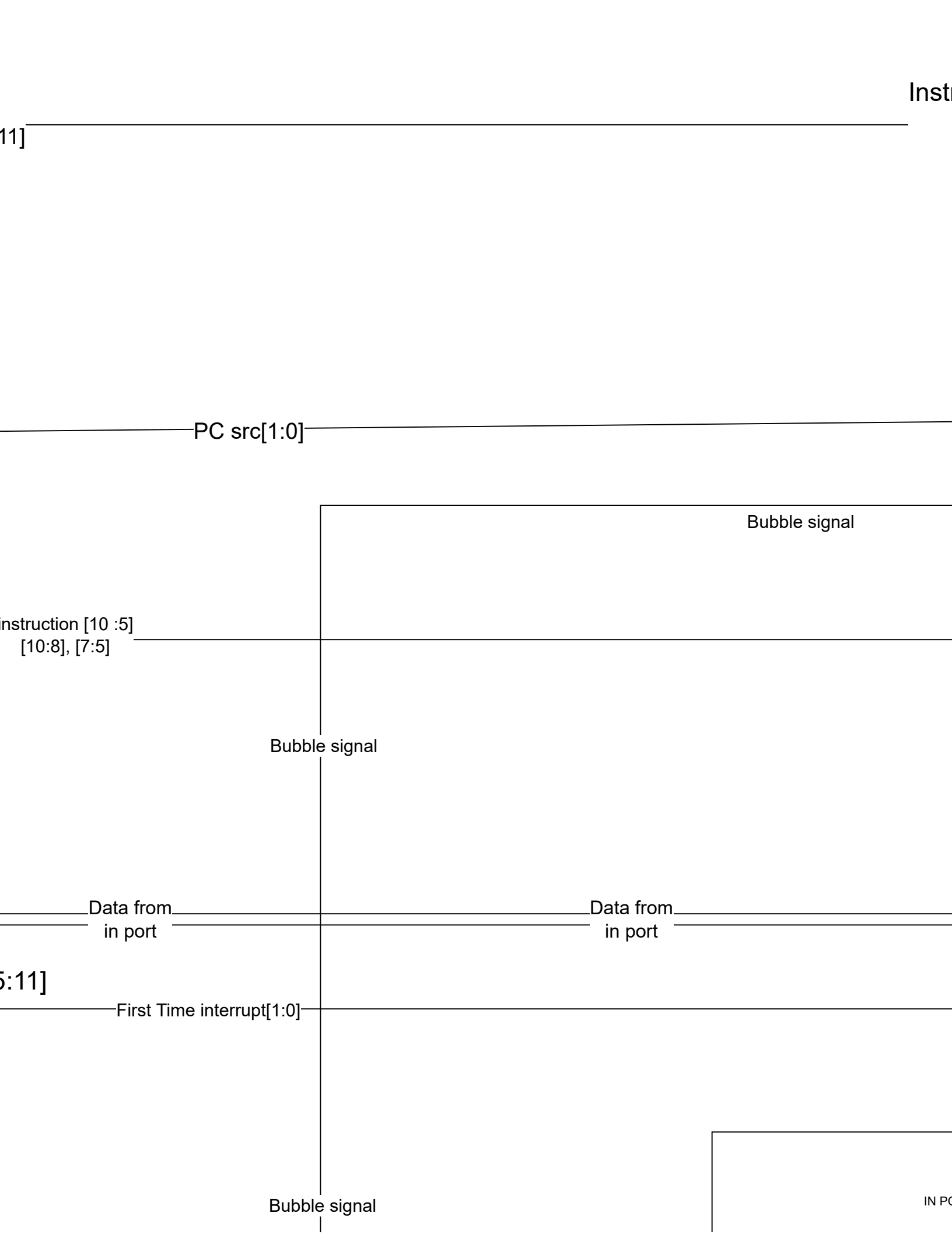


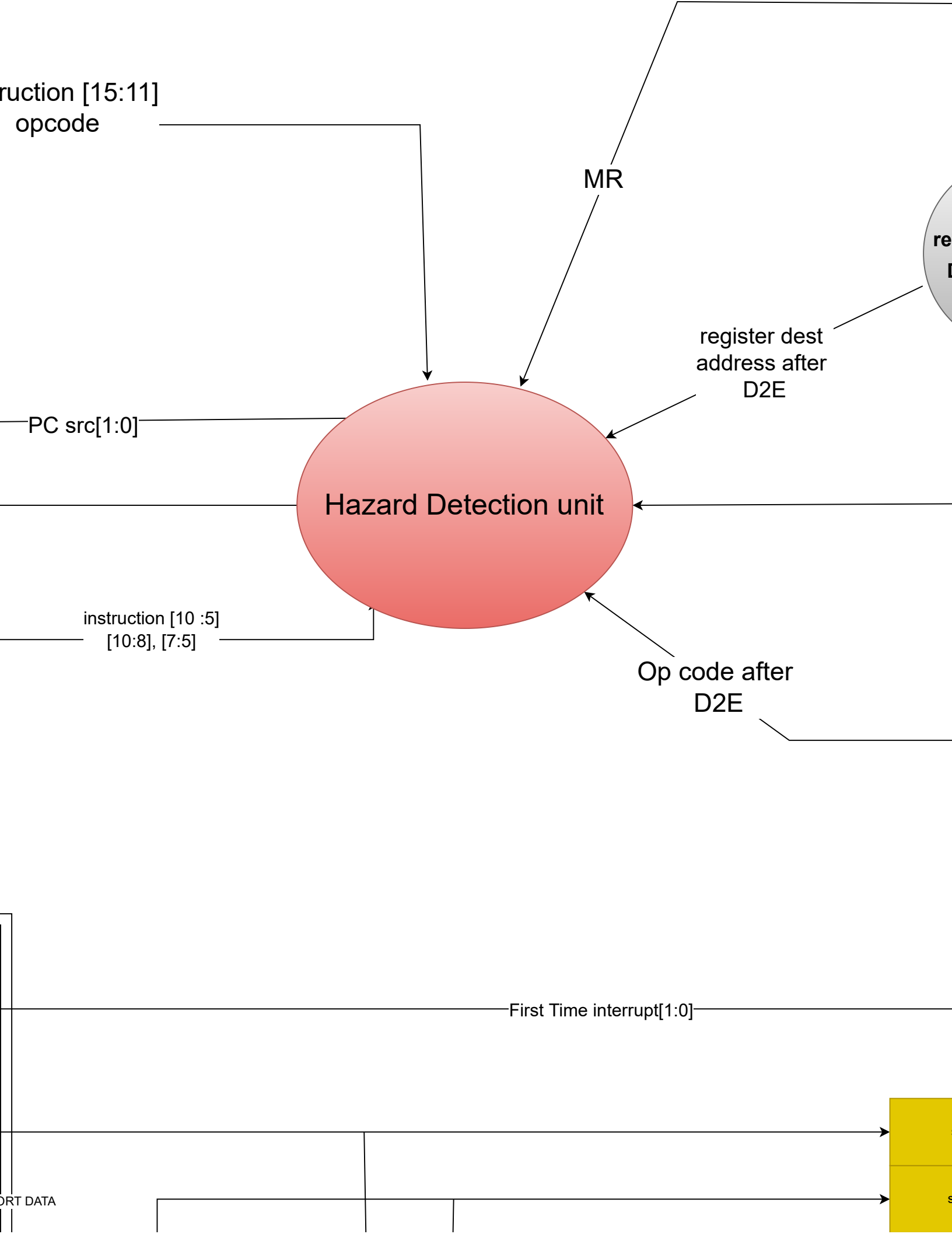
outsi

PC src[1:0]

First Time interrupt[1:0]







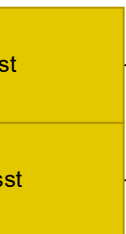




CCR

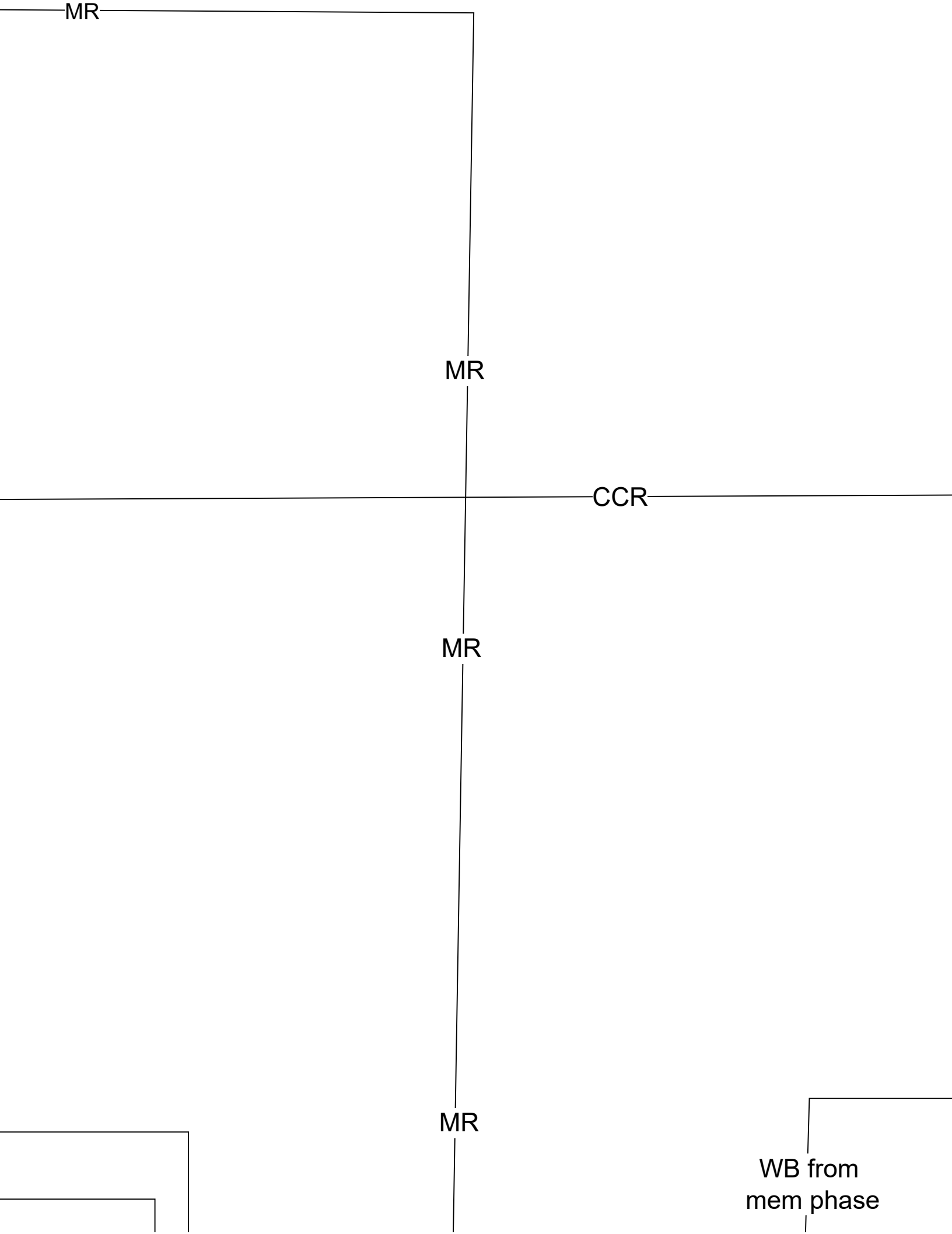
Op code after  
D2E

First Time interrupt[1:0]



st

sst





CCR

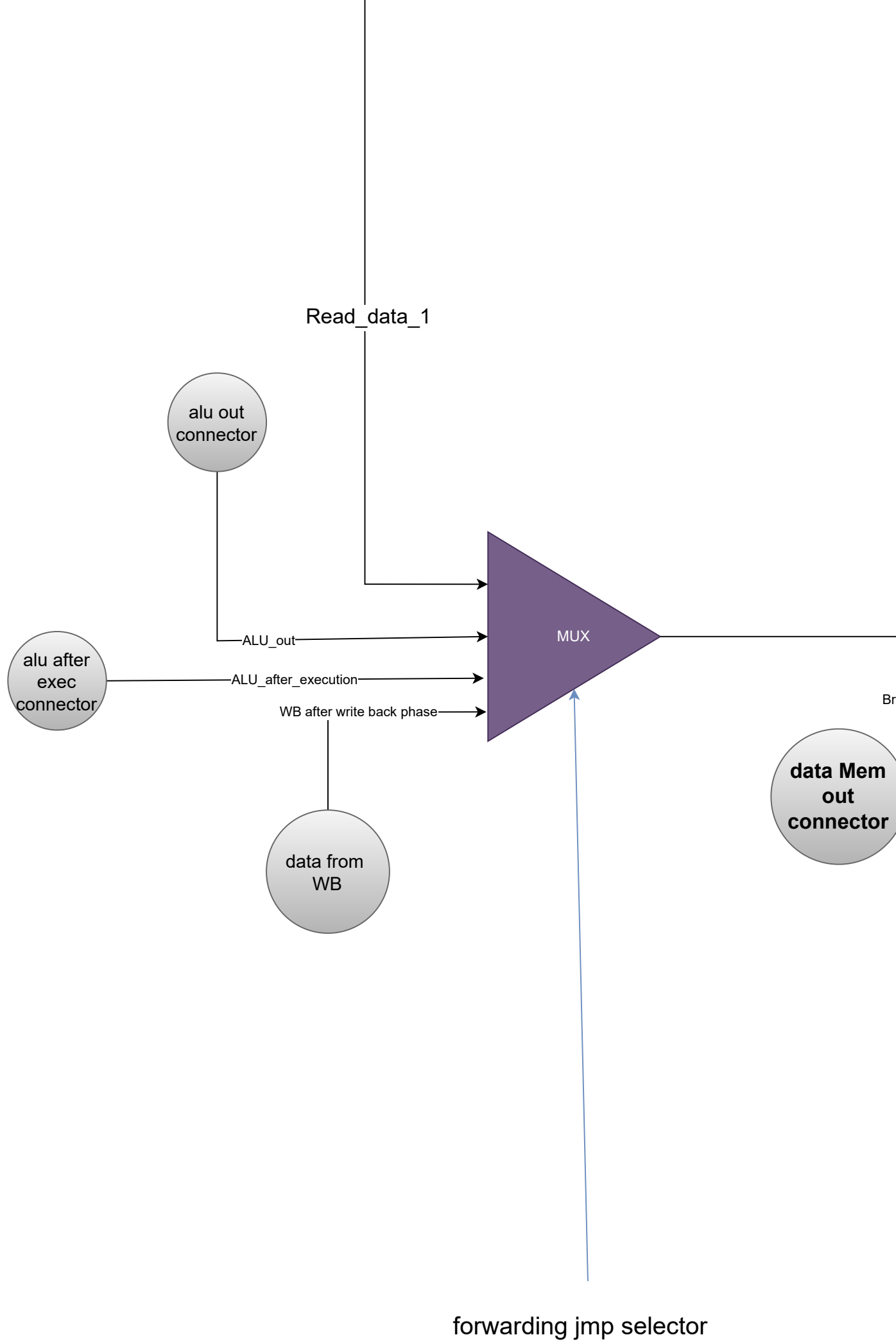
WB from  
mem phase

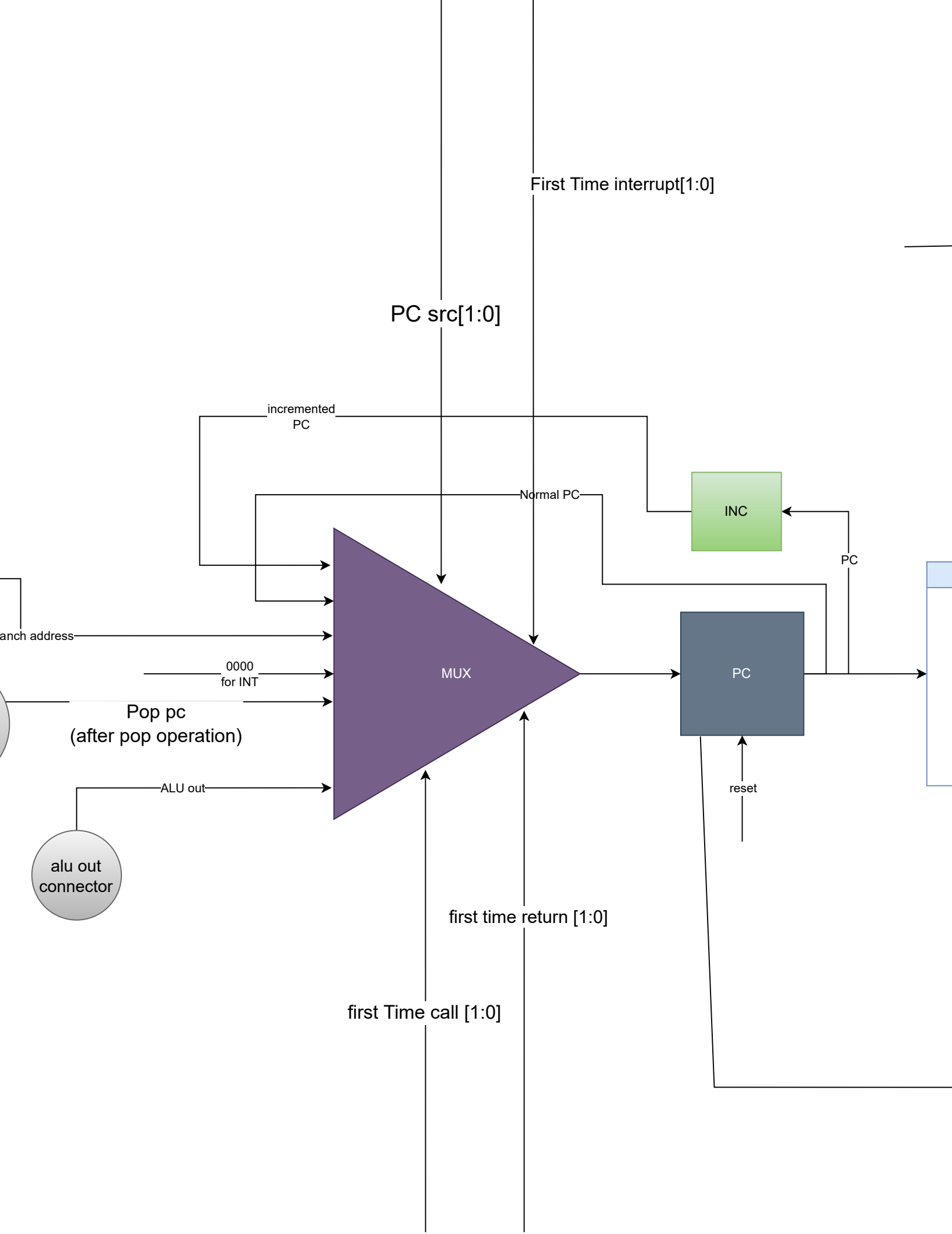
WB from  
mem phase



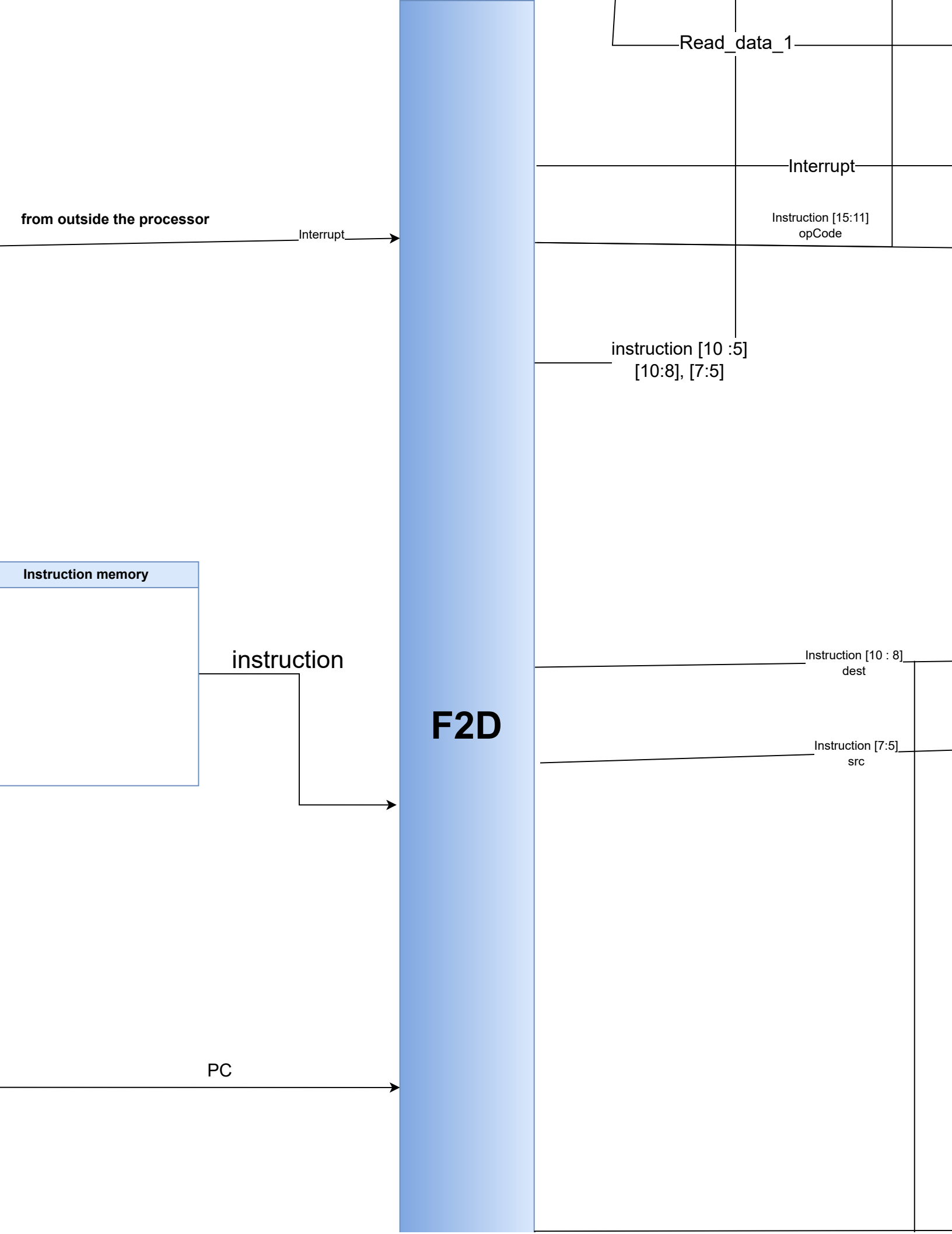


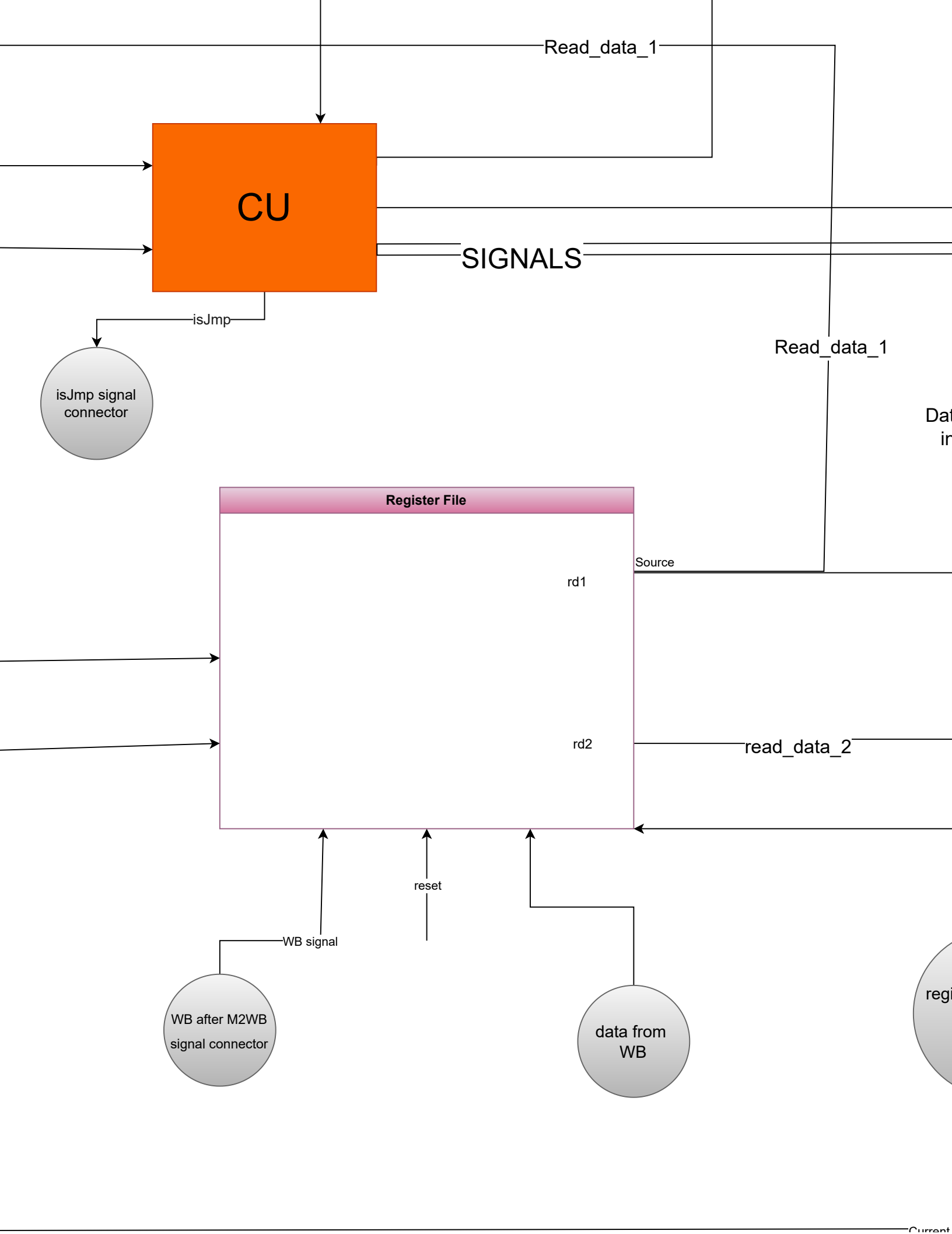


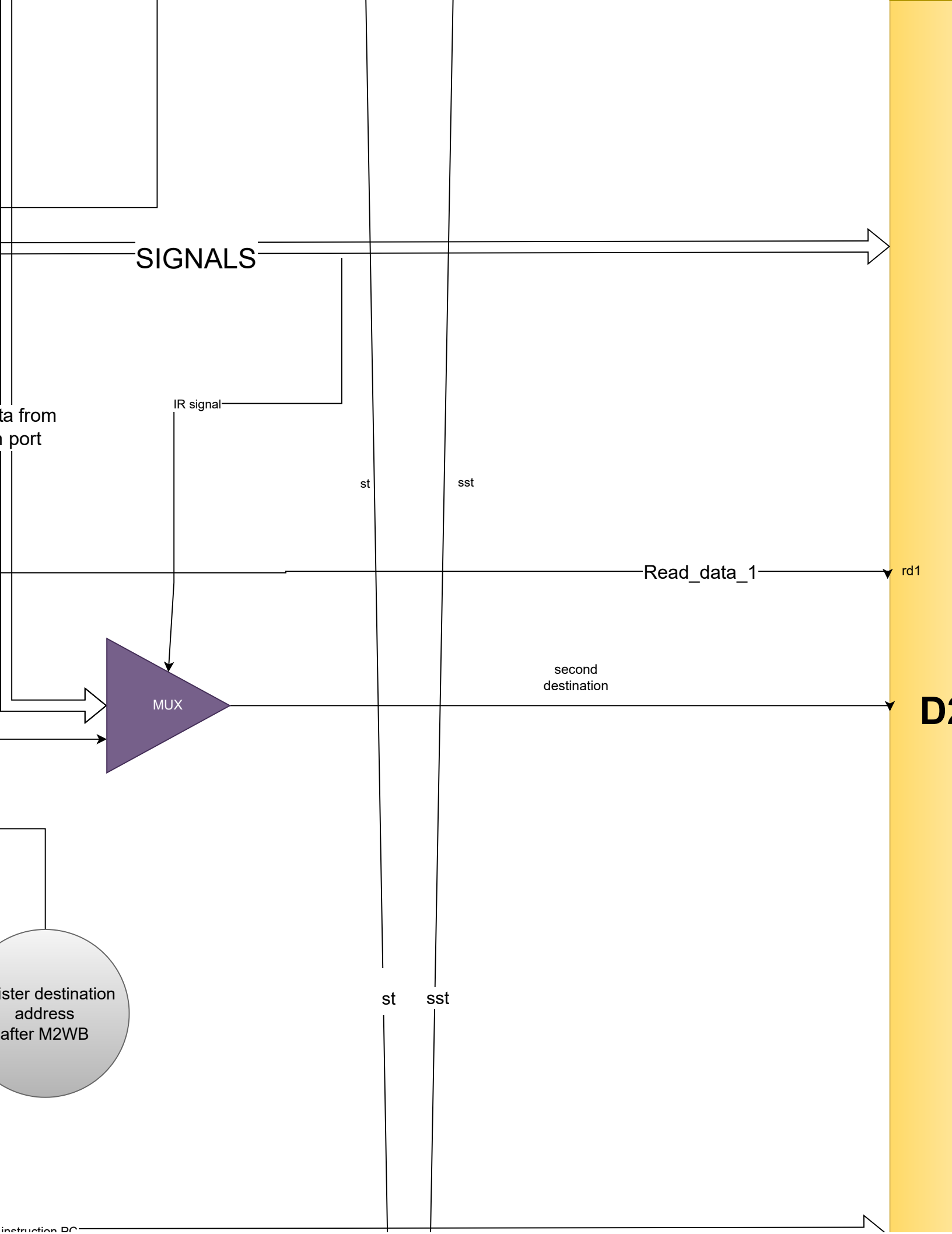












SIGNALS

Op code after  
D2E

alu after  
exec  
connector

alu after  
execution

first operand  
from reg file

forwarded  
memory

memory data  
connector

read data 2

instr[4:0]

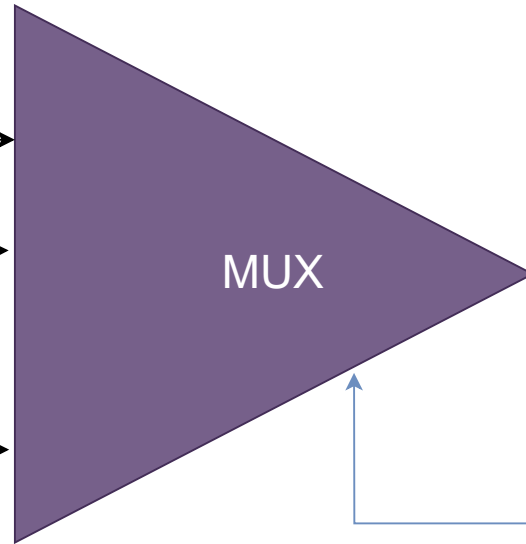
forwarded memory

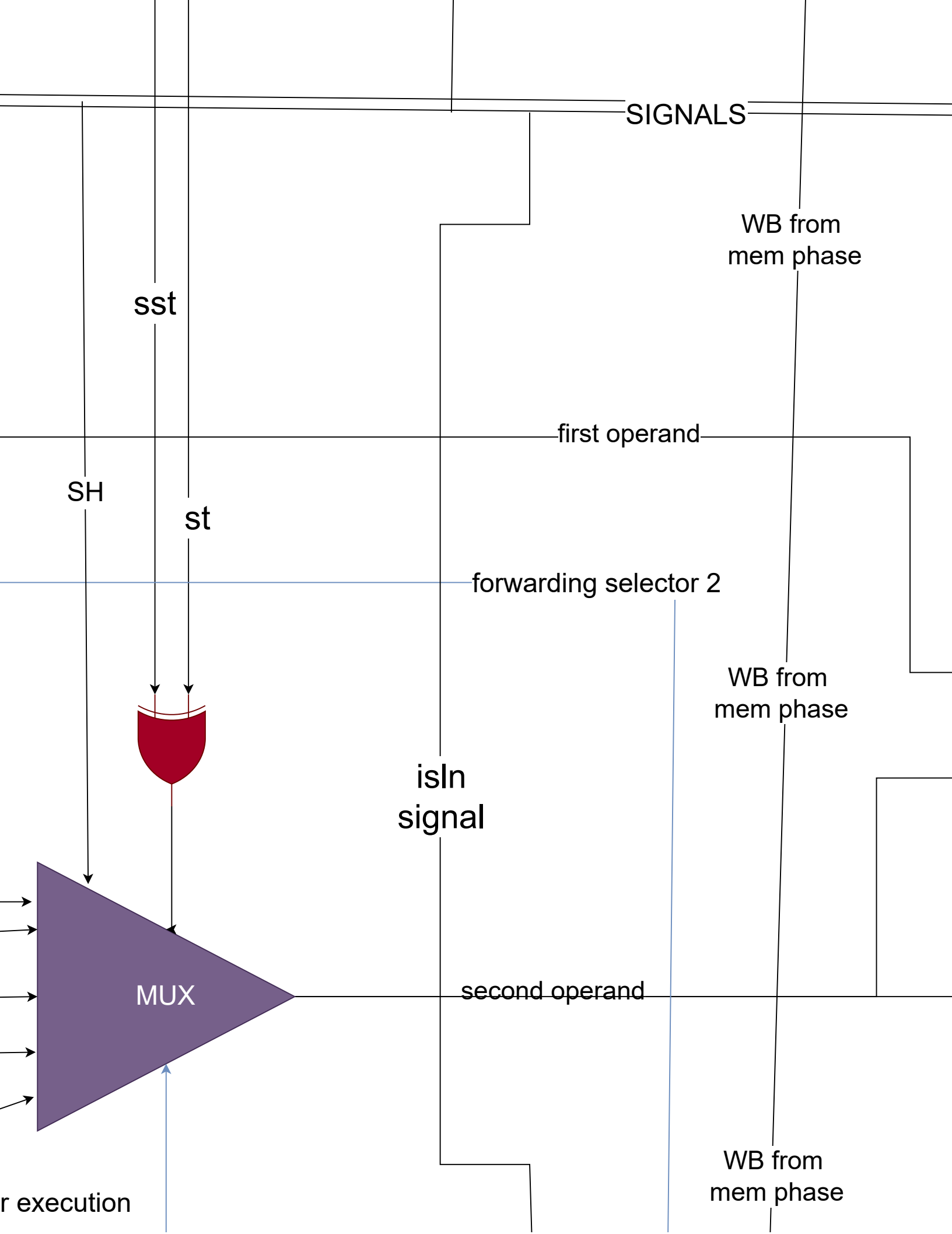
instruction or  
immediate

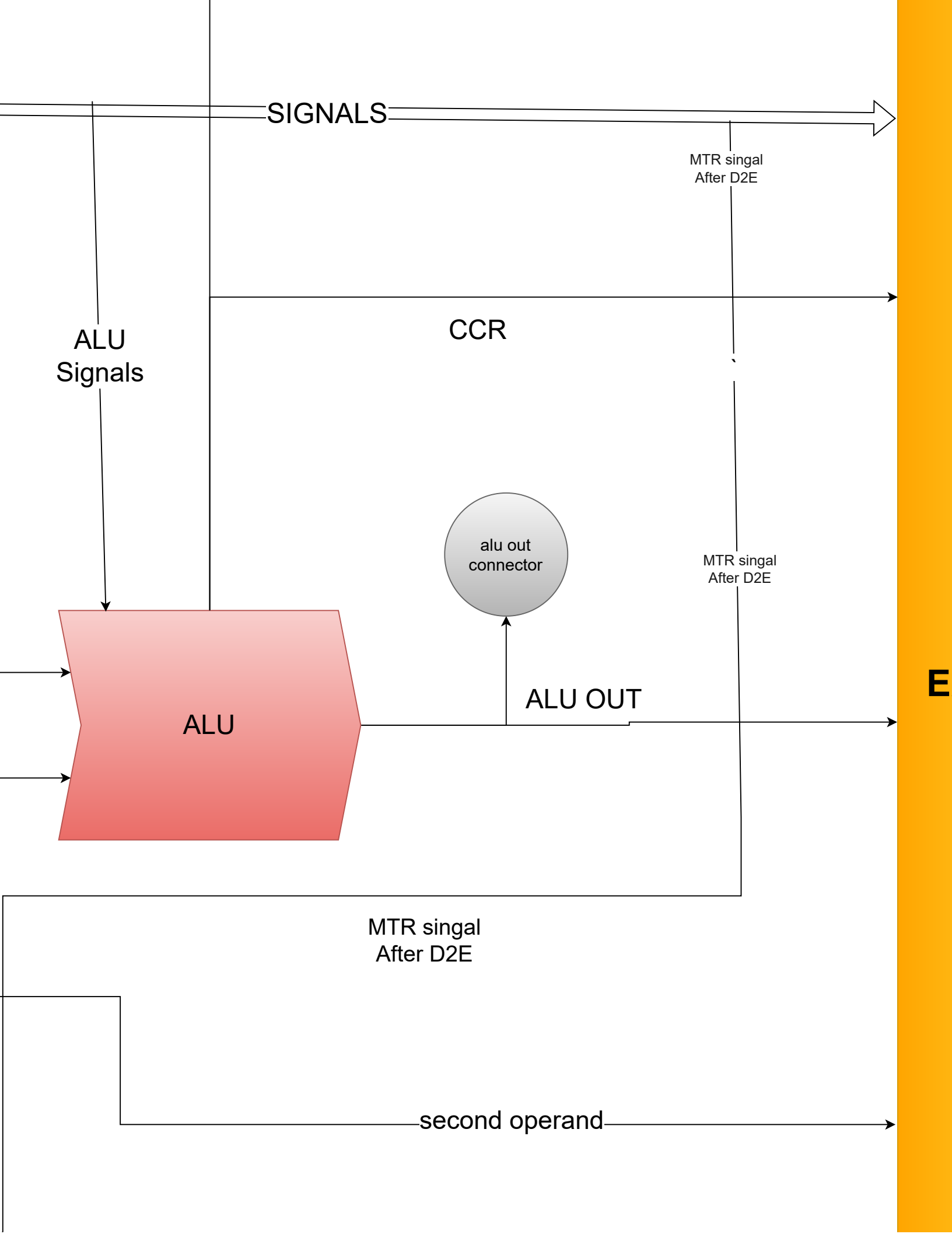
alu after  
exec  
connector

alu after

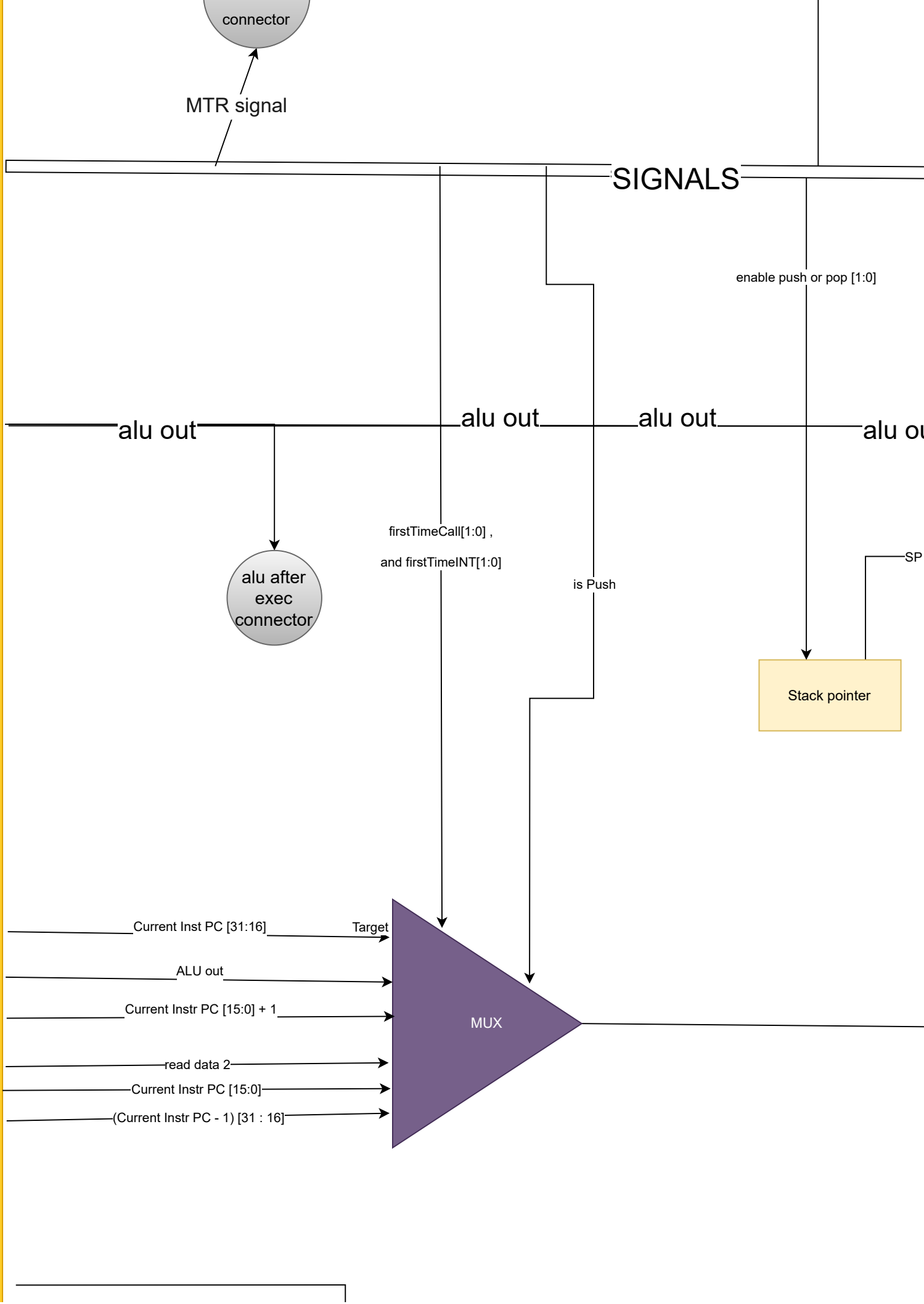
2E

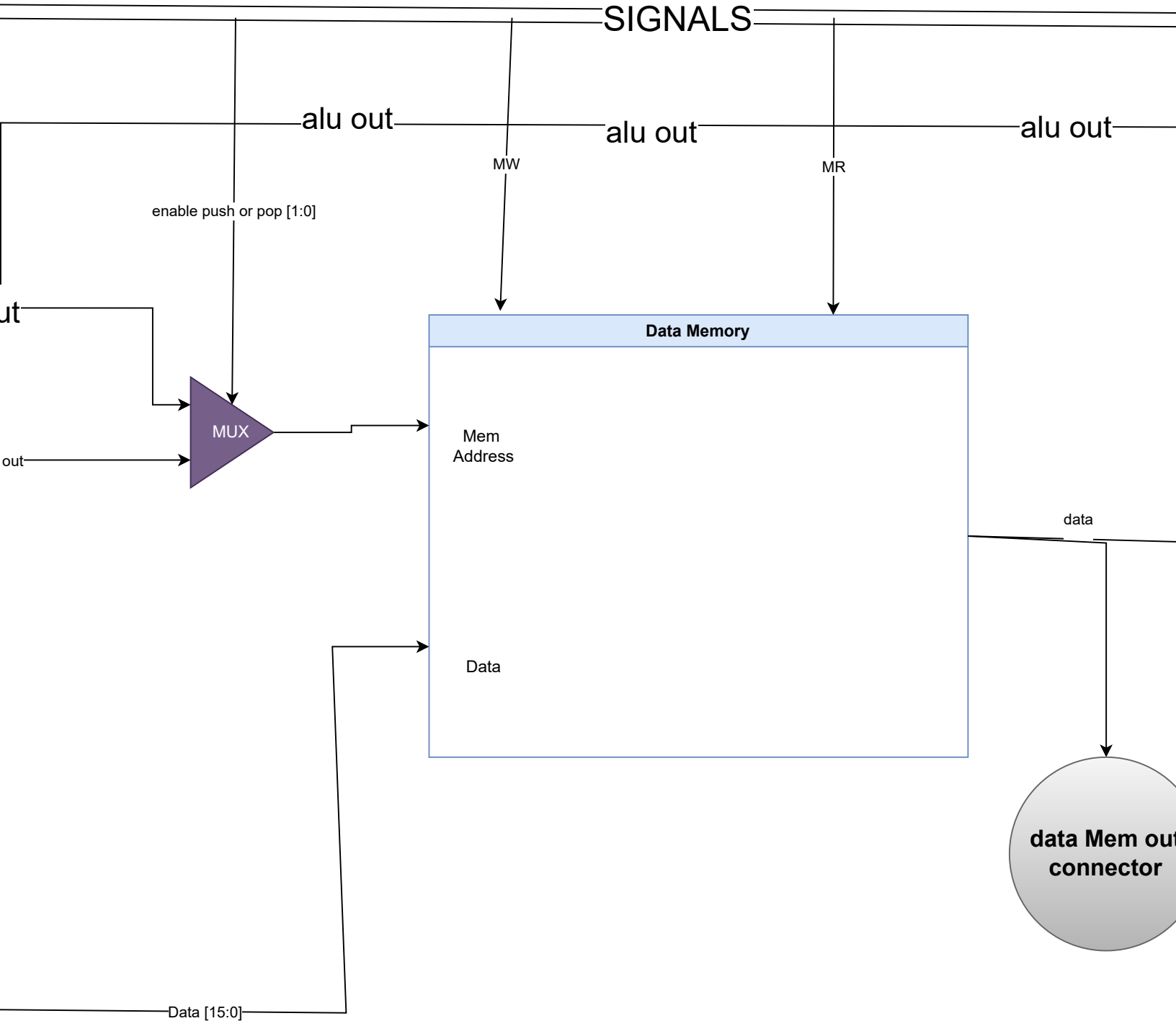




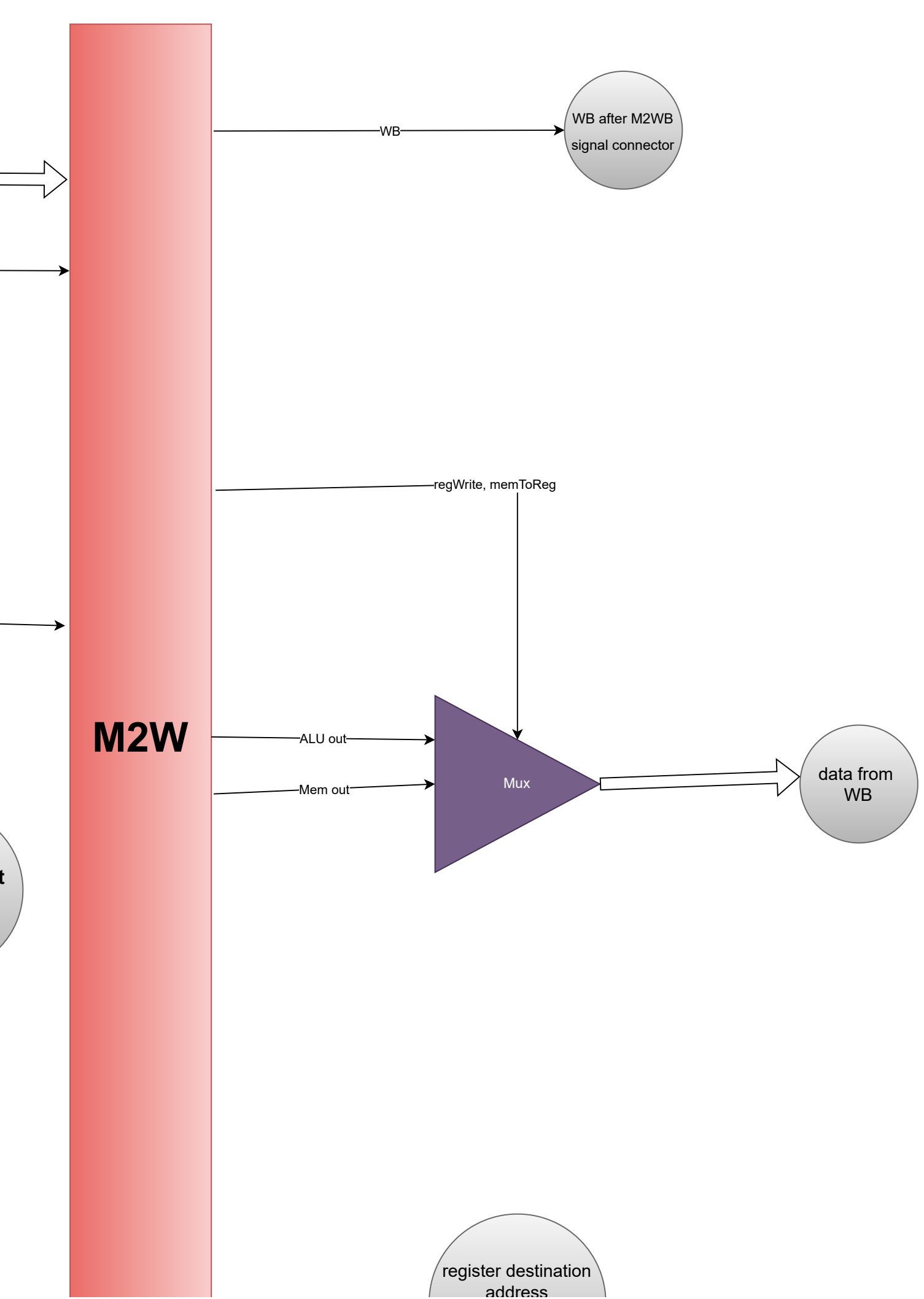


2M











first Time call [1:0]

forwarding jmp selector

first time return [1:0]



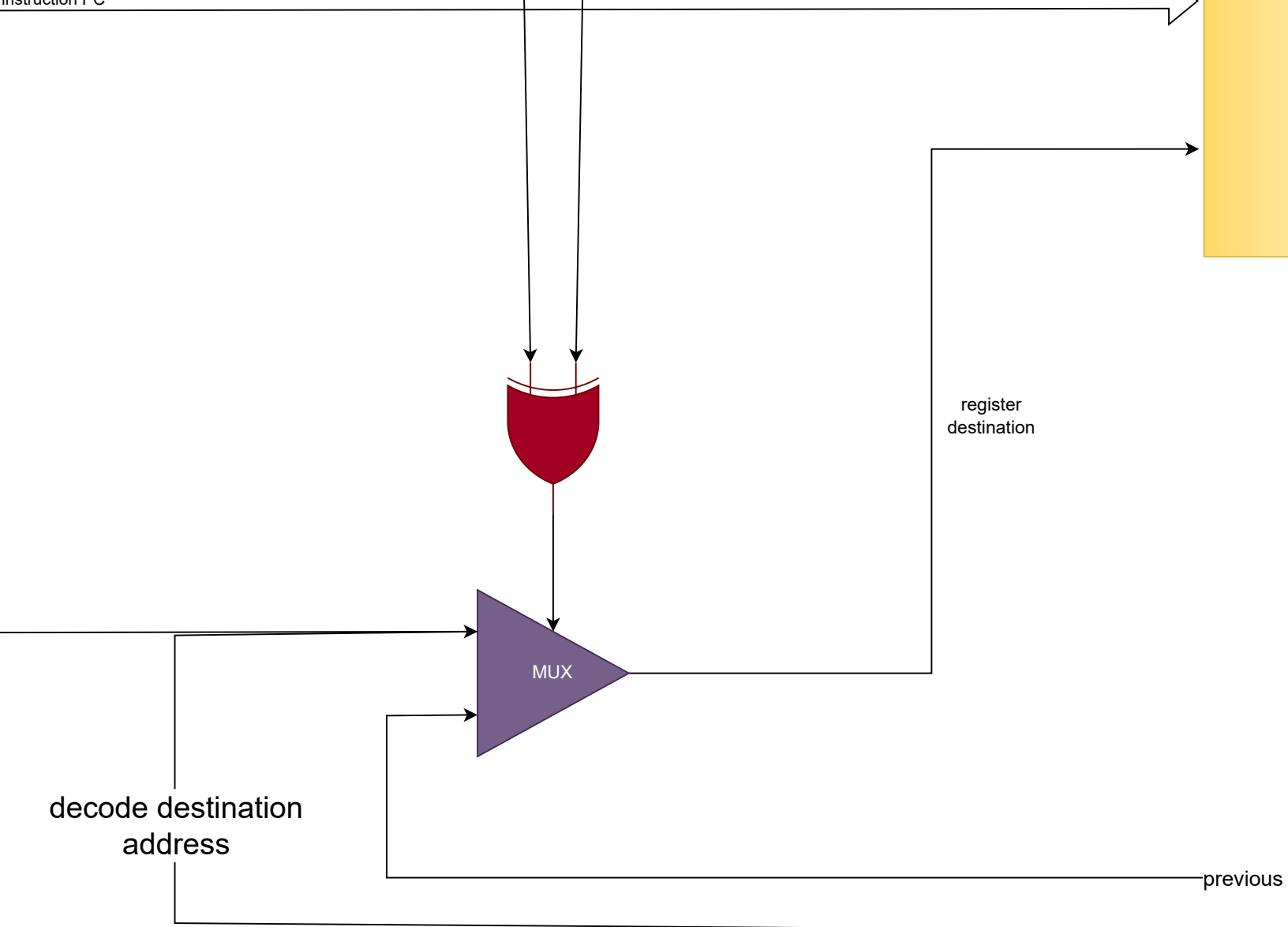
first time return [1:0]

Register destination

first Time call [1:0]

forwarding jmp selector

first time return [1:0]



forw

first time return [1:0]

