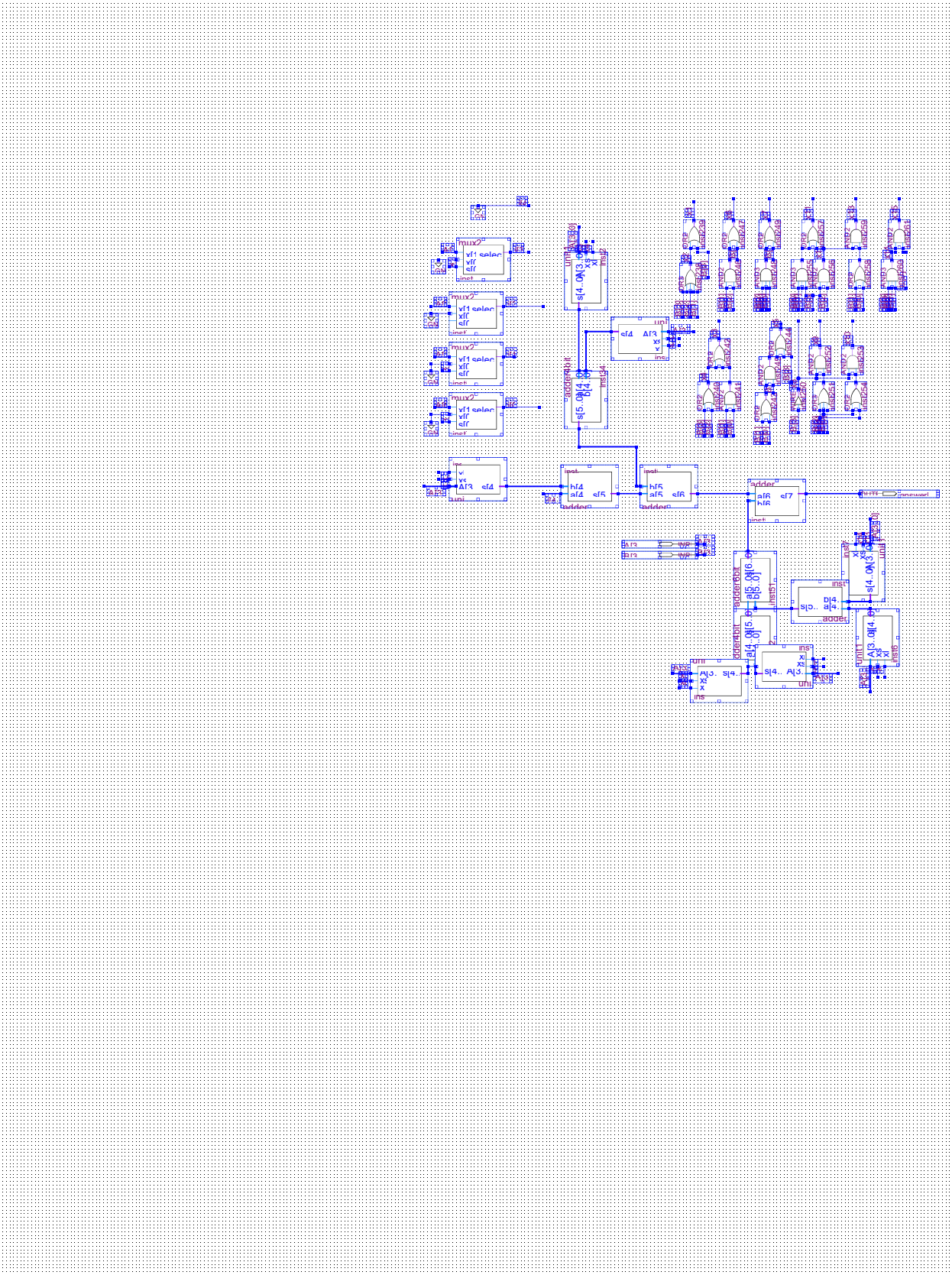
mux2
vf1 selec
xf1
xf2
inst
adder4bit
s[4..0]A[3..0]
s[5..0]A[4..0]
s[4..0]A[5..0]
s[4..0]A[3..0]
s[5..0]A[6..0]
s[4..0]A[5..0]
s[4..0]A[3..0]
s[4..0]A[3..0]
s[4..0]A[3