

School of Information Technology & Engineering (SITE) Programme: MCA

Course: Foundations of Data Science (MAT5010)
Digital Assignment 1
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Project Title: Hardware Accelerators in Supercomputing

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SUPERLIAMENTING

Hardware acceleration are important components in imperiompting because they can greatly improve the performance of certain types of computations. Supercompters are used for compten and computationally intensive tasks much as weather forcasting, underson modeling, and data analytics, among others. There tasks involve large amounts of data and require a reprisicant amount of processing power, which can be achieved through hardware accelerations.

Hardwere accelerations are specialized processors that one designed to perform specific tasks more efficiently than traditional CPUs. They can be used to speed up tasks such as matrix multiplication, data compression, and marchine herving, among others. By offloading these tasks of bandwere acceleration, supercompleis can achieve higher performance. and faster processing times.

One popular type of handwore accelerations used in repercomposing is the graphics processing writ (CrPV).

CrPUs were originally designed for rendering graphics in sideo games but have since been adapted for general-purpose computing. CrPUs can perform many computations in parallely which makes them well-swited for tasks such as madrine learning and data analytics.

Another type of handwere accelerator used in supercomputing in field-programmable gate array (FairA). Fer PAs are programmable logic denices that can be configured to perform a side variety of computations. They are part wanty useful for tables that require how laterny and high through pot, such as real-time data processing.

Overall, hardware accelerators are an important component of supercompiting because they allow for faster processing times and higher partormence. By leveraging the capabilities of hardware accelerators, supercompiters can table more complex and compitationally intensine tasks, which is essential for advancing research and solving real-world problems.

HOW WILL NORMAL COMPUTER BEHAVE IF WE USE HARDWARE ACCELBRATOR AS PLULIN?

It you add a hardware accelerator physin to a normal complex, it can greatly improve its performance in certain tasks. Hardware acceleration are specialised processors that are designed to perform specific tasks, much as graphic sendendering, madethe learning among others.

WHAT APPLICATIONS BENEFIT FROM HARDWARE

ACCELERATION

Hardware accelerations can benefit a vide range of applications by improving performance and efficiency. Some applications that can benefit from hardware accelerators include-

17 Artificial Intelligence | Marchine Learning

Accelerators like Copus and Thus can speed up the training and inference processes of ML algorithms.

27 Video Encoding / Decoding

Hordware acceleration like Quick Sync Video and NVENC can speed up the video encoding and deading process, making it faster and efficient.

37 Cuptography - Ha

Like TPMCTrusted Platform Moderes can speed up crytographic operations like encryption and decryption

- Ar Craming trouplies processing units Control can aciderate graphics rendering and improve overall gaming performance.
- SY Scientific composting Handware accelerators like FURAS can accelerate scientific compilting applications such as similations and modeling.

Hardware Accelerations can also improve in many day-to-day applications such as-

17 Amage and Video Editing

Applications like Adabe Mostorhoog, Priemere Pro, and After Effects use Capus to accelerate the sendering of images and videos.

27 Video Conferencing

Applications like 200m, hoogle Meet, and Microsoft Teams use Mandware accelerations to improve the quality and performance of mides and audio during video calls.

3. Web mouning -

Web browsers like choogle Chrome and Firefor use hardwore accelerations to speed up the rendering of web pages, especially those with complen graphics and emmation.

4. Voice Amistants

Hardware accelerators like Apple's Newral Engine and croogless Touror Processing Unit (TPUM) are used in voice assistants like six and hoogle Amilant to improve speech recognition and natural language procenting.

ADDING MARDWARE ACCELERATOR TO NORMAL

COMPLES White hardware accelerations can significantly improve the performance of normal complex, it is not possible to achieve the same lend of performance as a Supercompiler. nimply by adding handware accederation as a physin.

Adding hardware accelerations to a normal compiler can right cantly to boost its pertormance for specific tasks, but it does not change the fundamental architecture or limitations of the system.

For enough, adding a GPU to a computer can improve its ability to handle graphics-intensine applications, but it does not make it nitable for large-scale puralled processing tasks that require a supercomputer.

That being raid, there one name cases where clusters of hardwares - accelerated completes can be combined to create a mini-supercompiter that can handle certain types of parallel processing tools. However, this is still significantly less powerful than a two supercomputer and is limited by the capabilities of the Individual compiters in the dutter.