School of Information Technology and Engineering

Winter Semester 2022-2023 - Fresher

Digital Assignment-II

Programme Name & Branch : MCA

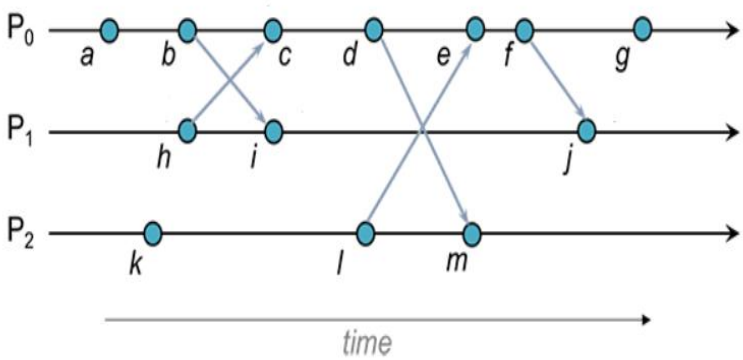**Course Name & code: Distributed Operating Systems & ITA5006**

Class Number (s): VL2022230500524

Slot: B2

Faculty Name (s) Dr.T.SENTHIL KUMAR

Maximum Marks:10

General instruction(s): Answer ALL Questions.

Q.No.	Question	Max Marks	CO	BL
1.	Consider the Figure, assume that initial logical clock values are all initialized to 0. a. List the Lamport timestamps for each event and provide timestamps for each labeled event. b. List the Vector Clock timestamps for each event and provide timestamps for each labeled event. 	10	CO3	BL4