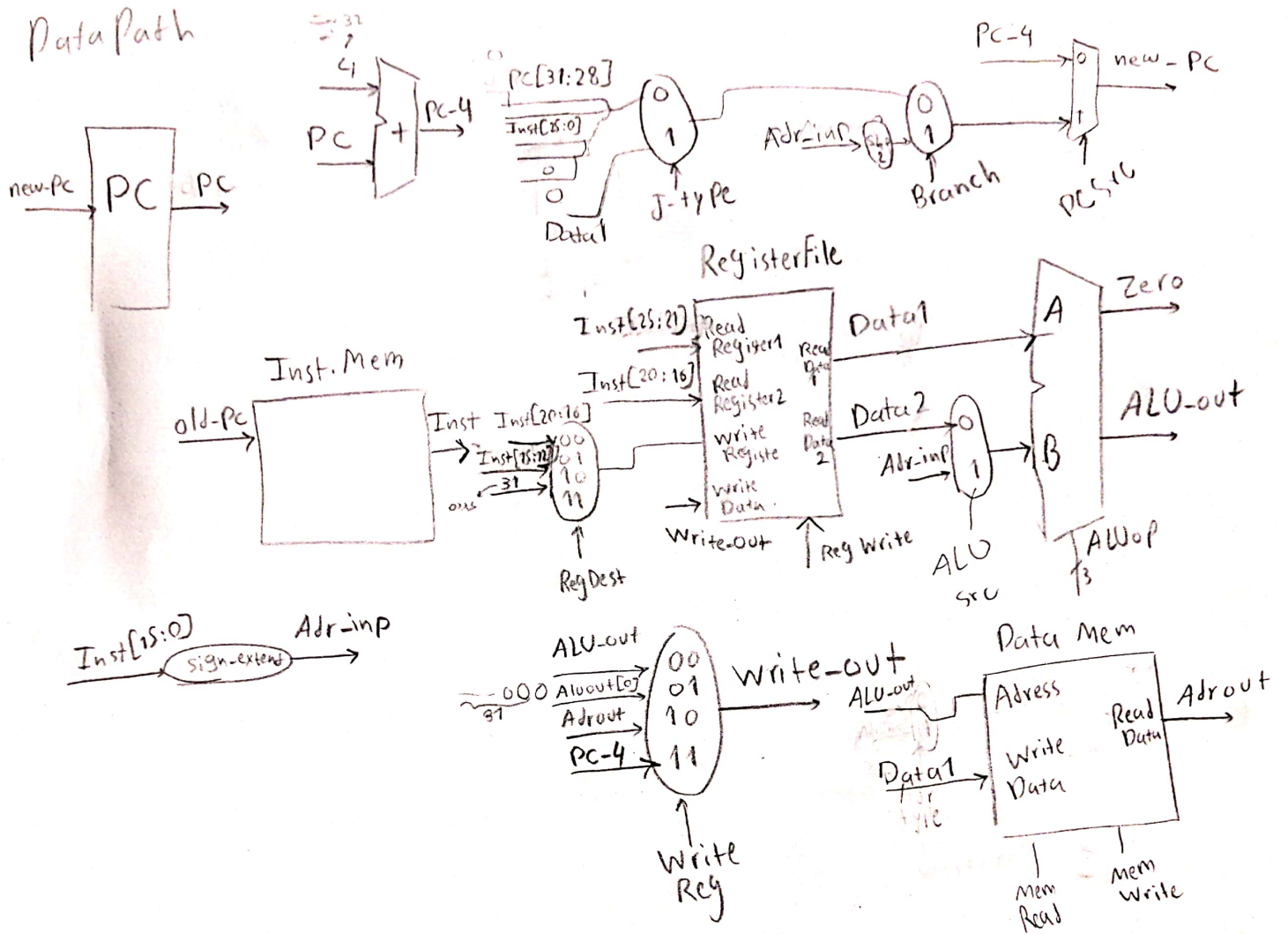


Data Path



Controller

Opcode	Func	Reg Dst	Reg Write	J+4	ALUSrc	ALUOp	Write Reg	Mem Write	Mem Read	PC Src	Branch
RT	add	01	1	-	0	ADD	00	0	0	0	-
RT	sub	01	1	-	0	SUB	00	0	0	0	-
RT	addi	00	1	-	1	ADD	00	0	0	0	-
RT	addi	00	1	-	0	SUB	01	0	0	0	-
RT	slti	01	1	-	1	SUB	01	0	0	0	-
RT	slti	00	1	-	1	SUB	01	0	0	0	-
RT	slti	00	1	-	1	ADD	10	0	1	0	-
lw	-	00	1	-	1	ADD	-	1	0	0	-
sw	-	-	0	-	1	ADD	-	1	0	0	-
beg	-	-	0	-	0	Sub	-	0	0	Zero	1
bne	-	-	0	-	0	Sub	-	0	0	~Zero	1
J	-	-	0	0	-	-	-	0	0	1	0
Jal	-	10	1	0	-	-	11	0	0	1	0
Jr	-	-	0	1	-	-	-	0	0	1	0