

Controller

be g	11	(Inst == beq)
breg		(Inst == breg)

	Signal	Max
.00	Posrc	3
$\sim i = \times i - 1$	beg	3
	7	3
	bne 9	2
	ALW SYC	
	ALUOP	2

ALO	
Membrite	3
memkead	5
WriteReg	1.4
J-type	3
Regost	2

Forwarding Unit if (Regwrite 3 == 1 && Write-adress-Teg == data 1-reg) forward 1 = 1 exif (Re 9 Write 4 == 1 dd write -adress-regl == data1-reg) forward 1 = 2 else forward1 = 0 28 write -adress -reg == data2-reg) if (Reg Write 3 == 1 forward 2 = 1 else if (Regwrite 4 == 1 dh write-adress-regl= = data 2-reg) forward 2 = 2 else forward 2= 0 Regwrite3 forward 1 write admissing forward Unit forward 2

dataz-19