

# 2SA1981S

**PNP Silicon Transistor** 

## **Description**

• Audio power amplifier application

#### **Features**

• High  $h_{FE}$ :  $h_{FE}=100\sim320$ 

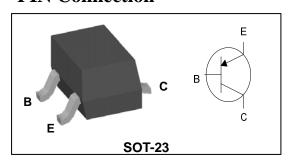
• Complementary pair with 2SC5344S

## **Ordering Information**

Type No.	Marking	Package Code
2SA1981S	<u>EA</u> <u> </u>	SOT-23

<sup>1</sup> Device Code 2 hFE Rank 3 Year&Week Code

### **PIN Connection**



## **Absolute maximum ratings**

(Ta=25°C)

Characteristic	Symbol	Ratings	Unit
Collector-Base voltage	$V_{CBO}$	-35	V
Collector-Emitter voltage	$V_{CEO}$	-30	V
Emitter-Base voltage	$V_{EBO}$	-5	V
Collector current	I <sub>C</sub>	-800	mA
Collector dissipation	P <sub>C</sub> *	350	mW
Junction temperature	Tj	150	°C
Storage temperature	T <sub>stg</sub>	-55~150	°C

<sup>\*</sup> Package mounted on 99.5% alumina 10×8×0.6mm

## **Electrical Characteristics**

(Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Collector-Base breakdown voltage	BV <sub>CBO</sub>	$I_C = -500 \mu A, I_E = 0$	-35	-	-	V
Collector-Emitter breakdown voltage	BV <sub>CEO</sub>	$I_C=-1mA$ , $I_B=0$	-30	-	-	V
Emitter-Base breakdown voltage	BV <sub>EBO</sub>	$I_E = -50 \mu A, I_C = 0$	-5	-	-	V
Collector cut-off current	I <sub>CBO</sub>	$V_{CB} = -35V$ , $I_{E} = 0$	-	-	-0.1	μΑ
Emitter cut-off current	I <sub>EBO</sub>	$V_{EB} = -5V$ , $I_{C} = 0$	-	-	-0.1	μА
DC current gain	h <sub>FE</sub> *	$V_{CE} = -1V$ , $I_{C} = -100$ mA	100	-	320	-
Collector-Emitter saturation voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> =-500mA, I <sub>B</sub> =-20mA	-	-	-0.5	V
Transition frequency	f <sub>T</sub>	$V_{CE}$ =-5V, $I_{E}$ =10mA	-	120	-	MHz
Collector output capacitance	Cob	V <sub>CB</sub> =-10V, I <sub>E</sub> =0, f=1MHz	-	19	-	pF

<sup>\* :</sup>  $h_{FE}$  rank / O : 100~200, Y : 160~320

## **Electrical Characteristic Curves**

Fig. 1 Pc-Ta

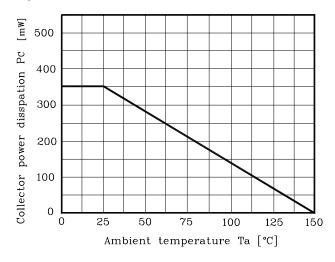


Fig. 2 IC - $V_{BE}$ 

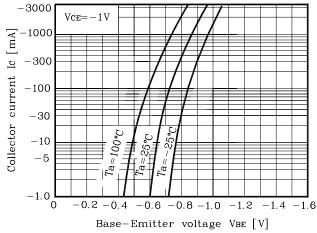


Fig. 3  $I_C$  -  $V_{CE}$ 

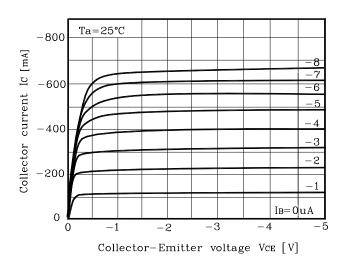


Fig. 4  $h_{\text{FE}}$  -  $I_{\text{C}}$ 

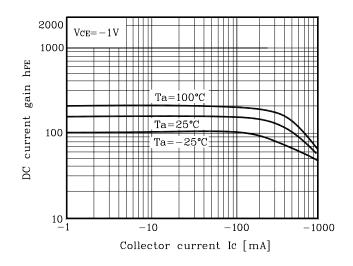
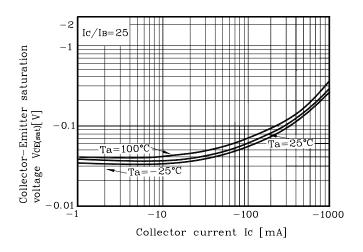
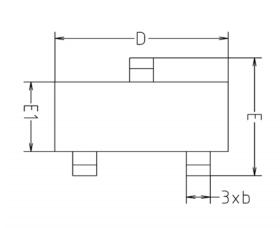


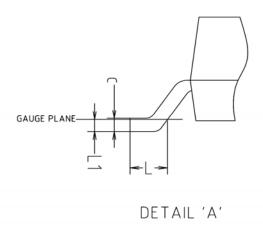
Fig. 5  $V_{\text{CE(SAT)}}$  -  $I_{\text{C}}$ 

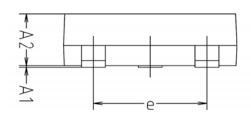


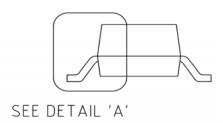
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# **Outline Dimension**



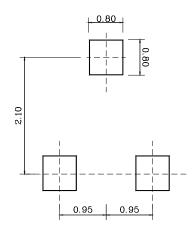






SYMBOL	MILLIMETERS			NOTE
STIDOL	MINIMUM	NOMINAL	MAXIMUM	NOTE
A1	0.00	-	0.10	
A2	0.82	-	1.02	
Ь	0.39	0.42	0.45	
С	0.09	0.12	0.15	
D	2.80	2.90	3.00	
E	2.20	2.40	2.60	
E1	1.20	1.30	1.40	
е	1.90BSC			
L	0.20	-	-	
L1	0.12BSC			

### \*Recommend PCB solder land [Unit: mm]



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