**Mohammad Issvand Nassiri**

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# **\_Education**

* + **\_ \_Shahid-Chamran University** Ahvaz, Iran

Master of Science in Electrical and Electronic Engineering July 2011 **GPA: 16.66** out of **2**0

**Thesis title**: *Digital Background Calibration of Timing and Gain Mismatch Errors in Time-Interleaved ADCs*

* + **\_ \_Quchan University of Technology** Quchan, Iran

Bachelor of Science in Telecommunication Technology Engineering July 2009 **GPA: 16.16** out of **2**0

**Project title**: *6-Channel High Frequency Remote Control*

* + **\_ \_Shahid-Mohajer Isfahan Technical Higher Educational Center** Isfahan, Iran

Associate Degree in Electronics (The Forth Top Student)

September 2007 **GPA: 16.16** out of **2**0

**Project title**: *Ultrasonic Distance Meter by Microcontroller*

* + **\_ \_Shahid-Beheshti High School** Dezful, Iran

Diploma in Electronics (Top Student in High School) September 2005 **GPA: 18.29** out of **2**0

# **\_Industry Experience**

* \_ \_Instrumentation & Control Engineer, Engineering Department, National Iranian South Oil Company Ahvaz, Iran

December 2019 – present

* I & C engineering in the most important EPC projects of Iran
* Assessing designing documents
* Evaluating design, engineering, and construction of projects in compliance with International Standards
* Corresponding with contractors in English
* \_ \_Telecommunication Engineer, Marun Oil & Gas Production Units, National Iranian South

Oil Company Ahvaz, Iran

June 2010 – November 2019

* Electronic Supervisor
* Design of Self-Powered Electronic Boards for Telephones
* Transforming the analog telecom. links into the digital
* \_ \_Founding *Fannavari Sotudeh Kardan Co.:*
* Offering engineering services
* Maintenance and installation of technical devices and systems

# **\_Teaching Experience**

* + ***\_ \_Applied Mathematics***• Undergraduate Students, Shahid-Mohajer Isfahan Technical Higher Educational Center **–** Recitation Classes
* ***\_ \_Electronics 2***• Undergraduate Students, Quchan University of Technology **–** Recitation Classes

# **\_Publications**

\_ \_Journal publications:

* ["](https://www.techrxiv.org/articles/preprint/A_New_Technique_for_Gain_and_Timing_Calibration_in_Time-Interleaved_ADCs_Using_an_Estimation_Signal/21938294)[*[A New Adaptive Blind Background Calibration of Gain and Timing Mismatch for a Two-Channel Time-Interleaved ADC](https://www.techrxiv.org/articles/preprint/A_New_Technique_for_Gain_and_Timing_Calibration_in_Time-Interleaved_ADCs_Using_an_Estimation_Signal/21938294)*[", Microelectronics Journal, 2018.](https://www.techrxiv.org/articles/preprint/A_New_Technique_for_Gain_and_Timing_Calibration_in_Time-Interleaved_ADCs_Using_an_Estimation_Signal/21938294)](https://www.sciencedirect.com/science/article/abs/pii/S0026269218300211)
* "*High-Speed Estimation-Based Calibration of Gain and Timing Mismatches in Multi-Channel Time-Interleaved ADCs*", submitted to Computers and Electrical Engineering, 2025. (July 18, 2025)

\_ \_Conference publications:

* "*A Reduction in Mismatch Error Tones by Randomization of Errors in Time-Interleaved ADCs*", Engineering Sciences and Novel Ideas Conference, 2013.
* "*Digital Background Calibration in Time-Interleaved ADCs*", Engineering Sciences and Novel Ideas Conference, 2013.

# **\_Professional Membership**

* Iranian Society of Electrical Engineer
* Reviewer at Journal of Circuits, Systems, and Computers (JCSC)

# **\_Research Interests**

* Analog & Mixed-Signal Circuits and Data Converters
* Integrated Circuit Design
* Brain-Computer Interfaces
* Wearable Sensors

# **\_Programming Languages**

* Python
* C/C++ Code
* MATLAB
* Assembly & Visual Basic
* VHDL

# **\_TECHNICAL SKILLS**

* **\_ \_Hardware Description & Scripting Languages**
* **VHDL**, **Verilog**, Python, TCL, Visual Basic, Assembly
* **\_ \_EDA & IC Design Tools**
* **Commercial:** Cadence Virtuoso
* **Open-Source:** Magic VLSI, OpenROAD, Xschem, KLayout, Netgen
* **PDKs:** SKY130 (Efabless ecosystem)
* **\_ \_Design Flow Expertise**
* Schematic Entry
* Analog/Digital Simulation
* Layout
* LVS/DRC
* Post-Layout Simulation
* GDSII
* **\_ \_Circuit Domains**
* Analog and Mixed-Signal CMOS Circuit Design and Interfaces
* Biomedical Signal Acquisition
* ADC Calibration
* Low Power and Low Noise Circuit Design
* Signal Conditioning and Digitization
* **\_ \_Simulation & Modeling**
* HSpice, PSpice, OrCAD, MATLAB/Simulink, Proteus, …
* **\_ \_PCB Design**
* Proteus • Altium Designer
* **\_ \_Other**
* **Industrial Sensors and Signal encoding**
* **\_Additional Info.**
* TOEFL (Oct, 2, 2024): 91

# **\_References**

* Alimorad Mahmoudi, Professor of Electrical & Electronic Engineering, Shahid Chamran University

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* Karim Ansari, Professor of Electrical & Electronic Engineering, Shahid Chamran University

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* Mahdi Rezvanyvardom, Professor of Electrical & Electronic Engineering, Arak University

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* Hamid Khalifi Asl, Electronic Supervisor, National Iranian South Oil Company

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