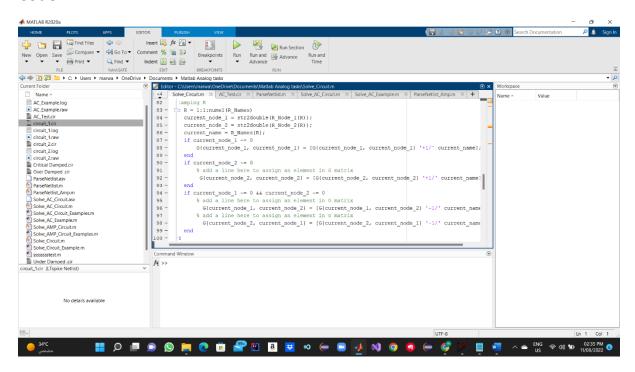
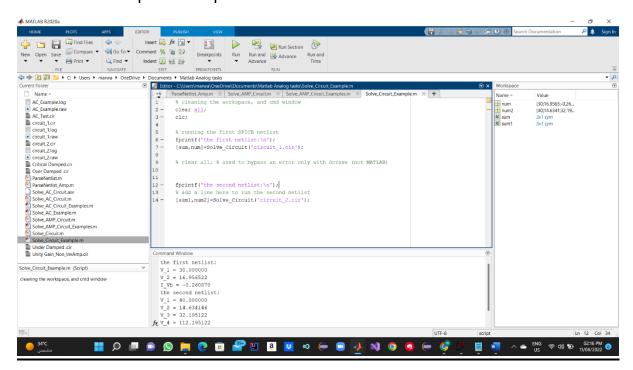
## Part 1:

#### Code:



#### Simulation Results

#### And Solve Example is completed:



## <u>Table Compare</u>:

Matlab	Ltspice						
the first netlist:	the first netlist:						
V 1 = 30.000000	V(1):		30	voltage			
V 2 = 16.956522	V(2):		16.9565	voltage			
I Vb = -0.260870	I(Is):						
the second	2		device_current				
netlist:	I(R3):		1.69565	device_current			
	I(R2):		0.565217	device_current			
V_1 = 40.000000	I(R1):	0.2007	0.26087 device curre	device_current			
V_2 = 14.634146	I(Vb): -	ent					
V_3 = 32.195122	the second notlist:						
V_4 =	the second netlist:						
112.195122 I_Vb = -1.268293	v(1):		40	voltage			
	V(2):		14.6341	voltage			
	V(3):		32.1951	voltage			
	V(4):		112.195	voltage			
	I(Is):		1	device_current			
	I(R6):		0.804878	device_current			
	I(R4):		1.46341	device_current			
	I(R3):		-1	device_current			
	I(R2):		-0.195122	device_current			
	I(R1):		1.26829	device_current			
	I(Vb):		-1.26829	device_current			

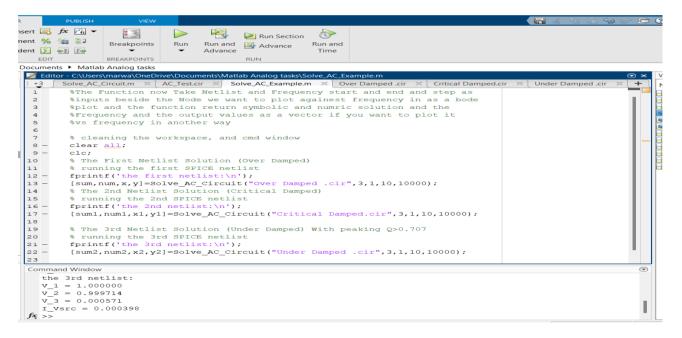
Ltspice Find The value of the Current in each branch while Its Not Supported in my code And The results of The voltage Nodes My Code have higher precision.

## Part 2:

Created the script for solving Ac.

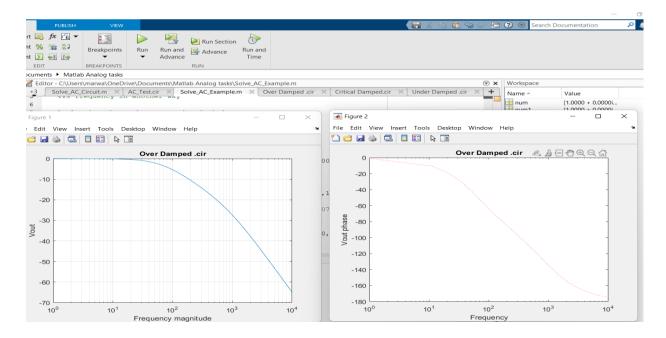
```
| Pull |
```

## Created the script for solving Ac Example.

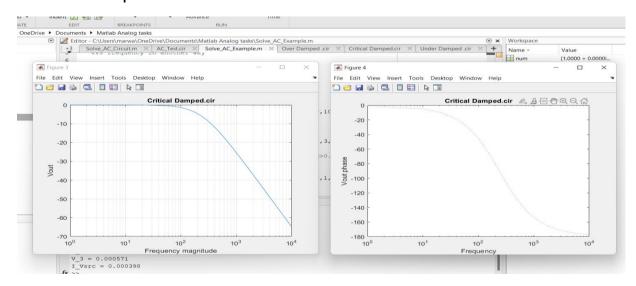


## My Simulator Results:

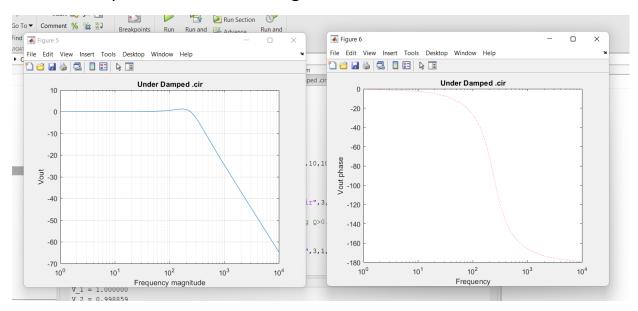
## Over Damped RLC:



## Critical Damped RLC:

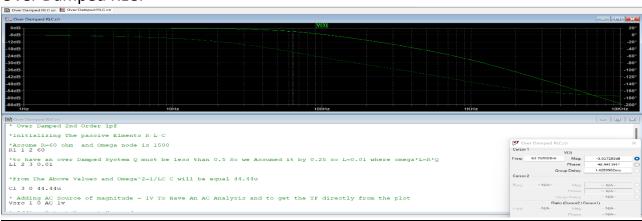


## Under Damped RLC With Peaking:

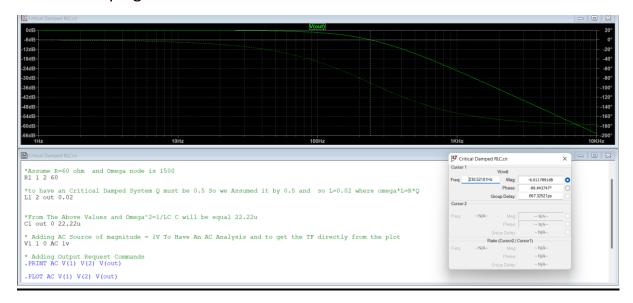


# **LTspice Results:**

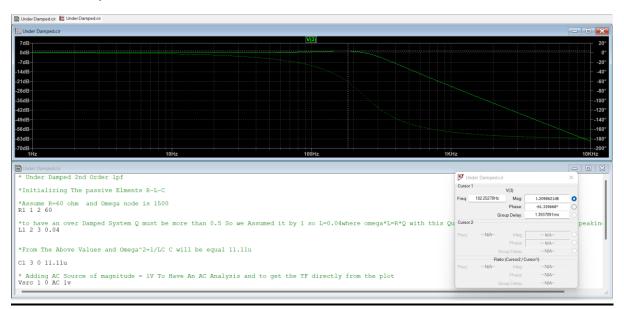
#### Over Damped RLC:



### **Critical Damping RLC:**



#### **Under Damped RLC:**



## **Comment:**

The Graphs Are The Same For Both Lt Spice and my Simulator
But LtSpice Is faster at getting Results and have more helping tools
like cursor to have an accurate number for the cutoff Freq etc.

## **PART 3:**

## Added Support for both VCVS and VCCS:

```
ocuments • Matlab Analog tasks
Editor
                                                                         Solve_AMP_Circuit.m* × Solve_AMP_Circuit_Ex
                    C\{V, current node 2\} = '-1';
248 -
249 -
250
          %Stamping VCVS
251 -
252 -
        for E =1:1:numel(E_Names)
              current_node_1 = str2double(E_Node_1(E));
current_node_2 = str2double(E_Node_2(E));
Control_node_1 = str2double(E_Node_C1(E));
Control_node_2 = str2double(E_Node_C2(E));
254 -
256 -
               current name =E Names{E};
257 -
258
               259 -
               C{E+(numel(V_Names)),current_node_1} = '+1';
260 -
261 -
               262
263 -
264 -
265 -
                    if Control_node_1 ~= 0
266
267 -
               % add a line here to assign an element in B matrix
C{E+(numel(V_Names)),Control_node_1} =['-1*' current_name];
268 -
               if Control_node_2 ~= 0
                                   here to assign an element in B matrix
270
271 -
272 -
                    C{E+(numel(V_Names)),Control_node_2} =['+1*' current_name];
Command Windov
```

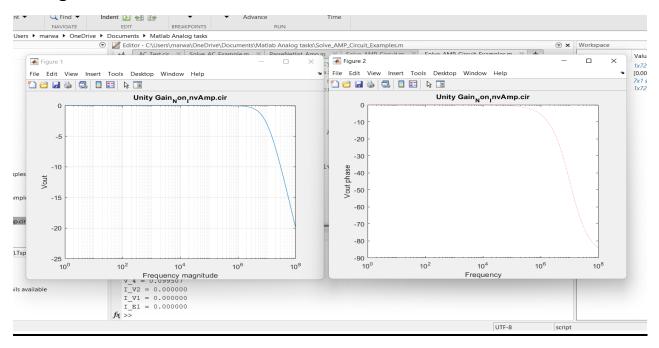
## Note:

Modified The Parseing function to add support for VCVS And VCCS

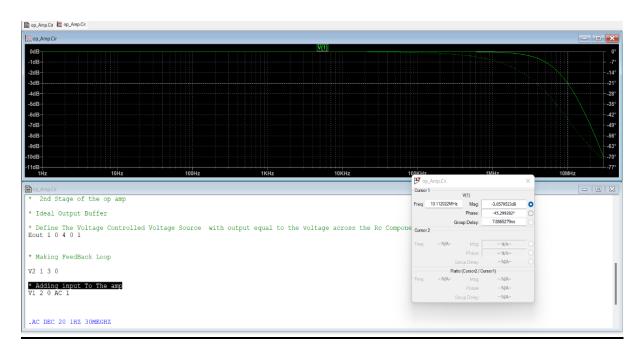
I used the modifeid one along with the original one ,its not optimum for coding it can be done with the original one with some modifications only.

## My Simulator Results:

# Non Inverting amplifier with UGF=10 Megahz connected in UGain Configuration



## Lt SPice Results:



<u>Comment:</u> The Graphs are Identical The Simulator is spice accurate but its much slower than spice.