Mohamed Manessouri  
SSE

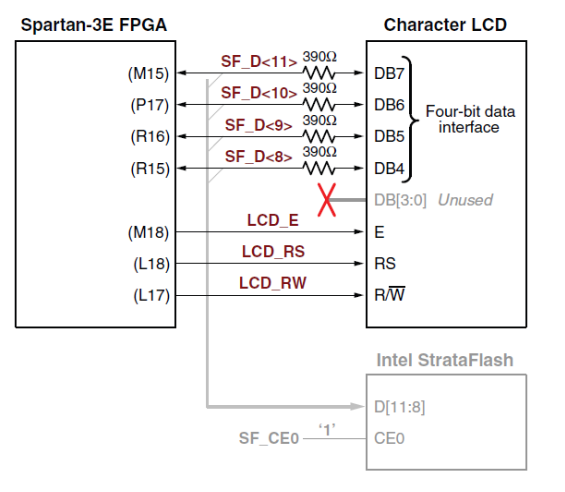
**LCD PROJECT**

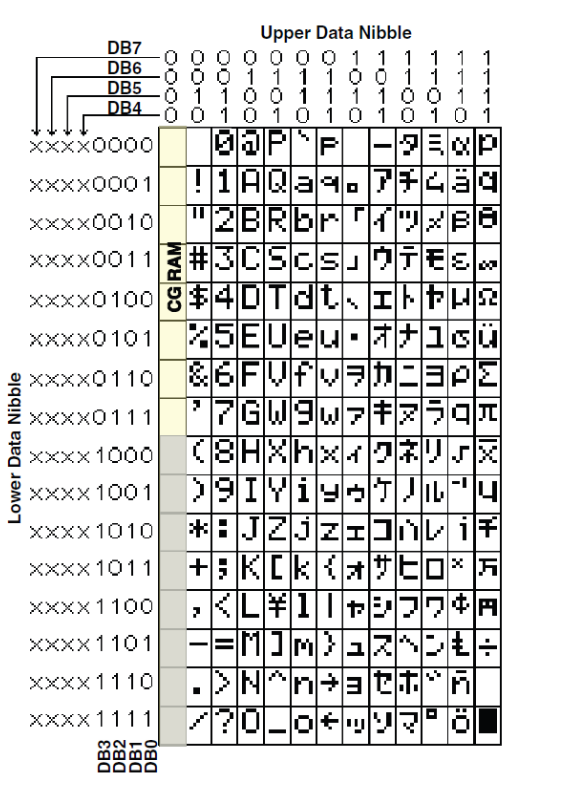
The Spartan-3E contains a Liquid Crystal Display (LCD) which displays 2 lines of 16 characters each.

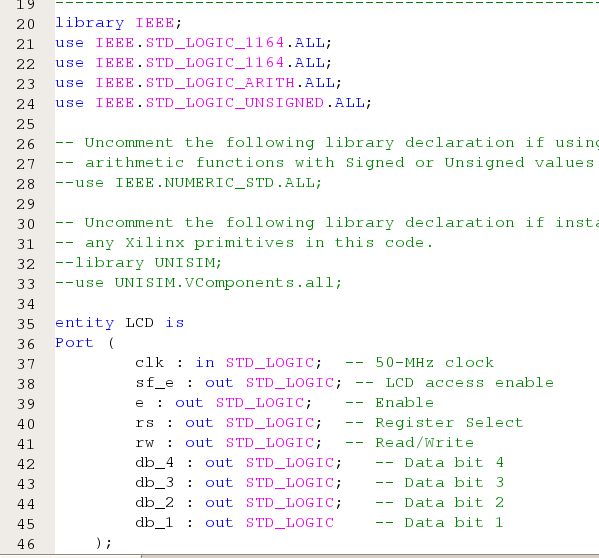
The FPGA controls the LCD through a 4-bit interface, as shown in Figure 4.1. The LCD actually

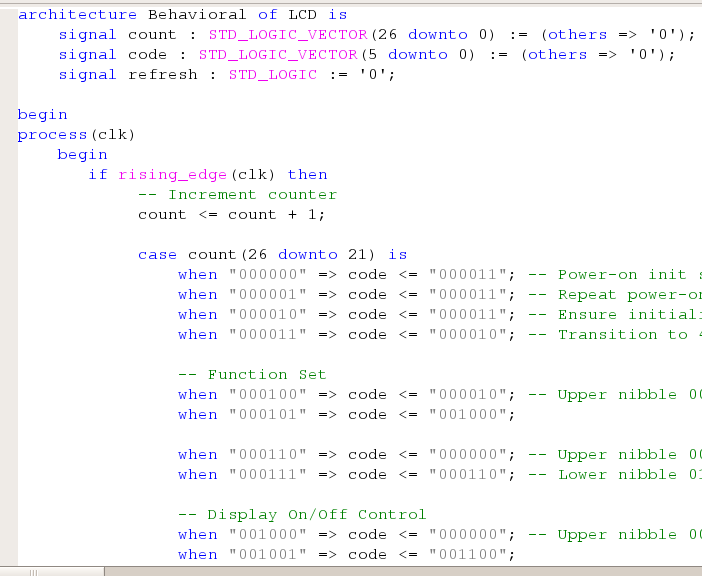
supports an 8-bit interface, however the Spartan-3E uses a 4-bit interface to remain compatible with

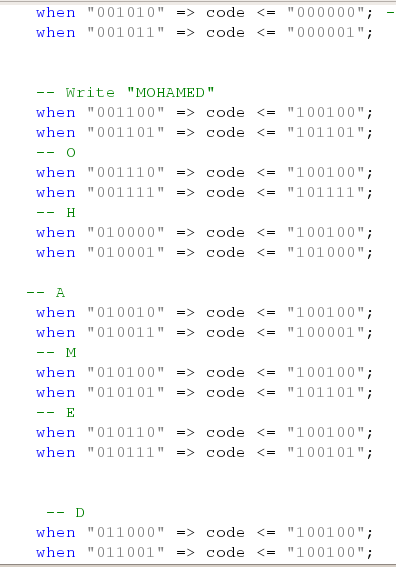
other Xilinx products, and to minimise pin count.

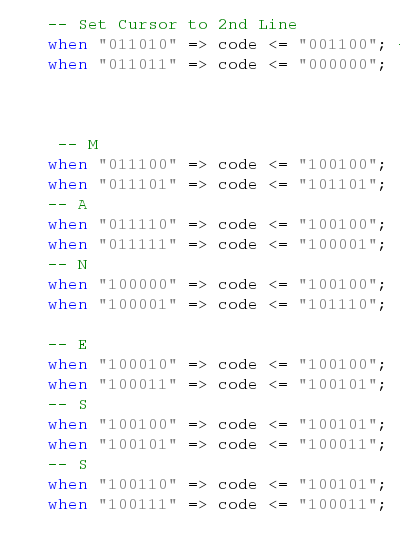
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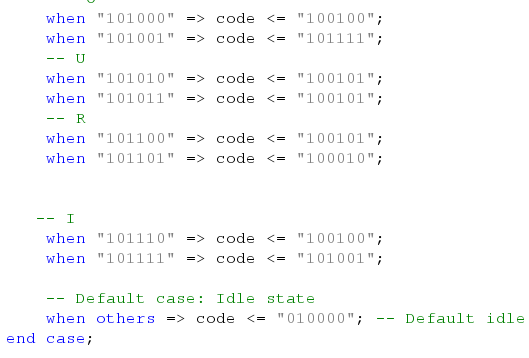
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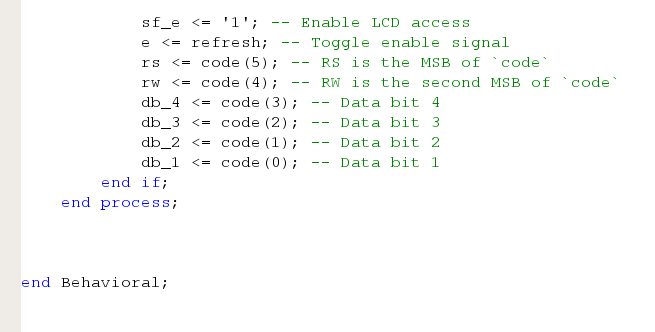
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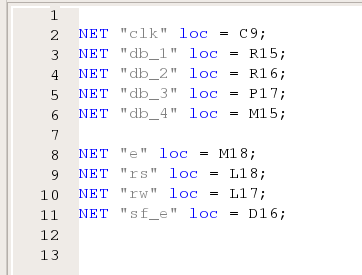
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