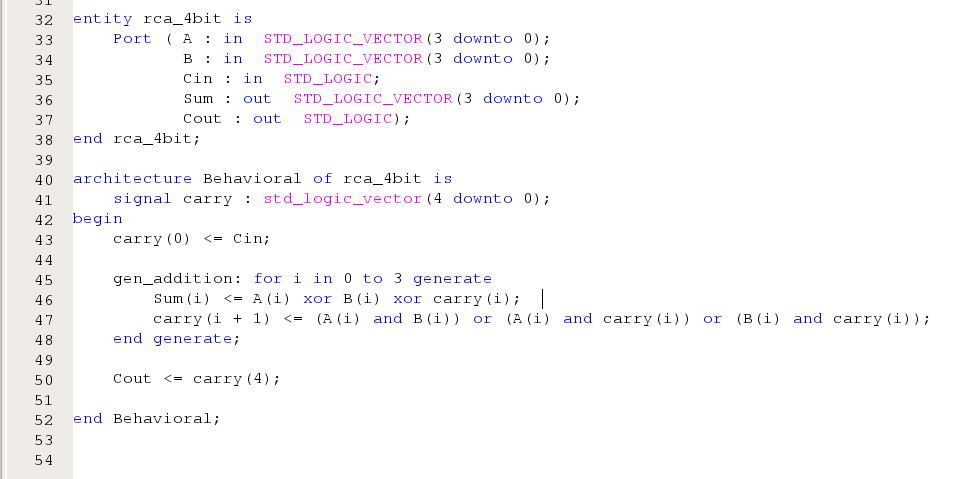
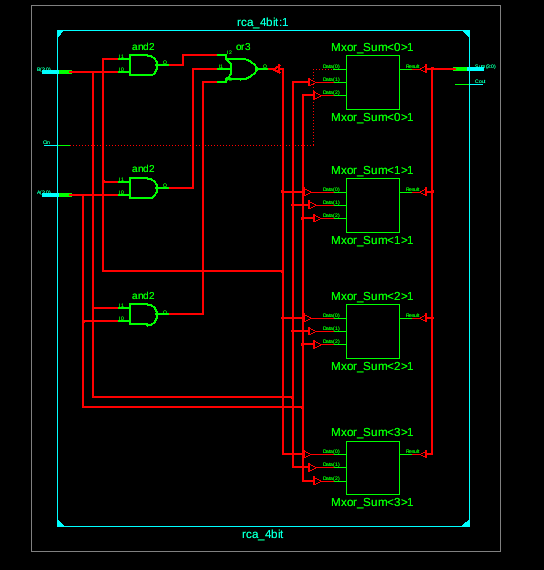
Mohamed Manessouri  
SSE

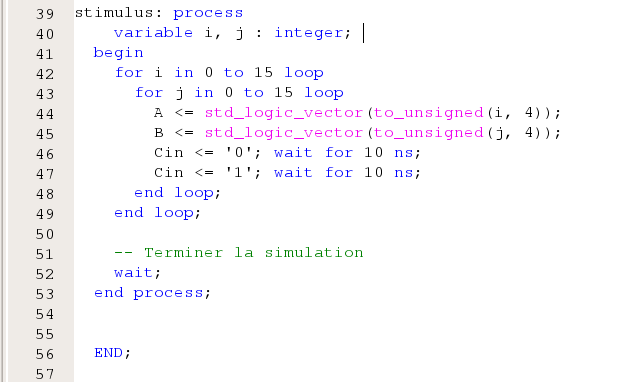
**Tp4: Test Benches**

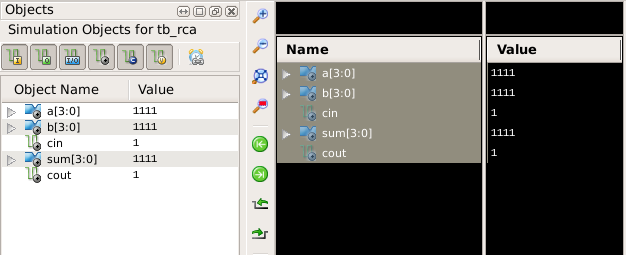
**Exercice 1:**

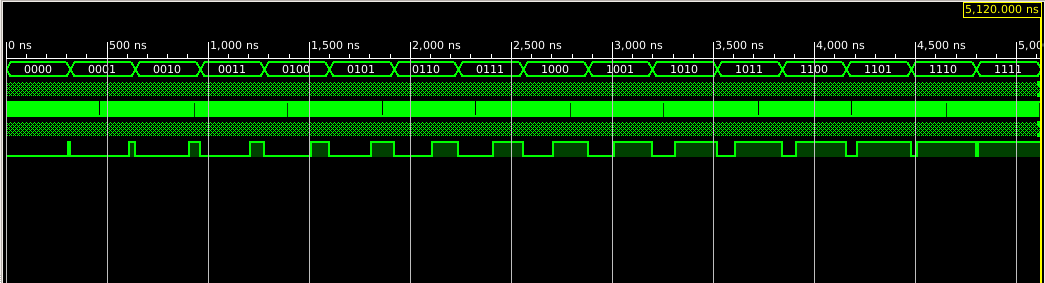
**Rca\_4bit:  
**

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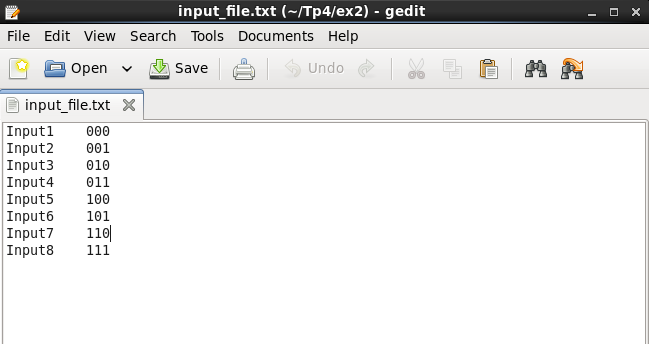
**Test Benche:**

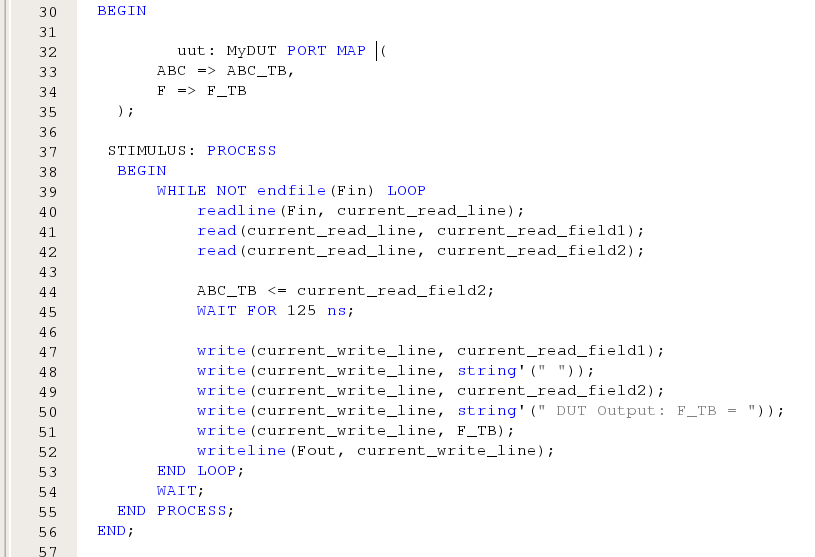
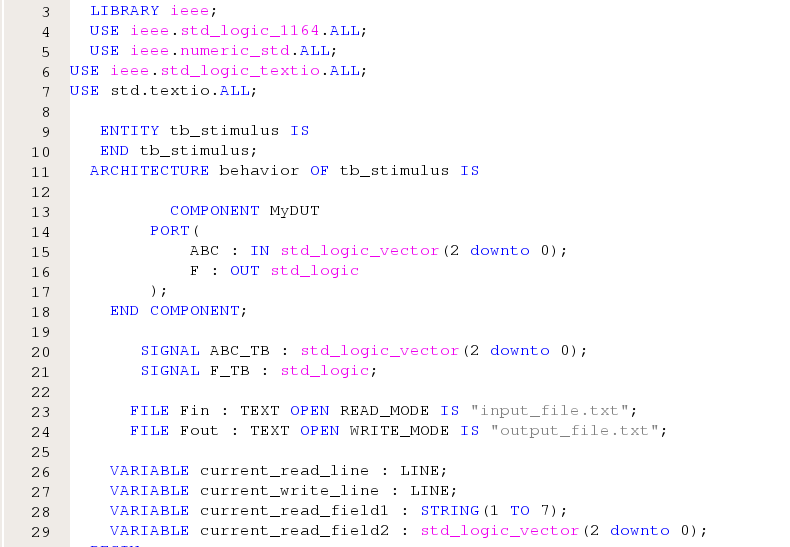
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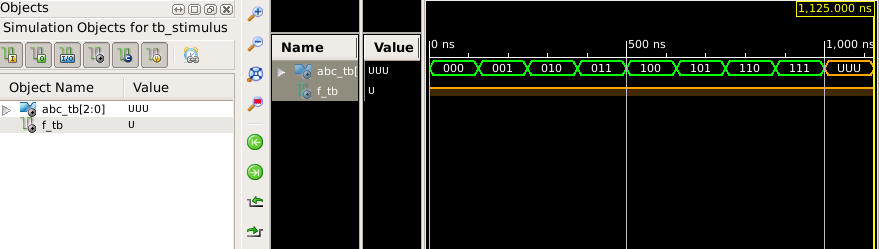
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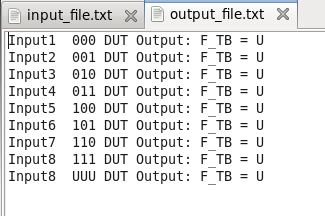
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**Exercice 2:**

* **Préparer le fichier d’entrée:  
  **

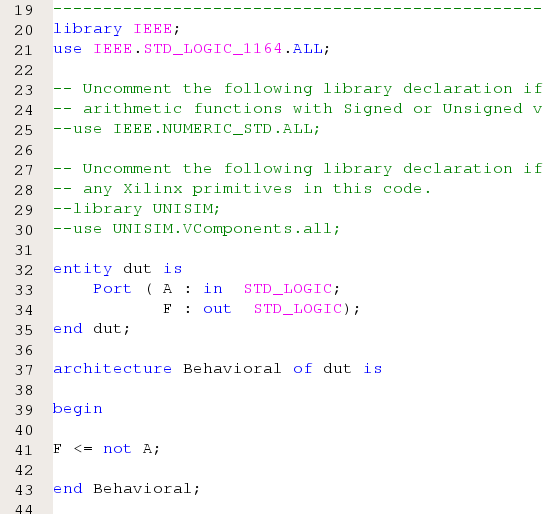
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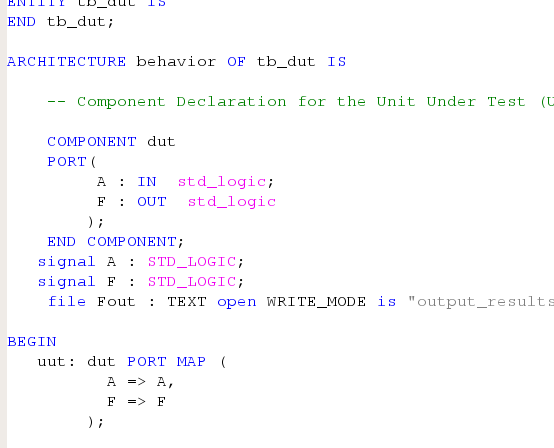
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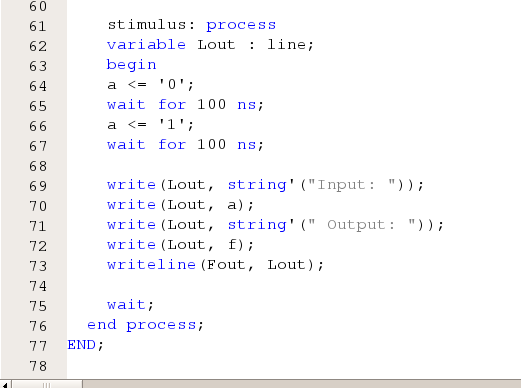
1. **Utilisation de l’écriture dans un fichier externe**

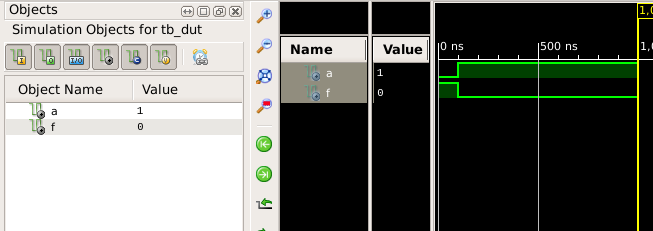
Dut.vhdl

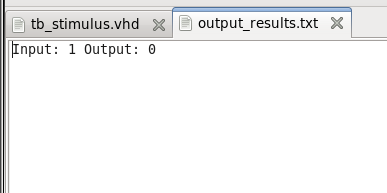


Tb\_dut.vhdl

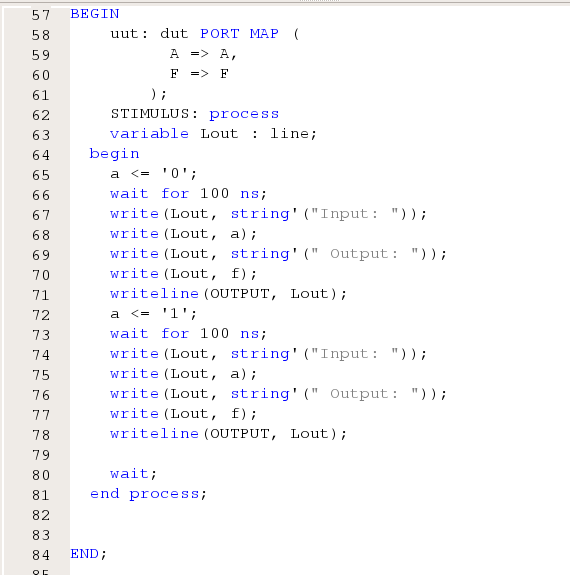


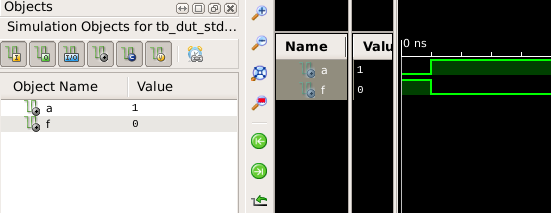


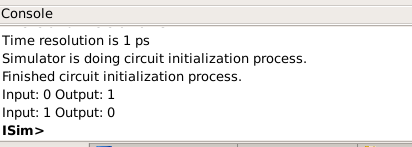




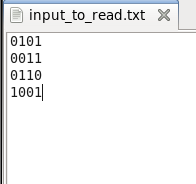
1. L’écriture dans STD\_OUTPUT à partir d’un test bensh :

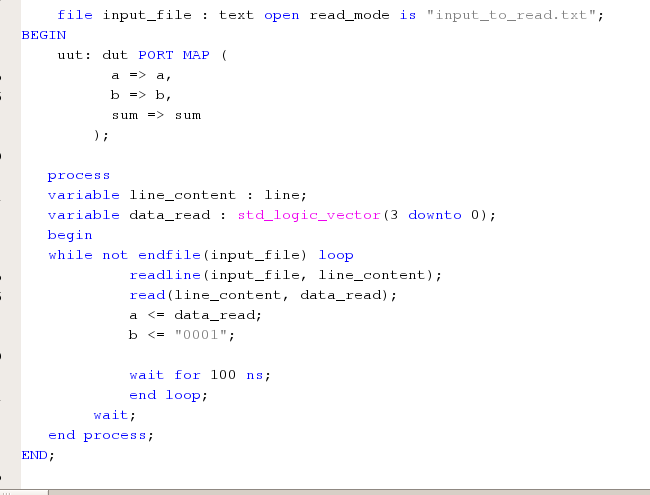


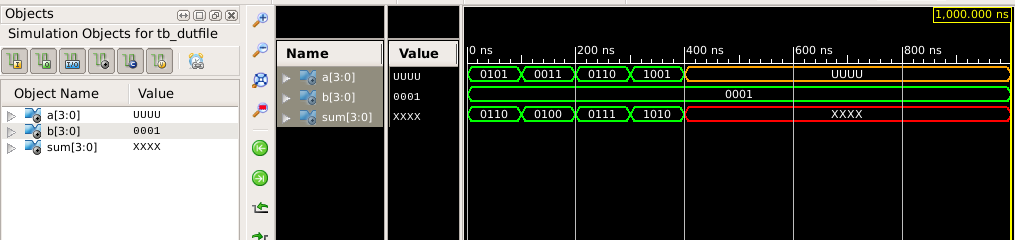




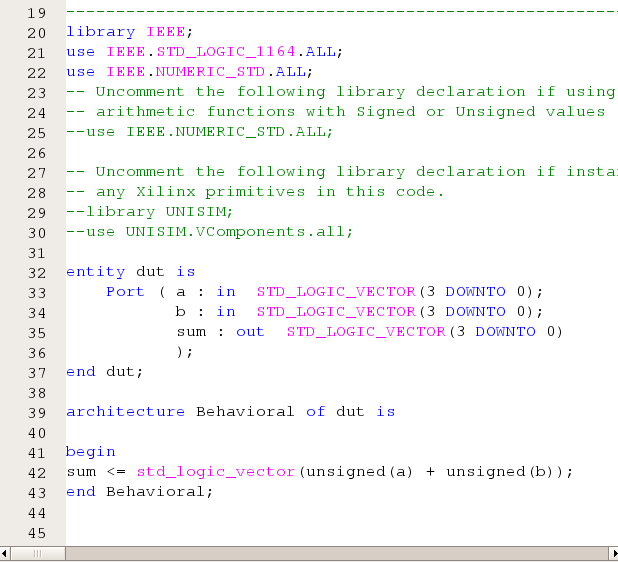
1. Lecture à partir d’un fichier externe dans un test Bench :







4) Lecture de données déliùitées par des espaces à partir d’un fichier externe dans un test Bench :

Dut.vhdl  


Tb\_dut.vhdl  
