

STM32F407xx MCU Header File

```
graph LR; A[Peripheral Register Definitions] --> B[STM32F407xx MCU Header File]
```

The diagram consists of two rectangular boxes. The box on the right is shaded gray and contains the text 'Peripheral Register Definitions'. The box on the left is white with a black border and contains the text 'STM32F407xx MCU Header File'. A black arrow points from the right box to the left box, indicating a dependency or inclusion relationship.

Peripheral Register
Definitions