

STM32F407xx MCU Header File

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graph LR; A[STM32F407xx MCU Header File] --> B[NVIC Priority Registers Base Address]
```

The diagram consists of two rectangular boxes. The box on the left is white with a black border and contains the text 'STM32F407xx MCU Header File'. The box on the right is gray with a black border and contains the text 'NVIC Priority Registers Base Address'. A black arrow points from the right side of the white box to the left side of the gray box, indicating a dependency or relationship between the two.

NVIC Priority Registers
Base Address