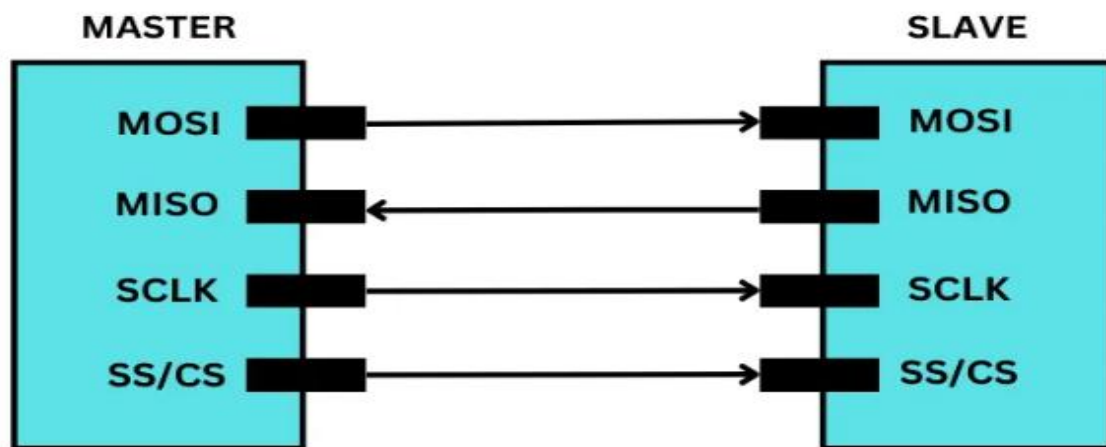


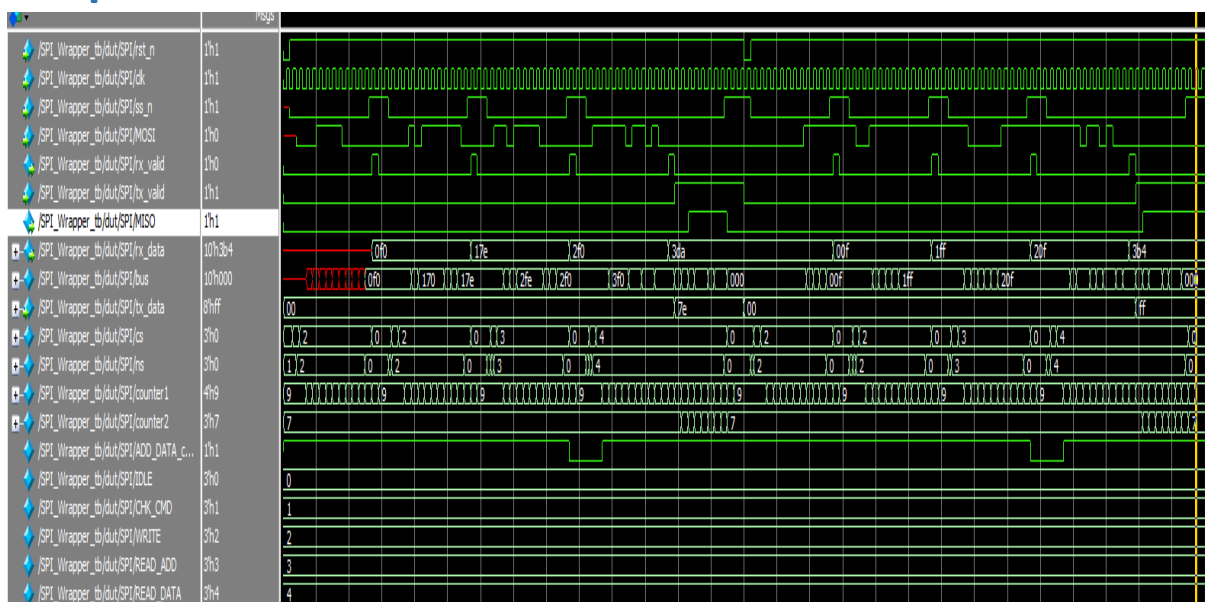
Name

Mohamed Ahmed Mohamed
Hussein

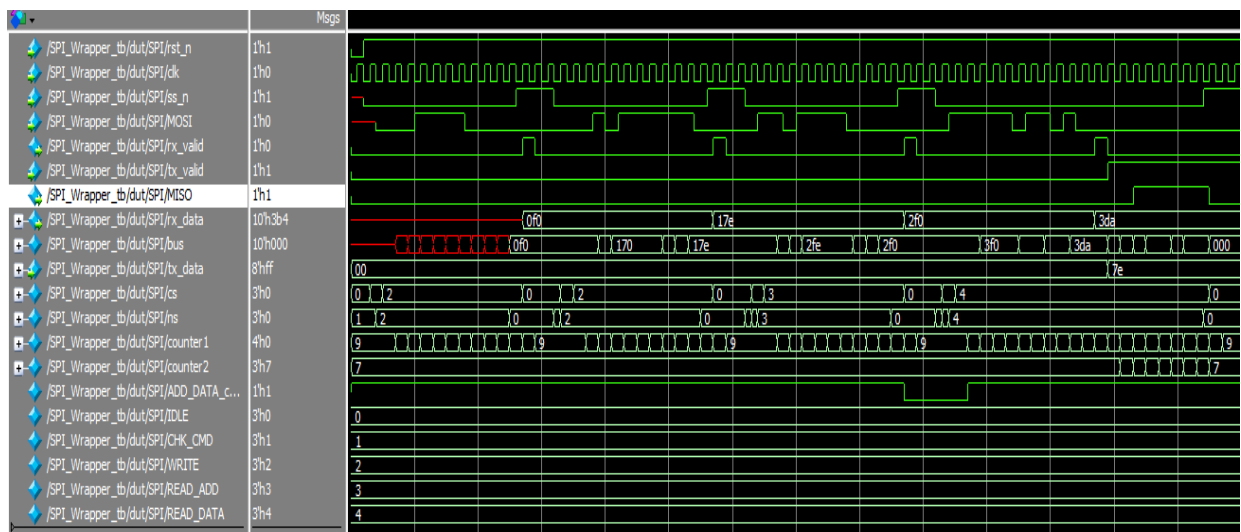
SPI_Project



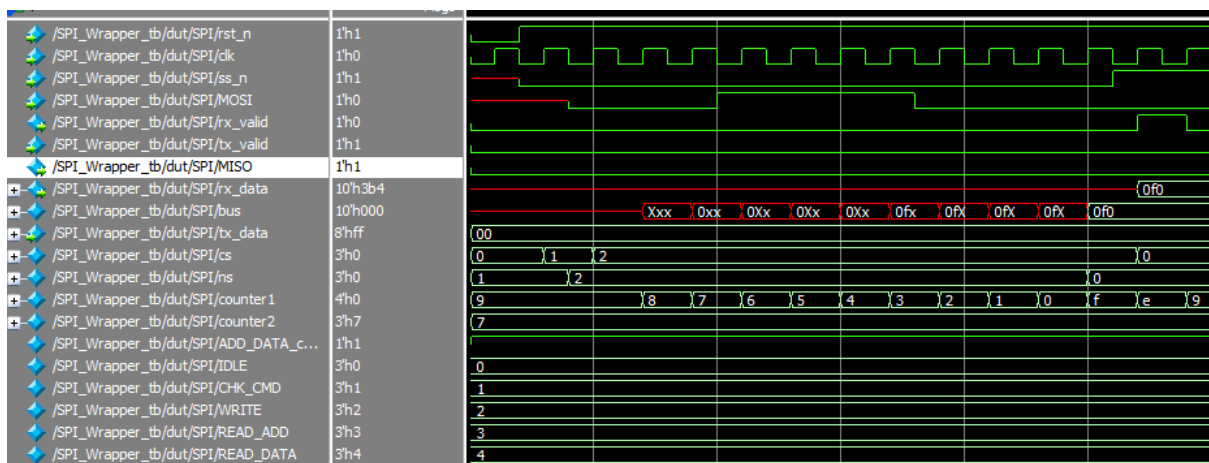
Snapshot of the whole wave form:



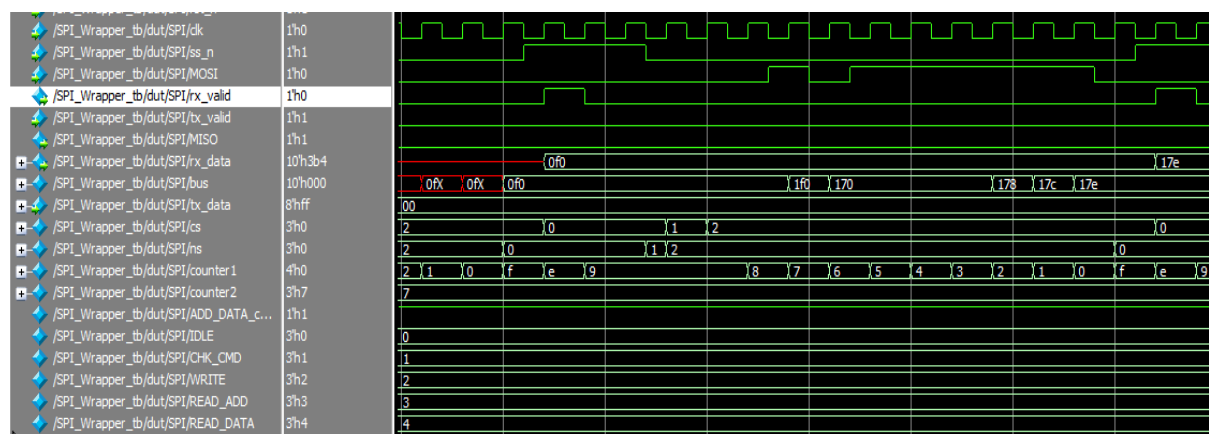
First scenario:



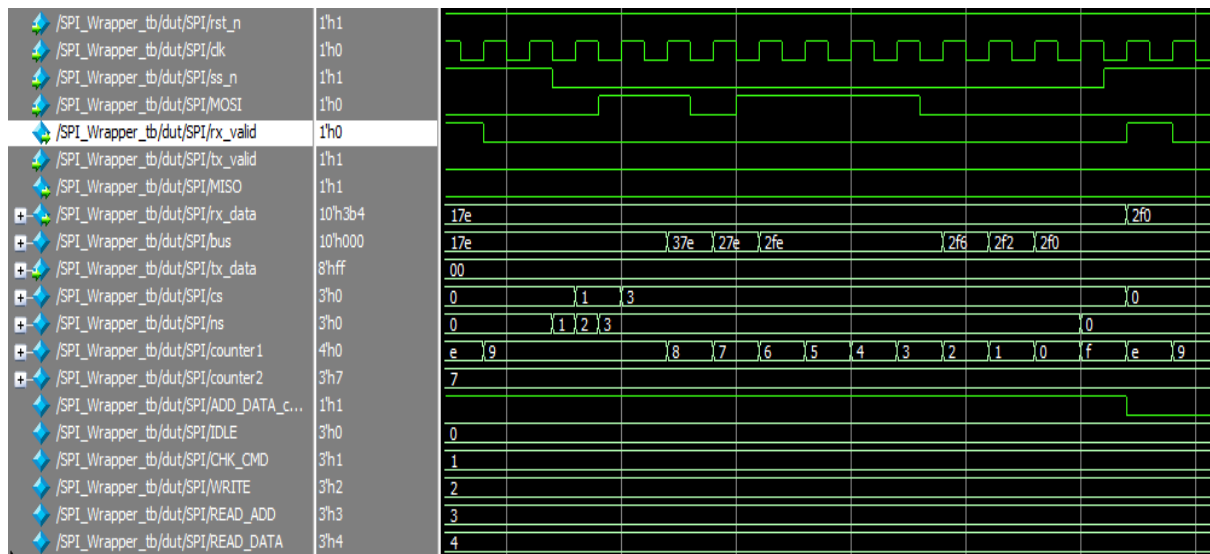
• WRITE state (Address):



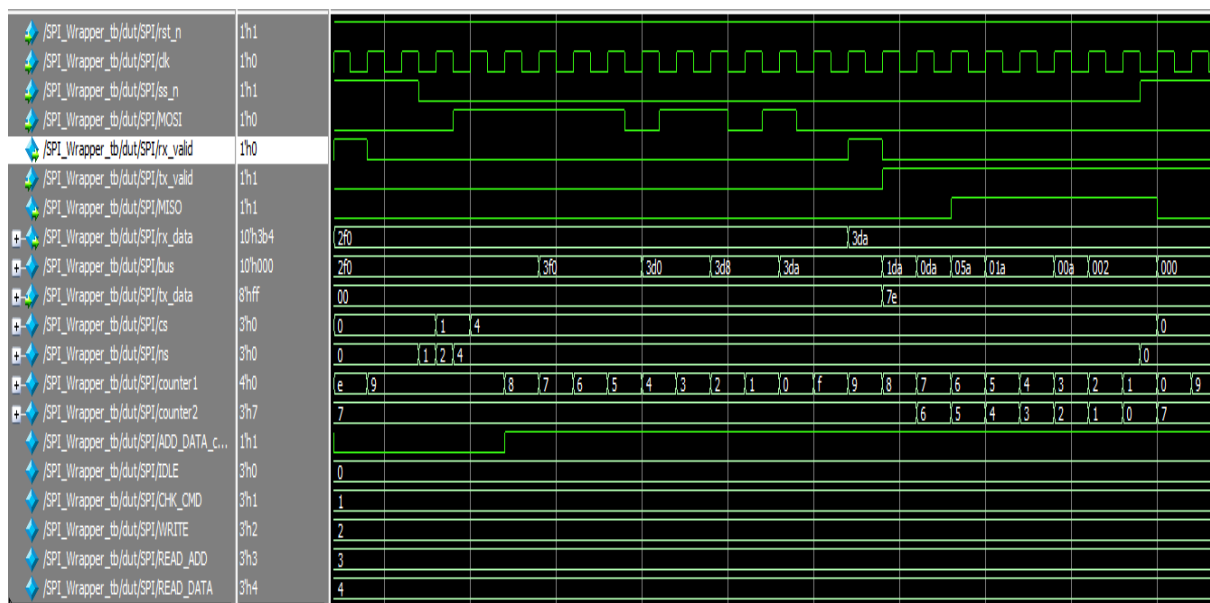
• WRITE state (Data):



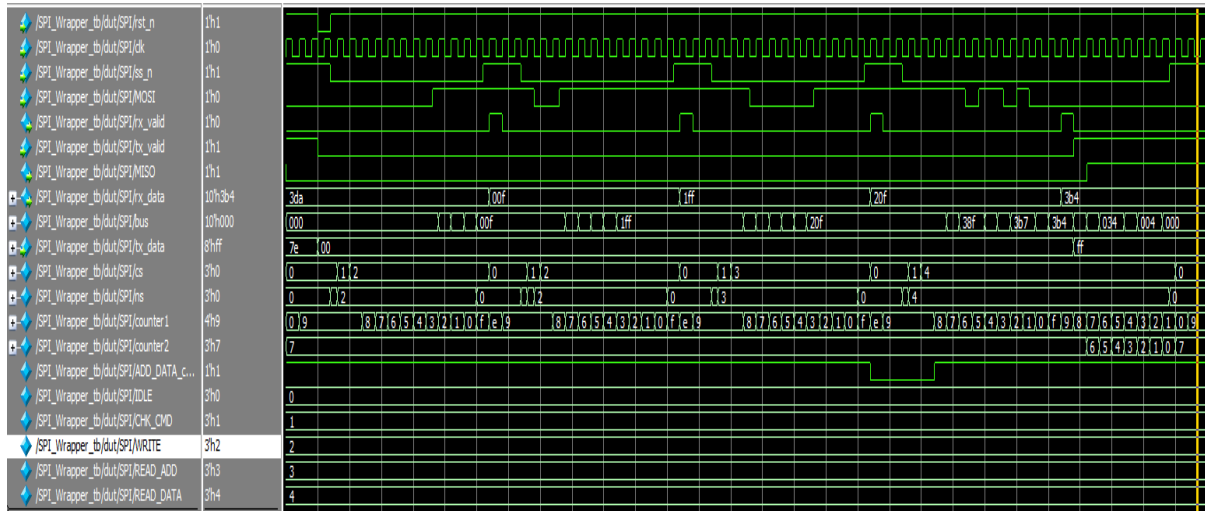
- READ_ADD state:



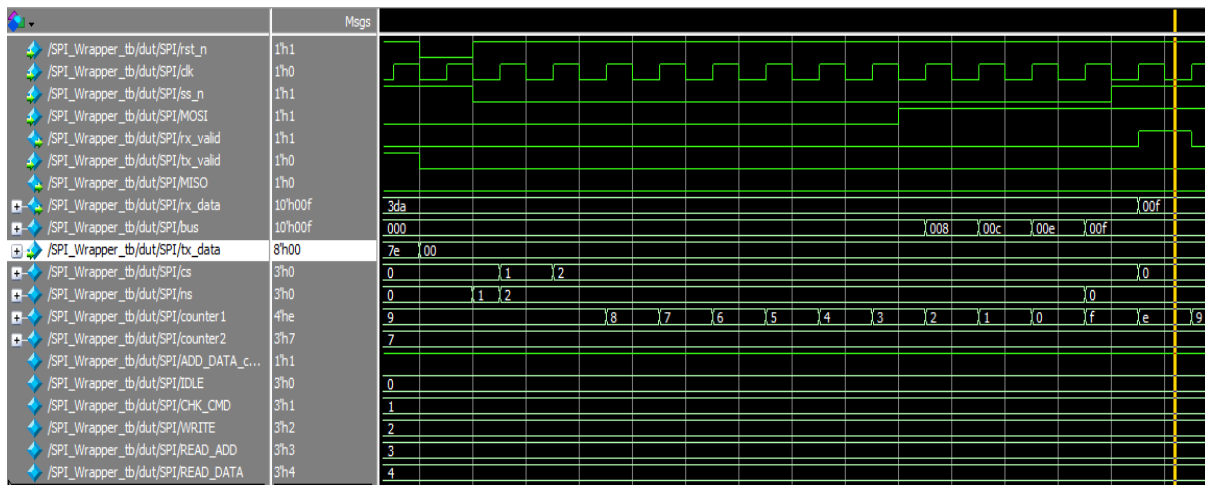
- READ_DATA state:



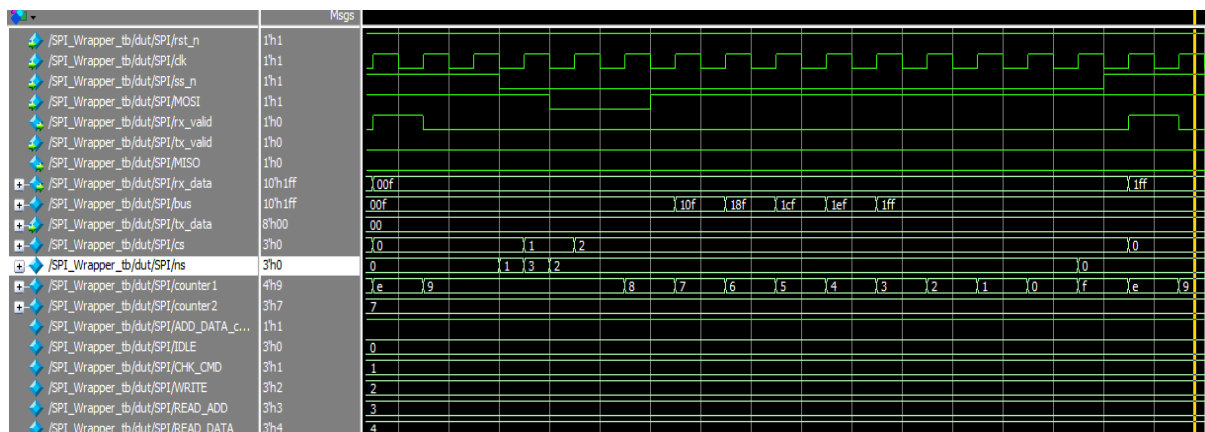
Second scenario:



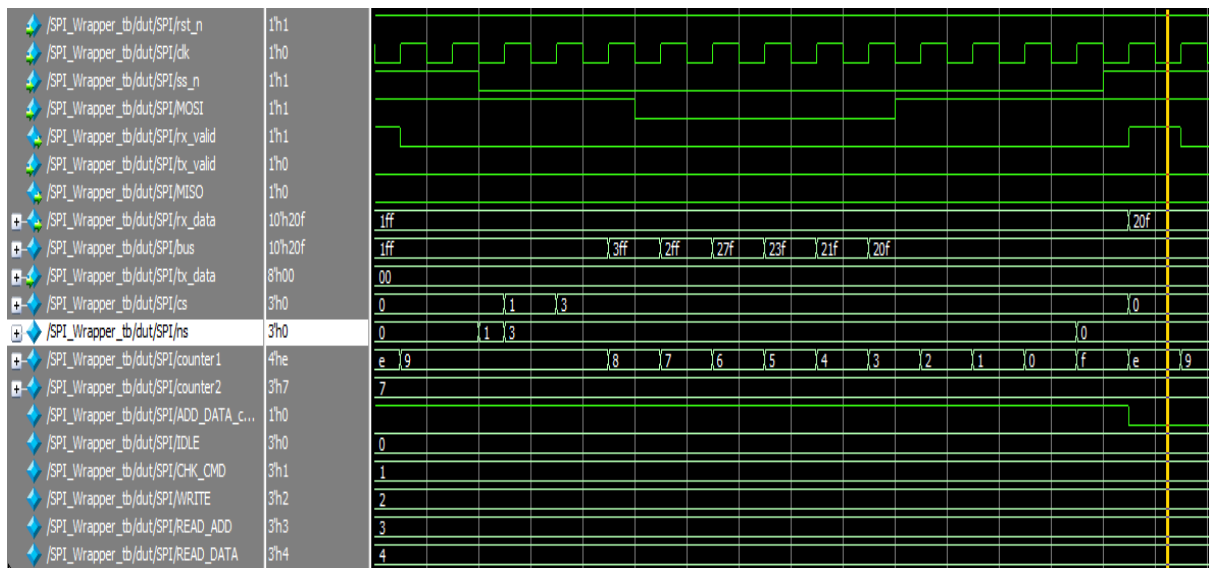
- WRITE state (Address):



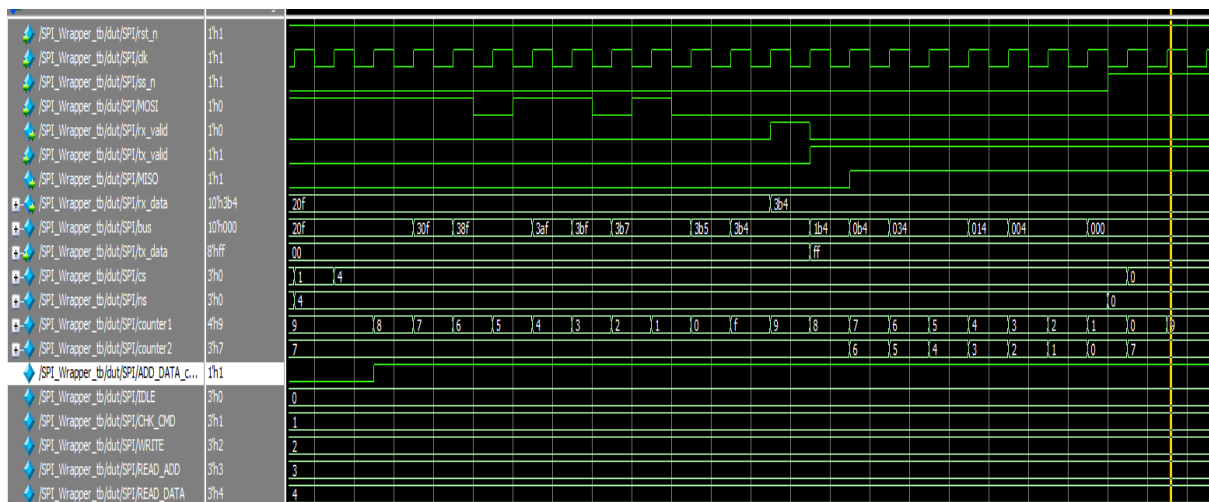
- WRITE state (Data):



- READ_ADD state:

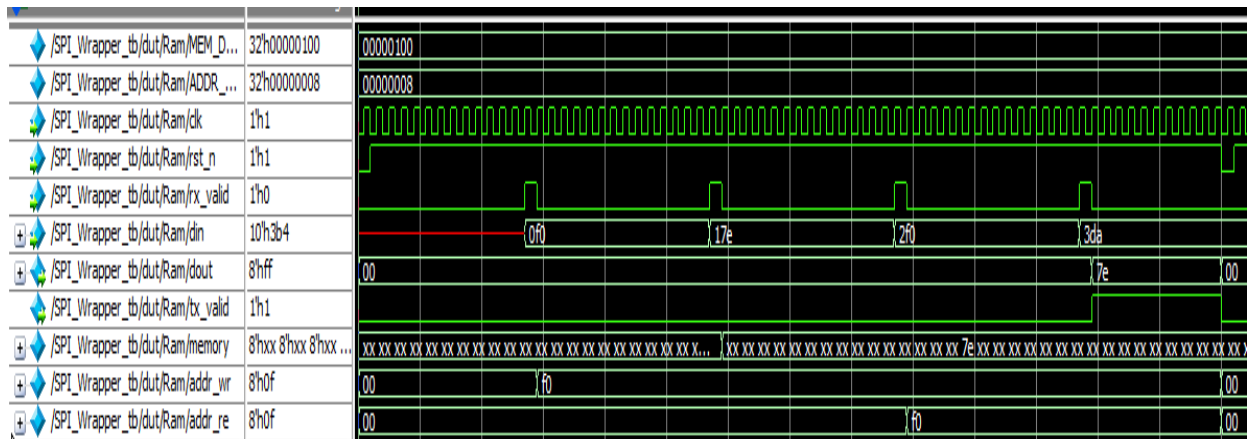


- READ_DATA state:

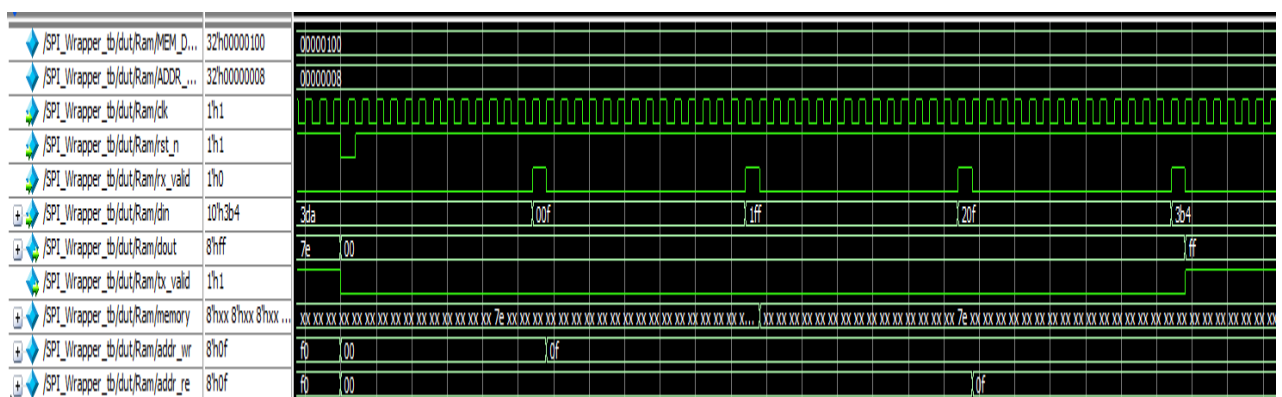


Snapshots of RAM:

- First scenario:



- Second scenario:



Synthesized Design using Vivado:

