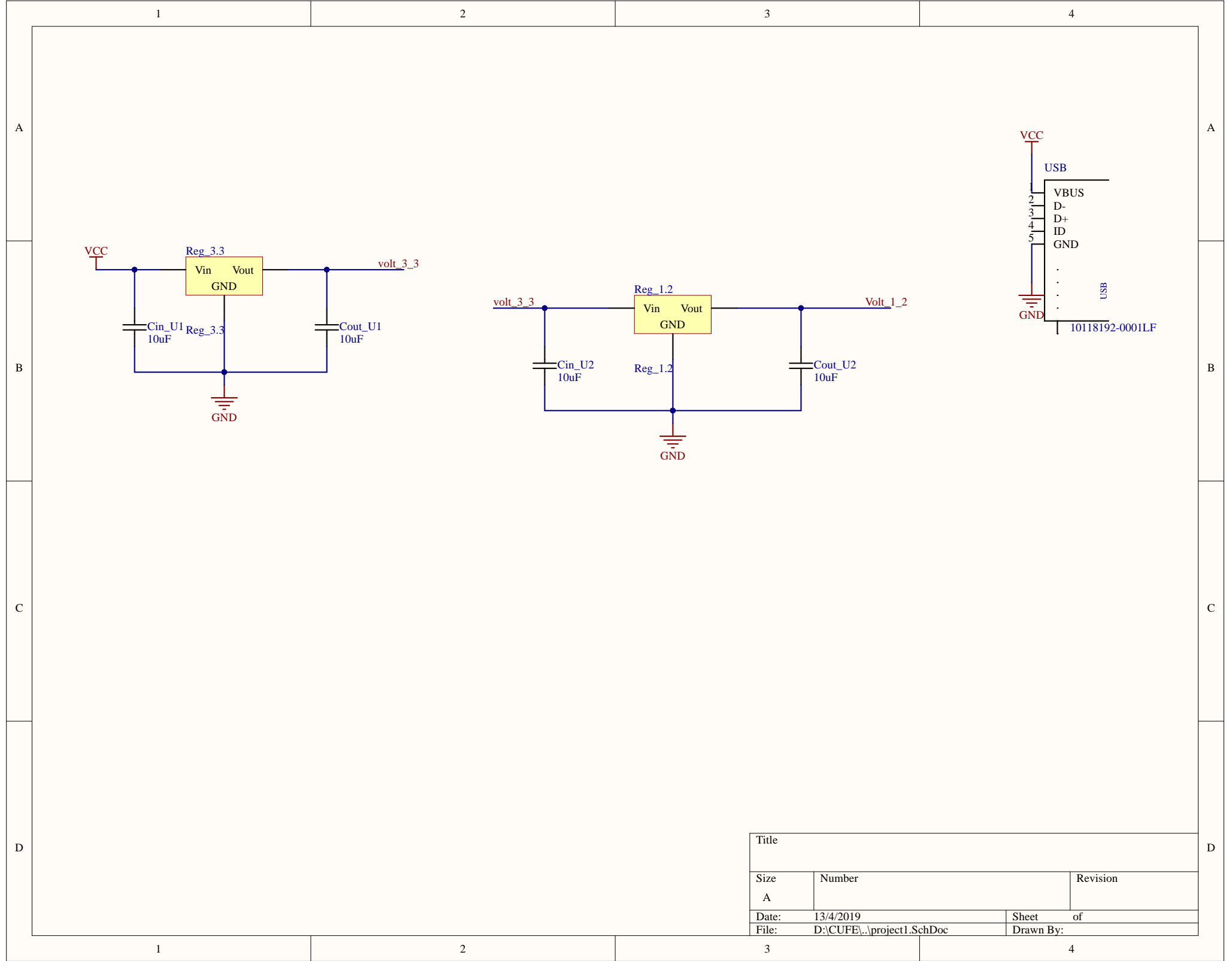
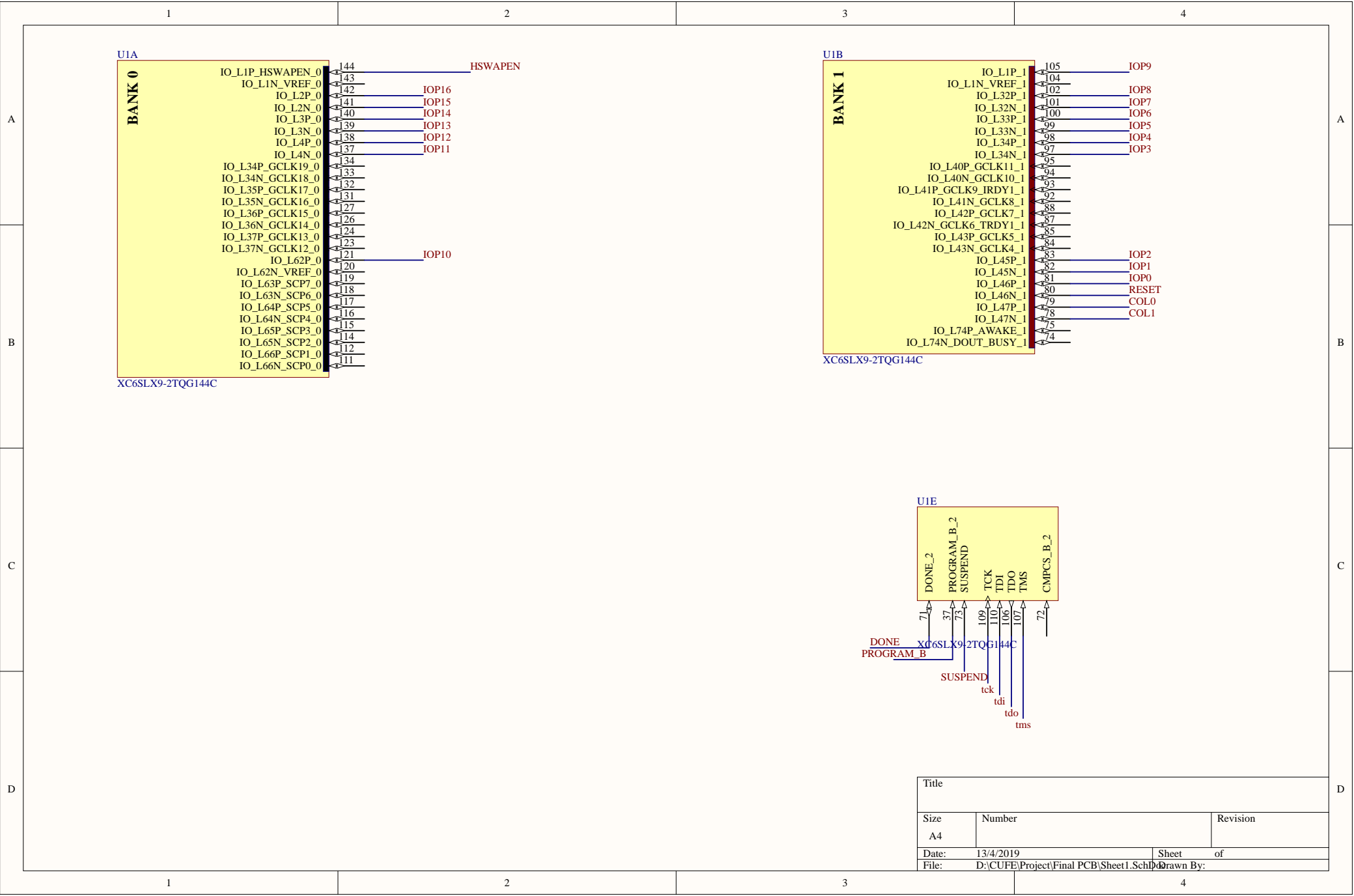
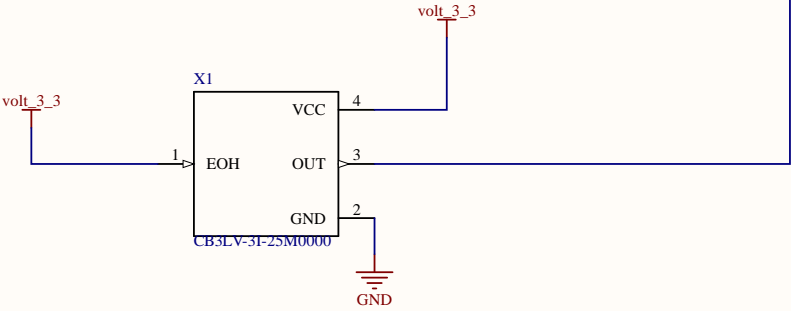
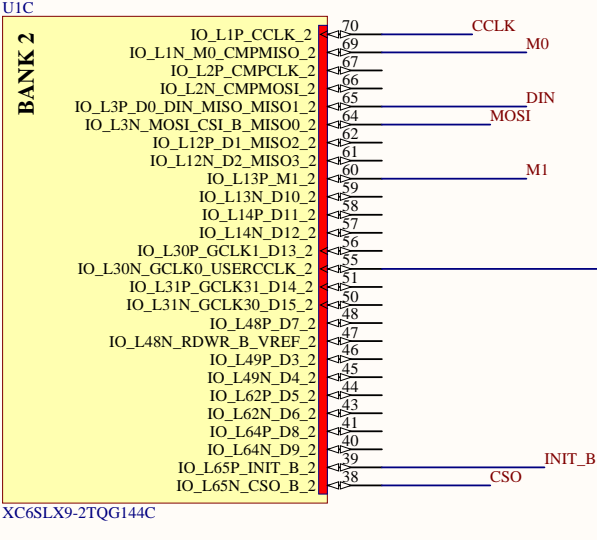
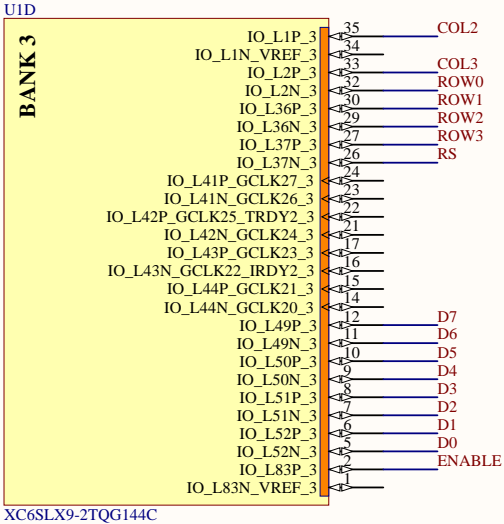


Title		
Size	Number	Revision
A4		
Date:	13/4/2019	Sheet of
File:	D:\CUFE\...\FPGA_SPI_JTAG_IO.SchDoc	Drawn By:

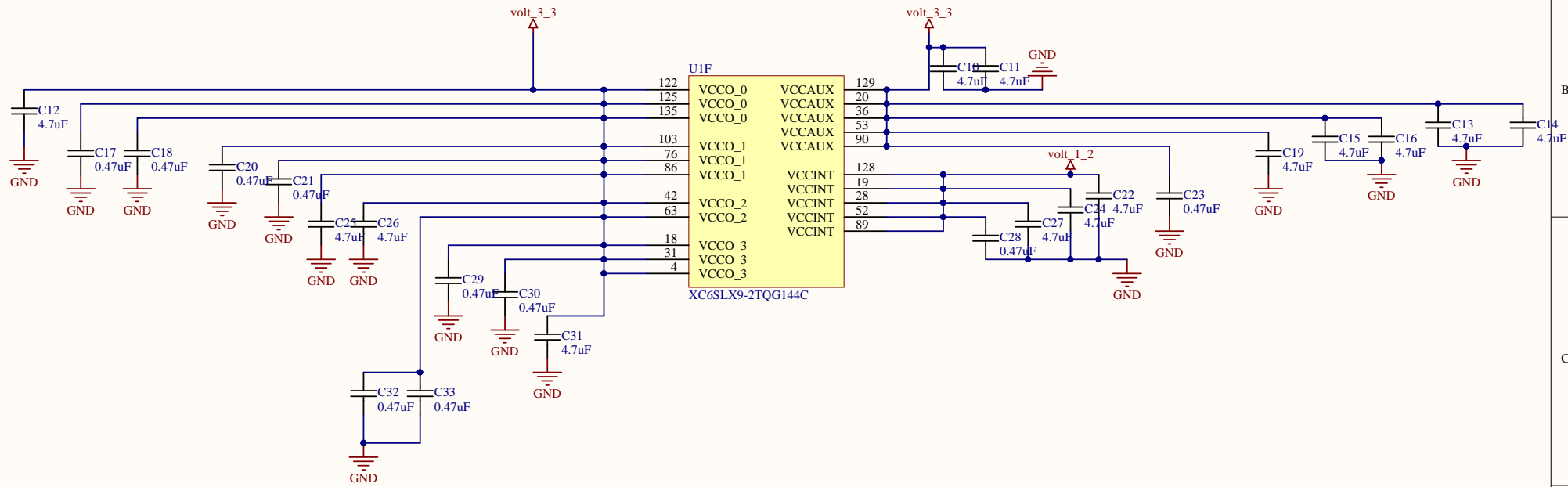




Title		
Size	Number	Revision
A4		
Date:	13/4/2019	Sheet of
File:	D:\CUFE\Project\Final PCB\Sheet1.SchDoc Drawn By:	



Title		
Size	Number	Revision
A4		
Date:	13/4/2019	Sheet of
File:	D:\CUFE\Project\Final PCB\Sheet4.SchDoc Drawn By:	



Title			
Size	Number		Revision
A4			
Date:	13/4/2019		Sheet of
File:	D:\CUFE\Project\Final PCB\Sheet5.SchDoc		Drawn By: