SIIP Scripts for Elkhart Lake Platform

Releasenote

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***Revision History***

|  |  |  |
| --- | --- | --- |
| **Date** | **Revision** | **Description** |
| July 26, 2019 | 0.6.1 | Pre-Alpha Release Candidate2 corresponding to SIIP Scripts version 0.6.1 with the following changes:   1. Overall:    1. Update Readme to match the latest code for all scripts    2. Fixed inconsistent data handling 2. SIIP Sign Script:    1. RTC#304639: Fix coding styles 3. SIIP Stitch Script:    1. Fixed #304887, Readme file is not updated based on ww29.3 Release    2. Fixed #304889, SIIPStitch tools do not have a check on missing GenFv.exe    3. Fixed #304892, Error message for missing required executable file is not updated based on latest directory name    4. Fixed #304893, Misleading error message for privatekey when stitch with invalid private key's name    5. Fixed #304918, Timeout time too short causing the failure of stitching    6. Updated siip\_stitch.py to meet flake8 and pycodestyle 4. Sub-Region Capsule:    1. Fixed #304639, Fixing coding style issues |
| July 16, 2019 | 0.6.0 | Pre-Alpha Release Candidate corresponding to SIIP Scripts version 0.6.0 with the following changes:   1. SIIP Sign Script:    1. Fixed #29951, previous fix was not complete to handle verify subcommand when two keys are provided from command line and removed dead bash script 'test\_me'    2. Update: separated FKM as a separate file    3. Fixed coding style (PEP8 and Flake8)    4. Fix script per security review feedback:       1. SHA1 shall not be allowed as hashing option       2. RSA key size shall not be less than 2048 bits long    5. Fixed signing issue for GOP/GFX/VBT    6. Fixed bug in stitching PEIM GFX driver, PEIM GFX driver should be compressed using PI\_STD flag, instead of PI\_NONE 2. SIIP Stitch Script: 3. Added stitching feature for GOP, PEM GFX and VBT images to BIOS, fixed signing as GOP image required to be signed before stitching 4. Fixed #303203 Stitch passed but temporary working directory unable to remove when we switch the suggested root path to the other path 5. Fixed #302775 Typo in README.md and SIIPStitch.py file for version description |
| June 21, 2019 | 0.2.0 | Engineering Drop Release corresponding to SIIP Scripts version 0.2.0 with the following changes:   1. Overall: 2. Add a top level README as user guide document 3. Sub-Region Capsule: 4. Add Print output message 5. Fixed STDIN input to return bytes array 6. Fixed #299938 STDIN input to return bytes array 7. Fixed #299937 and #299569 Typo in readme file 8. Removed unused files and tools 9. Removed the 2 from IntelOob2Config 10. Updated README.md consistent with the code 11. SIIP Stitch: 12. Updated check valid IP names from a list 13. Removed GUID tool Rsa2048Sah256Sign 14. Removed debug code 15. Fixed #299330 Stitch failed with error "Error using FMMT.exe" when input file in half path 16. Fixed #299322 Stitch failed with error "Error base\_tool is not located: GenSec" when manually set EDK\_TOOLS\_BIN path 17. Fixed #300930 SIIPStitch should return a error when it does not find the firmware volume that match the requirement file to replace. 18. Fixed #300551 Stitch tool hang without error message prompt out when input empty bios 19. Fixed #300553 Stich tool hang without error message prompt out when input random gen binary file as BIOS 20. Fixed #300554 Stitch tool successfully stitch empty OseFw without error 21. Fixed #299943 Wrong Command to check OpenSSL is located in README file 22. Fixed #299945 Wrong Path for siipSupport folder in README file 23. SIIP Sign:     1. Fixed #299515 Sign and verify function not able to completed successfully if input files path consists of semicolon |
| May 24, 2019 | 0.1.0 | Engineering Drop Release corresponding to SIIP Scripts version 0.1.0 with the following changes:   1. This package consists of three scripts:    1. SIIP Stitch Script ( SIIPStitch.py ): Stitch a signed image into a full IFWI image    2. New Release: SIIP Sign Script ( SIIPSign.py ): Sign a payload image according to SIIP specification    3. New Release: Sub-region Capsule Script ( GenerateSubRegionCapsule.py ): Create a UEFI capsule image containing the signed image 2. Update BSD 2 Clause Licenses for release. 3. SIIP Stitch Script:    1. Correct SIIPStitch version control from v0.6.0 to v0.5.2 for engineering drop release.    2. Fixed stitching IFWI error. 4. SIIP Sign Script: New release, version v0.2.0 5. Sub-Region Capsule Script: New release, version 0.1.0 |

# Introduction

## Purpose

This document provides system requirements, installation instructions, issues and limitations, and legal information.

To learn more about this product, see:

* New features listed in the [New in this Release](#_What's_New) section below, or in the help.
* Reference documentation listed in the [Related Documentation](#_Related_Documentation) section below
* Installation instructions can be found at ***README.md.html***

## Audience

The audience for this document is developers and product design teams who are creating system running on the Elkhart Lake platform.

## Acronyms

The following acronyms are used in this document (arranged in alphabetical order).

Table 1 Acronyms Used in Document

| Acronym | Description |
| --- | --- |
| FMMT | Intel(R) Firmware Module Management Tool |
| PSE | Intel® Programmable Services Engine |
| SIIP | SoC Independent IP |

# New in This Release

## New Features

* None in this release.

## Changes to Existing Features

* None in this release.

## Unsupported or Discontinued Features

None in this release.

# Fixed Issues

## Overall

* Update Readme to match the latest code for all scripts
* Fixed inconsistent data handling

## SIIP Stitch Script

* Fixed #304887, Readme file is not updated based on ww29.3 Release
* Fixed #304889, SIIPStitch tools do not have a check on missing GenFv.exe
* Fixed #304892, Error message for missing required executable file is not updated based on latest directory name
* Fixed #304893, Misleading error message for privatekey when stitch with invalid private key's name
* Fixed #304918, Timeout time too short causing the failure of stitching
* Updated siip\_stitch.py to meet flake8 and pycodestyle

## SIIP Sign Script

* RTC#304639: Fix coding styles

## Sub-Region Capsule

* Fixed #304639, Fixing coding style issues

# Known Issues

* Does not support Linux
* [SIIP Stitch]: There’s known issue on time out failure when execute siip\_stitch.py to stitch BIOS and PSE FW binaries. This is due to FMMT requires more time to process checking input file format.
  + Workaround: increase ‘**timeout’** in siip\_stitch.py, default is 10 seconds, recommend **timeout** to ~60 seconds.

# Related Documentation

README.md.html

# Where to Find the Release

Intel® Programmable Services Engine (PSE) , link: TBD

# Release Content

## Component Revision Numbers

Table 2 SIIP Scripts Component Revision Numbers

| Subproject (Component) | Location | Revision |
| --- | --- | --- |
| SIIP Scripts | SIIP\_Scripts\siipSign  SIIP\_Scripts\siipStitch  SIIP\_Scripts\siipSupport  SIIP\_Scripts\SubRegionCapsule  SIIP\_Scripts\siip\_support.py  SIIP\_Scripts\siip\_constants.py | N/A |
| Readme Files | README.md.html  README.md | N/A |
| Releasenote | Releasenotes.docx | 0.6.1 |

Table 3 SIIP Stitch Script Component Revision Numbers

| Subproject (Component) | Location | Revision |
| --- | --- | --- |
| SIIP Stitch Script Files | siipStitch\README.md  siipStitch\README.md.html  siipStitch\SIIPStitch.py | 0.6.1 |

Table 4 SIIP Sign Script Component Revision Numbers

| Subproject (Component) | Location | Revision |
| --- | --- | --- |
| SIIP Sign Script Files | siipSign\README.md  siipSign\README.md.html  siipSign\SIIPSign.py | 0.6.1 |

Table 5 Sub-Region Capsule Script Component Revision Numbers

| Subproject (Component) | Location | Revision |
| --- | --- | --- |
| Sub-Region Capsule Script Files | SubRegionCapsule\GenerateCapsule.py  SubRegionCapsule\GenerateSubRegionCapsule.py  SubRegionCapsule\README.md  SubRegionCapsule\README.md.html  SubRegionCapsule\SubRegionDescriptor.py  SubRegionCapsule\SubRegionImage.py | 0.6.1 |
| Common Tools and Files | SubRegionCapsule\Common  SubRegionCapsule\CommonDataClass  SubRegionCapsule\Examples | N/A |

Table 6 SIIP Scripts Support Component Revision Numbers

| Subproject (Component) | Location | Revision |
| --- | --- | --- |
| SIIP Scripts Support Files | siipSupport\Bin\Win32\FMMT.exe  siipSupport\Bin\Win32\FmmtConf.ini  siipSupport\Bin\Win32\GenFfs.exe  siipSupport\Bin\Win32\GenFv.exe  siipSupport\Bin\Win32\GenSec.exe  siipSupport\Bin\Win32\LzmaCompress.exe  siipSupport\Bin\Win32\rsa\_helper.py | N/A |

# Hardware and Software Compatibility

Elkhart Lake platform

## BIOS/Firmware Version

* TBD

## Supported Operating Systems

* Window 10