

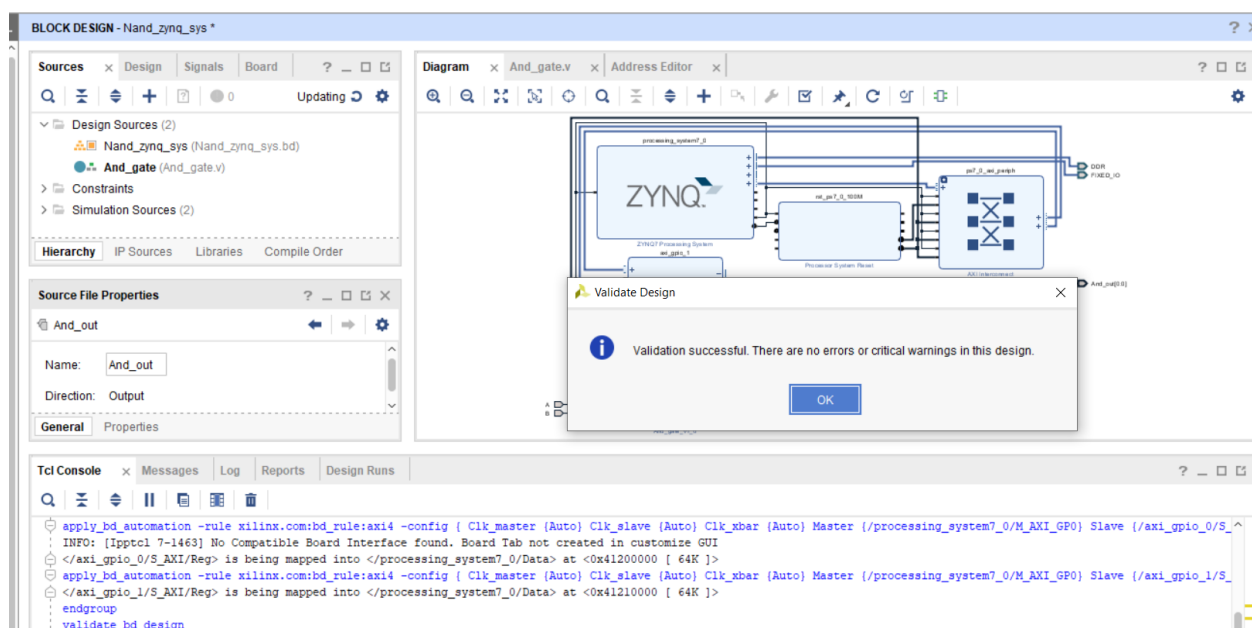


NAND GATE WITH ZYNQ SYSTEM

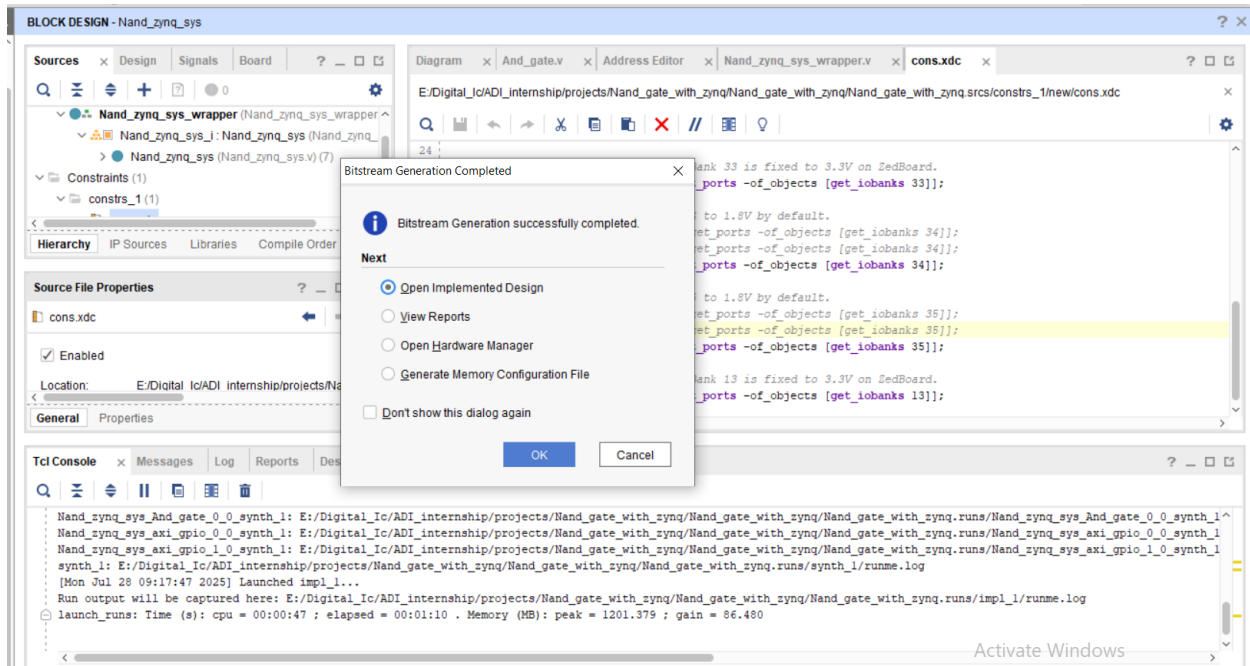
Mohamed Haysam Afify



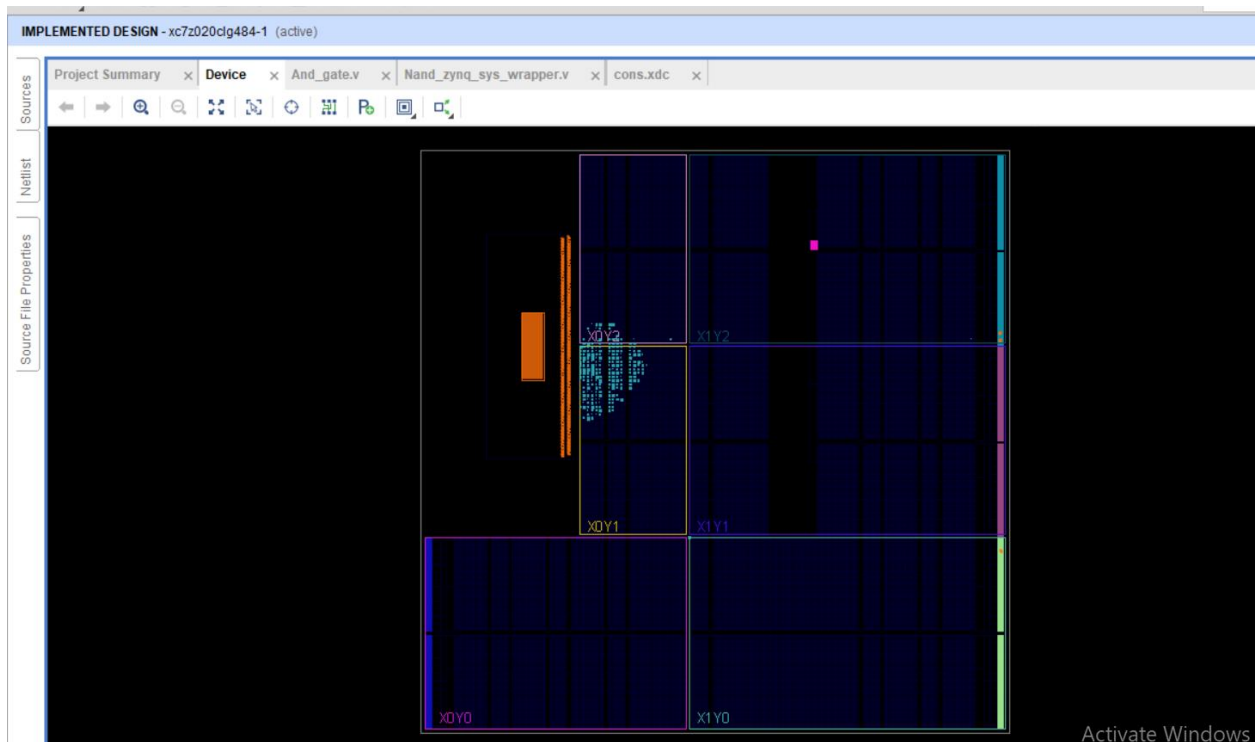
- 1- After selecting the ZED board and creating And gate with Verilog code we have create block diagram and connect Zynq processor with And gate by AXI Gpio



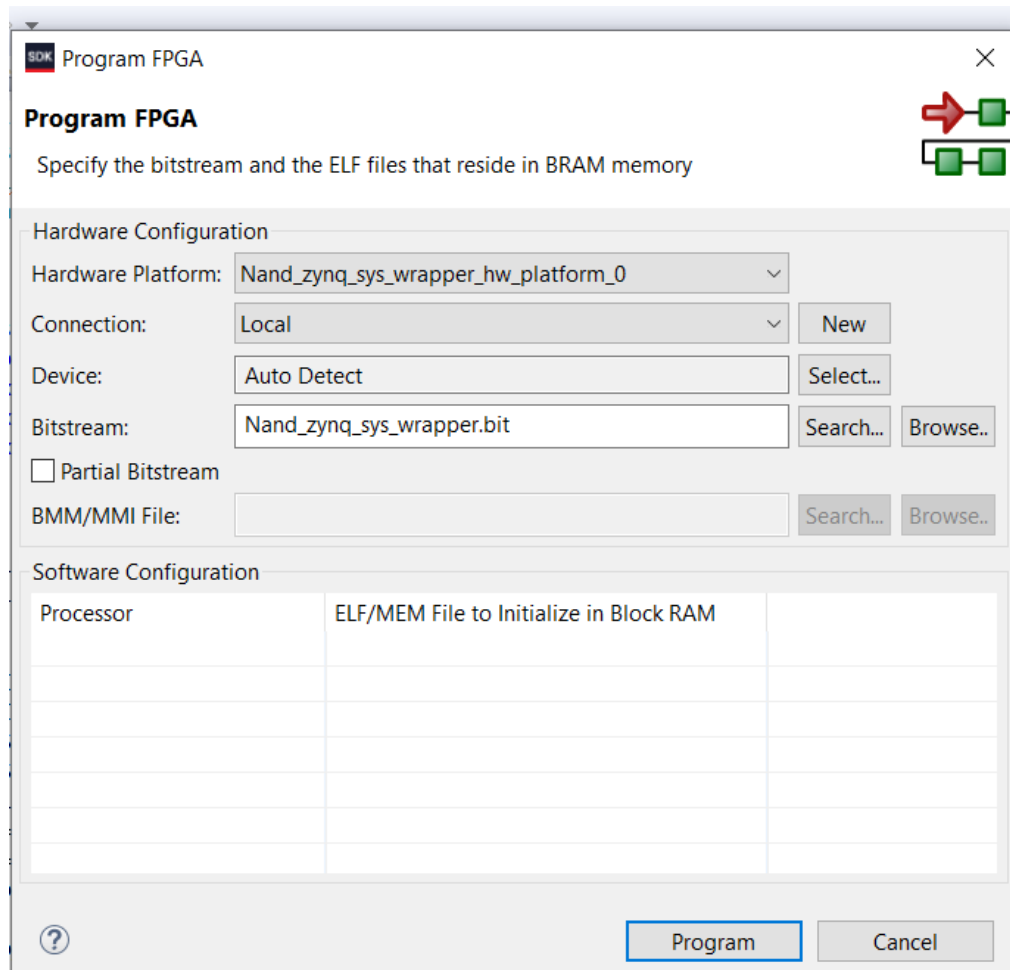
3- Create constraint file and run bitstream



4- Implementation design



- 6- program our board and test the functionality in real world but unfortunately I don't have the board



7-

29. Finally, you need to run your C-code on the processor to complete the NOR gate.

