

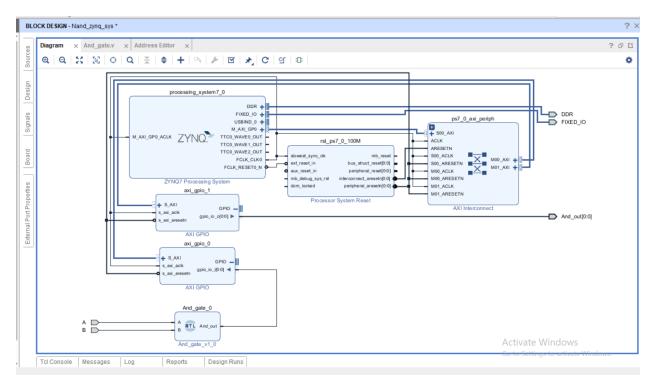
## NAND GATE WITH ZYNQ SYSTEM

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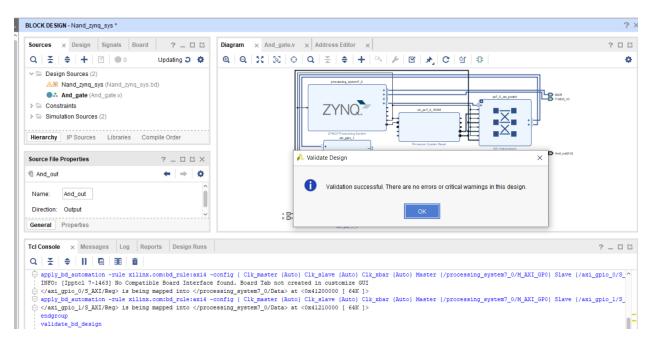


Implement a NAND gate by designing an AND gate in hardware and using a C program running on the Zynq processor to perform the inversion.

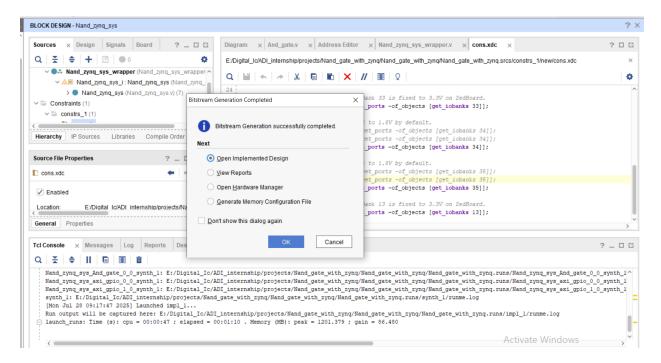
1- After selecting the ZED board and creating And gate with Verilog code we have create block diagram and connect Zynq processor with And gate by AXI Gpio



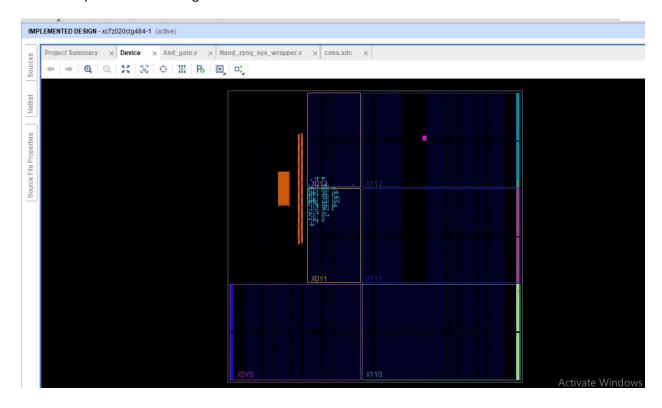
2- Validation successful



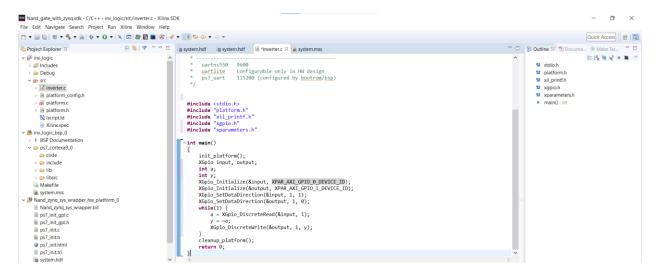
## 3- Create constraint file and run bitstream



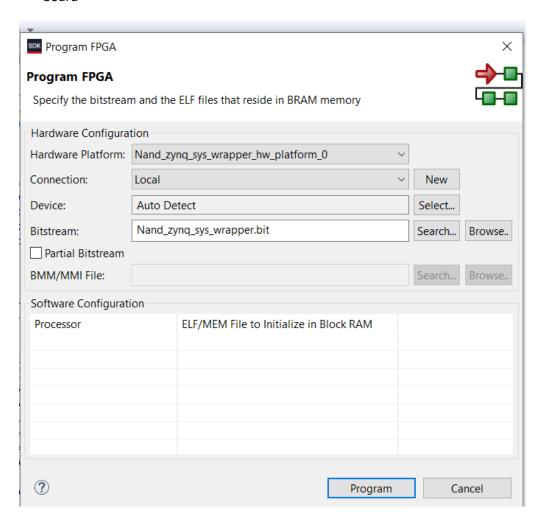
## 4- Implementation design



5- After that Export the hardware and launch SDK to program processor and peripherals to make not gate



6- program our board and test the functionality in real world but unfortunately I don't have the board



29. Finally, you need to run your C-code on the processor to complete the NOR gate.

