

Lec 1 Comp. Architecture

Slide 1:-

Computer Architecture Definition:-

* قبل ما تبدأ تشتغل على ال Design لازم تترن ال User needs
مثلا هتستخدمه على شان Games ولا تشتغل كوشل
ال User هيفرق معاه ال Components رخيصة أو غالية ؟

Slide 2, 3, 4, 5:-

* ال Computer متكون من :-
(1) Software
(2) Hardware

* ال مهندس بييجي يشتغل على Design لازم يقابل
Trade offs يعني لازم يخسر حاجة في مقابل انه يكتسب حاجة
تانية (السعر مثلا بالنسبة للجودة).

Components of a computer:-

- * processor → عقل الكمبيوتر ياخذ orders وينفذها.
- * Memory (instruction, Data), I/O Devices (Speakers, keyboa
Ex. [Embedded Systems (ADC, Sensors, ...)]
- * Embedded system processor → Micro Controller.

Course Contents:-

- * How to make a Computer go Fast? → Dr/Omar Nasr.
- * How different components Communicate? → Dr/khattab.
→ with each other or with the physical world.
- (1) within a computer system (interconnection).
→ How components Communicate with each other?
- (2) interfacing. How components Communicate with
the outside world?

Slide 8 :-

Buses :-

هې channel بنسټ لاندې devices انځور کول دي
وېشيل Data او Address او Control Structures
Ex.:- USB

* Bus variations :-

* on chip Buses

/ off chip Buses

چوځه او chip منځ کې ليدل شوي

يعني پيوصل او processor
پاڼې حاحه پوره (شوفاکي)

* Serial Buses

/ parallel Buses

او Data سټرټ وړاندې
[Ex. USB] د سټرټ واحد

او Data سټرټ د کړا
Bit

* Wired Buses

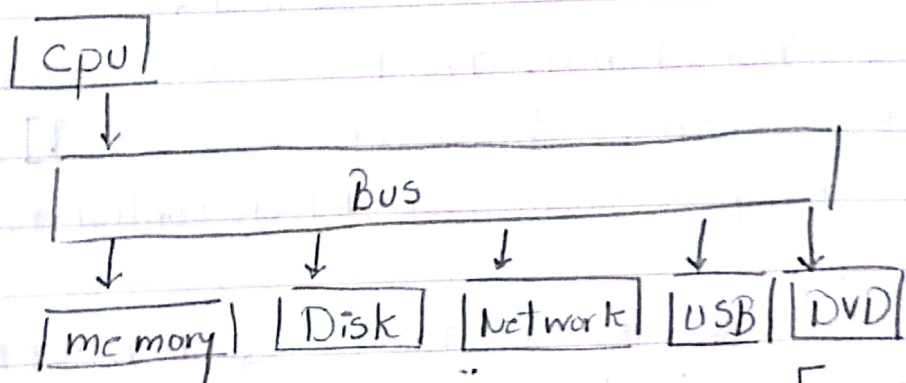
/ wireless Buses

(هډسټل عليه)

(Still on Research.)

Slide 9

Ex.:- off-chip Buses :-



* دې مثال Example ده چې مشكله وه چې انځور کول memory اسرځ
دې Disk و اسرځ دې Network دې پيوصل او memory
انځور کول دې Devices انځور کول دې پيوصل او memory
فالتي دې پيوصل او system کله
ازادې دې مشكله دې ده؟

Slide 10 :-

حل المشكلة ده هي اني اقسم ال Devices الى سرعاتهم
مقاربة واحطهم على Bus واحد ونكه زودت Block و
هو ال I/O Controller و فايدته هي انه لو ال CPU عايز يكلم
ال DVD ال memory مش هتضطر لتسني ال DVD كد ما تخلص
Note :- * ال Bus لازم يبقى اسرع من اسرع Device

Ex.:- intel Example → System Bus 800 MHz, 604 GB/sec
Device ← اسرع من اسرع
pentium 4 chip → on chip Bus.