SPECIFICATIONS

CUSTOMER . CDE030

SAMPLE CODE . SE12864LRF-042-H-Q

MASS PRODUCTION CODE PE12864LRF-042-H-Q

SAMPLE VERSION . 01

SPECIFICATIONS EDITION . 002

DRAWING NO. (Ver.) . DTE-08339 (Ver.0)

PACKAGING NO. (Ver.) : DPK-08582 (Ver.0)

Customer Approved

Date:

Approved	Checked	Designer		
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		\ HK RD APR /		

Preliminary specification for design input

Specification for sample approval

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RECORDS OF REVISION

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1. SPECIFICATIONS

1.1 Features

Item	Standard Value
Display Type	128*64 Dots
LCD Type	FSTN, Positive, Transflective, Extended Temperature
Driver Condition	LCD Module: 1/65 Duty, 1/9 Bias
Viewing Direction	6 O'clock
Backlight	Yellow-Green LED B/L
Weight	29.4 g
Interface	4-line serial interface
Other(controller / driver IC)	ST7567
	THIS PRODUCT CONFORMS THE ROHS OF PTC
ROHS	Detail information please refer web side :
	http://www.powertip.com.tw/news/LatestNews.asp

1.2 Mechanical Specifications

Item	Standard Value		
Outline Dimension	80.0(L) *54.0(w) (except FPC length) * 9.7(H)		
Viewing Area	70.7 (W) * 38.8 (L)		
Active Area	66.545(W) *33.265 (L)	mm	
Dot Size	0.505 (W) * 0.505 (L)	mm	
Dot Pitch	0.52(W) * 0.52 (L)	mm	

Note: For detailed information please refer to LCM drawing

1.3 Absolute Maximum Ratings

Item	Symbol	Condition	Min.	Max.	Unit
Power Supply Voltage	VDD	_	-0.3	3.6	V
LCD Driver Supply Voltage	V0-XV0	_	-0.3	16	V
Operating Temperature	T _{OP}	_	-20	70	$^{\circ}\!\mathbb{C}$
Storage Temperature	T_{ST}	_	-30	+80	$^{\circ}\!\mathbb{C}$
Storage Humidity	H_D	Ta < 60 °C	-	90	%RH



1.4 DC Electrical Characteristics

 $V_{SS} = 0V$, $Ta = 25^{\circ}C$

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Logic Supply Voltage	VDD	-	2.7	3.0	3.3	V
"H" Input Voltage	$V_{ m IH}$	-	0.7 VDD	-	VDD	V
"L" Input Voltage	$V_{ m IL}$	-	VSS	-	0.3VDD	V
"H" Output Voltage	V_{OH}	I _{OUT} =1mA,VDD=3.0V	0.8VDD	-	VDD	V
"L" Output Voltage	$V_{ m OL}$	I _{OUT} =-1mA,VDD=3.0V	Vss	-	0.2 VDD	V
Cymaly Cymant		V _{DD} =3.0V;V _{OP} =8.5V; Pattern= Full display	-	0.31	2	A
Supply Current	I_{dd}	V _{DD} =3.0V;V _{OP} =8.5V; Pattern= Horizontal line*1	-	0.75	1.5	mA
		-20°C	8.5	8.7	8.9	
LCM Driver Voltage	V _{OP} *2	25℃	8.3	8.5	8.7	V
		70℃	8.2	8.4	8.6	

NOTE: *1 The Maximum current display;

*2 The VOP test point is the capacitance of C10.





1.5 Optical Characteristics

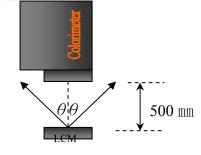
LCD Panel: 1/65Duty · 1/9Bias · $V_{LCD} = 8.5$ V · Ta = 25°C

Item		Symbol	Conditions	Min.	Тур.	Max.	Unit	Reference
Response Time	Rise	tr		-	100	150	ms	Note2
Response Time	Fall	tf		-	250	375		Notez
	Тор	ΘY+	C≥2.0,	-	- (40		
Viewing angle	Bottom	ΘΥ-	Ø = 270°	-	-	40	Dag	Notes 1
range	Left	ΘX-		-	-	45	Deg.	Notes 1
	Right	ΘX+		-	-	45		
Contrast Rat	rio	С	$\theta = 0^{\circ},$ $\emptyset = 270^{\circ}$	2	4	ì	-	Note 3
Average Bright (with LCD)		IV		2.5	3.0	-	cd/m ²	
Wavelengtl	n	Hue	IF=80mA	569	571	576	nm	Note 4
Uniformity '	*1	△B		70	1	-	%	

Note 4

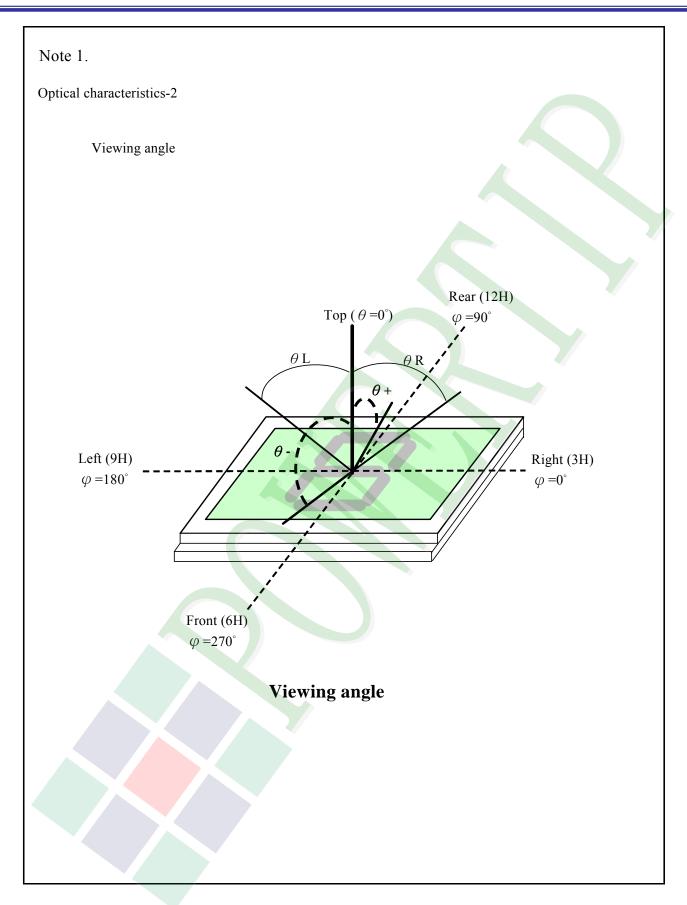
- $1 : \triangle B = B(min) / B(max) * 100\%$
- 2 : Measurement Condition for Optical Characteristics:
 - a : Environment: 25°C±5°C / 60±20%R.H , no wind , dark room below 10 Lux at typical lamp current and typical operating frequency.
 - b: Measurement Distance: 500 ± 50 mm $\theta = 0^{\circ}$
 - c: Equipment: TOPCON BM-7 fast, (field 1°), after 10 minutes operation.
 - d: The uncertainty of the C.I.E coordinate measurement ± 0.01 , Average Brightness $\pm 4\%$



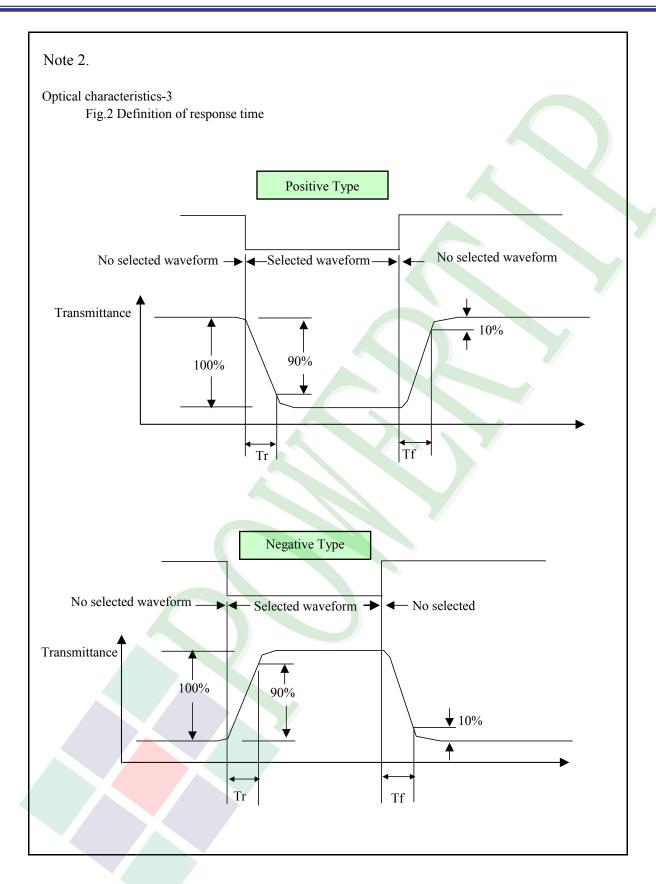


Colorimeter=BM-7 fast











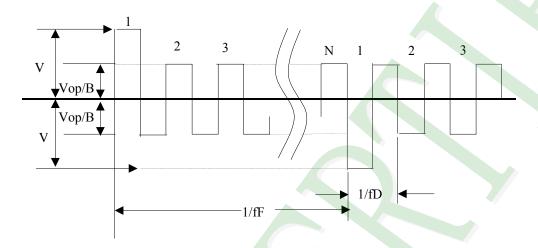
Electrical characteristics-2

※2 Drive waveform

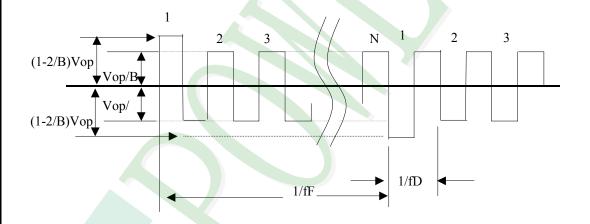
Vop: Drive voltage fF: Frame frequency 1/B: Bias fD: Drive frequency

N: Duty

(1) Selected waveform



(2) Non- Selected wave form

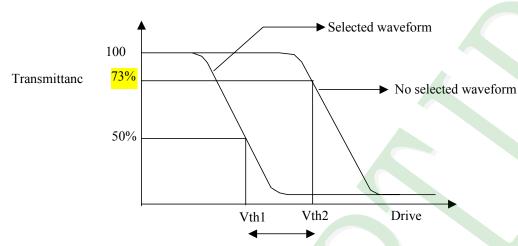


Note:

Frame frequency is defined as follows: Common side supply voltage peak - to - peak /2 = 1 period







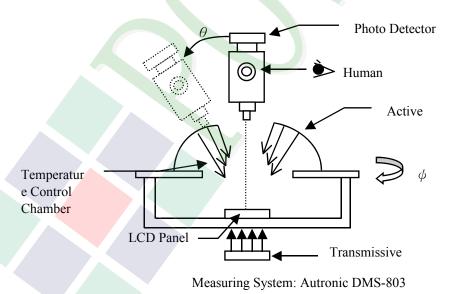
Active voltage range

	Vth1	Vth2
View direction	10°	40 °
Drive waveform	(Selected waveform)	(No selected waveform)
Transmittance	50%	73%

※1 Contrast ratio

= (Brightness in OFF state) / (Brightness in ON state)

Outline of Electro-Optical Characteristics Measuring System





1.6 Backlight Characteristics

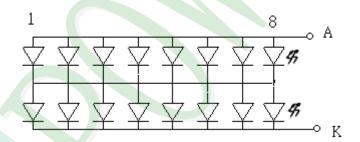
LCD Module with LED Backlight

Maximum Ratings

Item	Symbol	Conditions	Ratings	Unit
Forward Current	IF	Ta =25°C	80	mA
Power Dissipation	PD	Ta =25°℃	336	mW

Electrical / Optical Characteristics

Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward Voltage	VF	IF=80mA		4.2	4.6	V
Average Brightness (without LCD)	IV	IF=80mA	9.6	12.0	_	cd/m ²
Color	YELLOW-GREEN					





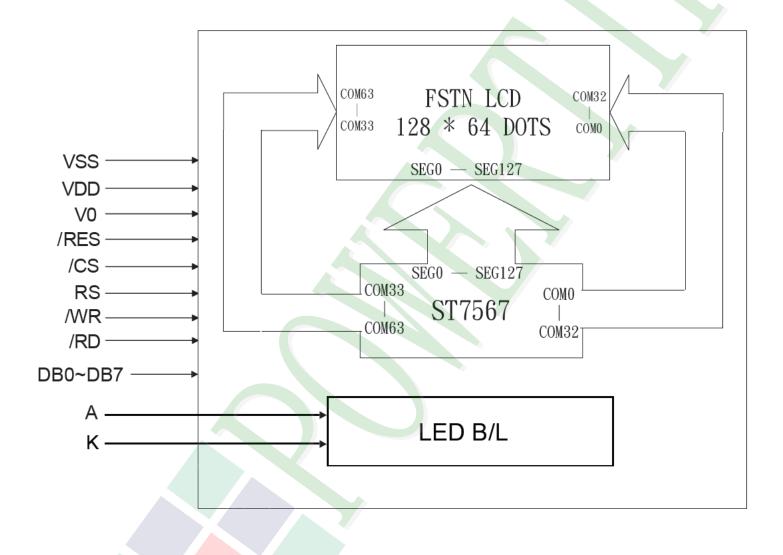
2. MODULE STRUCTURE

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

* See Appendix

2.1.2 Block Diagram





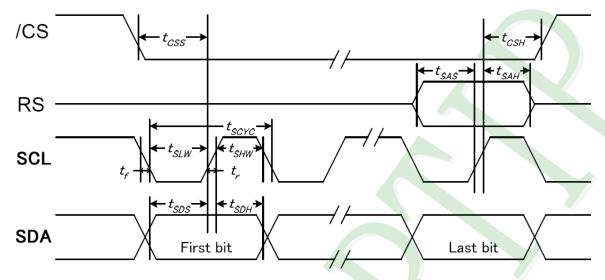
2.2 Interface Pin Description

Pin No.	Symbol	Function
1	VSS	Power Supply (VSS=0)
2	VDD	Power Supply (VDD>VSS)
3	V0	NO Connection
4	/RES	Controller reset (module reset)
5	/CS	Used to enter chip select signal
6	RS	Select control data or display data for read/write operation "L"=control data "H"=display data
7	/WR	Must be connected to VDD
8	/RD	Must be connected to VDD
9	DB0	Must be connected to VDD
10	DB1	Must be connected to VDD
11	DB2	Must be connected to VDD
12	DB3	Must be connected to VDD
13	DB4	Must be connected to VDD
14	DB5	Must be connected to VDD
15	DB6(SCL)	Serial data input
16	DB7(SDA)	serial clock input
17	A	Power supply LED backlight(+)
18	K	Power supply LED backlight(-)



2.3 Timing Characteristics

System Bus Timing for 4-Line Serial Interface



(VDD = 3.3V , Ta =-30~85°C)

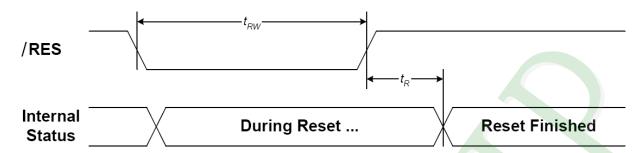
Item	Signal	Symbol	Condition	Min.	Max.	Unit
Serial clock period		tSCYC		50	_	
SCLK "H" pulse width	SCL	tSHW		25	_	
SCLK "L" pulse width		tSLW		25	_	
Address setup time	RS	tSAS		20	_	
Address hold time		tSAH		10	_	ns
Data setup time	SDA	tSDS		20	_	
Data hold time	SDA	tSDH		10	_	
CSB-SCLK time	/CS	tCSS		20	_	
CSB-SCLK time	103	tCSH		40	_	

(VDD = 2.8V , Ta =-30~85°C)

Item	Signal	Symbol	Condition	Min.	Max.	Unit
Serial clock period		tSCYC		100	_	
SCLK "H" pulse width	SCL	tSHW		50	_	
SCLK "L" pulse width		tSLW		50	_	
Address setup time	RS	tSAS		30	_	
Address hold time	No.	tSAH		20	_	ns
Data setup time	SDA.	tSDS		30	_	
Data hold time	SDA	tSDH		20	_	
CSB-SCLK time	/CS	tCSS		30	_	
CSB-SCLK time	/08	tCSH		60	_	



Hardware Reset Timing



 $(VDD = 3.3V, Ta = -30 \sim 85^{\circ}C)$

Item	Symbol	Condition	Min.	Max.	Unit
Reset time	tR		_	1.0	
Reset "L" pulse width	tRW		1.0		us

(VDD = 2.8V , Ta =-30~85°C)

Item	Symbol	Condition	Min.	Max.	Unit
Reset time	tR		-	2.0	
Reset "L" pulse width	tRW		2.0	_	us





2.4 Display command

INSTRUCTION AS R/W COMMAND BYTE					DESCRIPTION						
INSTRUCTION	A0	(RWR)	D7	D6	D5	D4	D3	D2	D1	D0	DESCRIPTION
(1) Display ON/OFF	0	0	1	0	1	0	1	1	1	D	D=1, display ON D=0, display OFF
(2) Set Start Line	0	0	0	1	S5	S4	S3	S2	S1	S0	Set display start line
(3) Set Page Address	0	0	1	0	1	1	Y3	Y2	Y1	Y0	Set page address
(4)	0	0	0	0	0	1	X7	X6	X 5	X4	Set column address (MSB)
Set Column Address	0	0	0	0	0	0	Х3	X2	X1	X0	Set column address (LSB)
(5) Read Status	0	1	0	MX	D	RST	0	0	0	0	Read IC Status
(6) Write Data	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write display data to RAM
(7) Read Data	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read display data from RAM
(8) SEG Direction	0	0	1	0	1	0	0	0	0	MX	Set scan direction of SEG MX=1, reverse direction MX=0, normal direction
(9) Inverse Display	0	0	1	0	1	0	0	1	1	INV	INV =1, inverse display INV =0, normal display
(10) All Pixel ON	0	0	1	0	1	0	0	1	0	AP	AP=1, set all pixel ON AP=0, normal display
(11) Bias Select	0	0	1	0	1	0	0	0	1	BS	Select bias setting 0=1/9; 1=1/7 (at 1/65 duty)
(12) Read-modify-Write	0	0	1	1	1	0	0	0	0	0	Column address increment: Read:+0 , Write:+1
(13) END	0	0	1	1	1	0	1	1	1	0	Exit Read-modify-Write mode
(14) RESET	0	0	1	1	1	0	0	0	1	0	Software reset
(15) COM Direction	0	0	1	1	0	0	MY	·	-	-	Set output direction of COM MY=1, reverse direction MY=0, normal direction
(16) Power Control	0	0	0	0	1	0	1	VB	VR	VF	Control built-in power circuit ON/OFF
(17) Regulation Ratio	0	0	0	0	1	0	0	RR2	RR1	RR0	Select regulation resistor ratio
(18) Set EV	0	0	1	0	0	0	0	0	0	1	Double command!! Set
(10) Set LV	0	0	0	0	EV5	EV4	EV3	EV2	EV1	EV0	electronic volume (EV) level
40.0.40	0	0	1	1	1	1	1	0	0	0	Double command!!
(19) Set Booster	0	0	0	0	0	0	0	0	BL1	BL0	Set booster level: 00=4X, 01=5X, 10=6X
(20) Power Save	0	0			Cor	mpound	Comm	and			Display OFF + All Pixel ON
(21) NOP	0	0	1	1	1	0	0	0	1	1	No operation
(22) Test	0	0	1	1	1	1	1	1	1	-	Do NOT use. Reserved for testing.

Note: Symbol "-" means this bit can be "H" or "L".

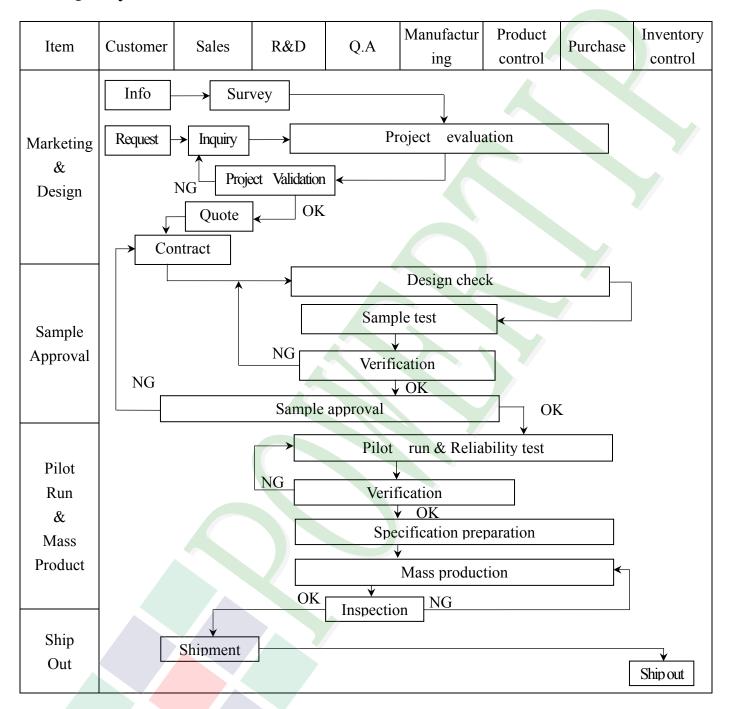
2.5 Jumper

J1(2.3)/J2(2.3)/J(2.3)/J6:SHORT;OTHER:OPEN

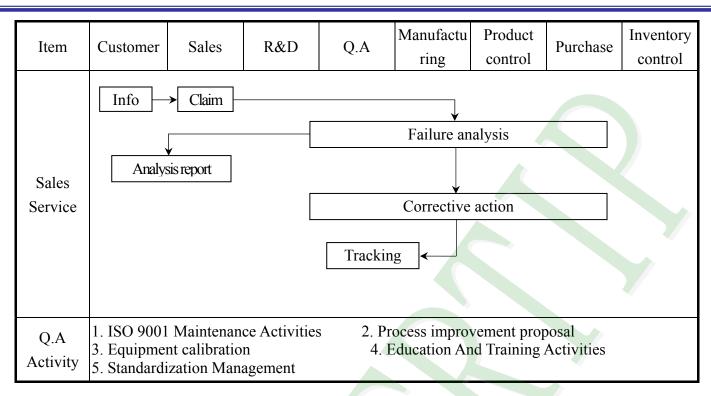


3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart









3.2 Inspection Specification

- ◆Scope: The document shall be applied to LCD Module for Monotype and Color STN (Ver. 03).
- ◆Inspection Standard: MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.
- ◆Equipment : Gauge · MIL-STD · Powertip Tester · Sample
- ◆Defect Level: Major Defect AQL: 0.4; Minor Defect: AQL: 1.5.
- ◆OUT Going Defect Level: Sampling.
- ◆Manner of appearance test :
 - (1). The test be under 20W×2 fluorescent light 'and distance of view must be at 30 cm.
 - (2). Standard of inspection: (Unit: mm)
 - (3). The test direction is base on about around 45° of vertical line. (Fig. 1)
 - (4). Definition of area. (Fig. 2)

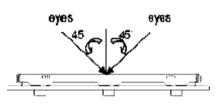


Fig.1

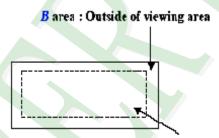


Fig. 2 A area: viewing area

♦ Specification:

NO	Item	Criterion	level
		1, 1 The part number is inconsistent with work order of Production.	Major
01	Product condition	1. 2 Mixed production types.	Major
		1.3 Assembled in inverse direction.	Major
0 2	Quantity	2. 1 The quantity is inconsistent with work order of production.	Major
03	Outline dimension	3.1 Product dimension and structure must conform to Structure diagram.	Major
		4. 1 Missing line character and icon.	Major
04	Electrical Testing	4. 2 No function or no display.	Major
		4, 3 Output data is error.	Major
		4. 4 LCD viewing angle defect.	Major
		4. 5 Current consumption exceeds product specifications.	Major



◆Specification For Monotype and Color STN:

(Yer. 03)

- -	CHICATION FOR IVION	otype and Color STN:				Yer. 03)	
NO	Item	C	riteri	on		level	
	Black or white dot · scratch · contamination	 5, 1 Round type: 5, 1, 1 display only: • White and black spots on 4 white or black spots pr • Densely spaced: NO more 	esent.				
		5. 1. 2 Non-display :					
	D 1.	Dimension		Acceptance (Q'ty)	Minor	
	Round type	(diameter : Φ)	A area		B area		
	⇔ l√ I⊀⊥	Φ ≤ 0 . 10	Acce	pt no dense			
05	Y	$0.10 < \Phi \leq 0.20$		3	T		
เอ	-	$0.20 < \Phi \leq 0.30$	2		Ignore		
	$\Phi = (x+y)/2$	Total quantity		4			
		5. 1. 3 Line type:					
	Line type	Dimension	Acceptance (Q'ty)				
		Length (L) Width (W)			B area		
	_ /¥w	W ≦ (). 03	Accept no dens	se e		
	→ L +	$L \le 3.0 \qquad 0.03 < W \le 0$	─ 4		Ignore		
		$L \le 2.5$ 0.05 $< W \le 0.$		4			
		W >0	. טוט	Asre	ound type		
		Dimension		Aggentange	(024)		
		(diameter : Φ)	Acceptance A area Accept no dense		B area		
		Φ ≤ 0 . 20			1 2 444		
	Polarizer	$0.20 < \Phi \leq 0.50$	3		1	Minor	
06	Bubble	$0.50 < \Phi \le 1.00$		2	Ignore		
		$\Phi > 1.00$	0		-		
		Total quantity	4				
		family		•			



◆Specification For Monotype and Color STN: (Yer. 03)

NO	Item	Criter	ion	Leve
		Symbols: X: The length of crack Z: The thickness of crack t: The thickness of glass	Y : The width of crack. W : terminal length a : LCD side length	
		7. 1 General glass chip: 7. 1. 1 Chip on panel surface and c	rack between panels:	
		Z	Y	
07	The crack of glass	SPY (OK)	[NG]	Min
		Seal width Z	Y	
		X Y	Z	
		≦ a Crack can't ente viewing area	r ≦1/2 t	
		≤ a Crack can't exceed half of SP width		



◆Specification For Monotype and Color STN: (Ver. 03)

7. 2 Protrusion over terminal: 7. 2. 1 Chip on electrode pad: $ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	NO	Item	Criterion	Level
The crack of glass The crack of $X = 1/2 t$ The crack can't enter $X = 1/2 t$ The crack can't ente			X: The length of crack Z: The thickness of crack t: The thickness of glass X: The width of crack W: terminal length a: LCD side length	
The crack of glass The crack of $\leq 1/5$ a Crack can't exceed the half of SP width. 7.2 Protrusion over terminal: 7.2.1 Chip on electrode pad: $X $			X Y Z	
The crack of glass 7. 2 Protrusion over terminal: 7. 2. 1 Chip on electrode pad: $ \begin{array}{c ccccccccccccccccccccccccccccccccccc$			1 1 2 1/3 9 1 1 1 1 2 2 1/2 1 1 1	
glass 7.2 Protrusion over terminal: 7.2.1 Chip on electrode pad: $ \begin{array}{c ccccccccccccccccccccccccccccccccccc$		The crack of	1 5 75 9 1 1 77 7 7 5 7 5 7 1 1	
7.2.1 Chip on electrode pad: X X Y	07		7.9 Destruction over towards	Minor
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$				
Front \leq a \leq 1/2 W \leq t			X Y Z X Y Z	
Front \leq a \leq 1/2 W \leq t			X Y Z	
Back Neglect				
Duch Pregrett			Back Neglect	



◆Specification For Monotype and Color STN:

(Yer. 03)

NO	Item	Criterion	Level
		Symbols: X: The length of crack Z: The thickness of crack t: The thickness of glass X: The width of crack W: terminal length a: LCD side length	
07	The crack of glass	7. 2. 2 Non-conductive portion: X	Minor
		X	



◆Specification For Monotype and Color STN: (Yer. 03)

NO	Item	Criterion	Level
		8. 1 Backlight can't work normally.	Major
08	Backlight elements	8. 2 Backlight doesn't light or color is wrong.	Major
		8. 3 Illumination source flickers when lit.	Major
	General	9. 1 Pin type must match type in specification sheet.	Major
		9. 2 No short circuits in components on PCB or FPC.	Major
09		9. 3 Product packaging must the same as specified on packaging specification sheet.	Minor
		9. 4 The folding and peeled off in polarizer are not acceptable.	Minor
		9. 5 The PCB or FPC between B/L assembled distance (PCB or FPC) is ≤1. 5 mm.	Minor



4. RELIABILITY TEST

4.1 Reliability Test Condition

NO.	TEST ITEM	TEST CONDITION				
1	High Temperature Storage Test	Keep in 80 ±2°C 96 hrs				
		Surrounding temperature, then storage at normal condition 4hrs				
2	Low Temperature Storage Test	Keep in -30 ±2°C 96 hrs				
		Surrounding temperature, then storage at normal condition 4hrs				
	****	Keep in +60°C/90%RH duration for 96 hrs				
3	High Humidity Storage	Surrounding temperature, then storage at normal condition 4hrs				
		Air Discharge: Contact Discharge:				
		Apply 2 KV with 5 times Apply 250V with 5 times				
		Discharge for each polarity +/- discharge for each polarity +/-				
		1. Temperature Ambient:15°C ~35°C				
		2. Humidity relative: 30%~60%				
4	ESD Test	3. Energy Storage Capacitance(Cs+Cd):150pF±10%				
		4. Discharge Resistance(Rd):330 Ω±10%				
		5. Discharge, mode of operation:				
		Single Discharge (time between successive discharges at least 1 s)				
		(Tolerance If the output voltage indication: ±5%)				
		$-20^{\circ}\text{C} \rightarrow 25^{\circ}\text{C} \rightarrow 70^{\circ}\text{C} \rightarrow 25^{\circ}\text{C}$				
5	Temperature Cycling Test	(30mins) (5mins) (30mins) (5mins)				
	Temperature Cyching Test	10 Cycle				
		Surrounding temperature, then storage at normal condition 4hrs				
		1. Sine wave 10~55HZ frequency (1 min)				
6	Vibration Test (Packaged)	2. The amplitude of vibration :1.5 mm				
		3. Each direction (XYZ) duration for 2 Hrs				
		Packing Weight (Kg) Drop Height (cm)				
		$0 \sim 45.4$ 122				
		45.4 ~ 90.8				
7	Dron Test (Paglagad)					
'	Drop Test (Packaged)	90.8 ~ 454				
		Over 454 46				
		Drop direction: **3 comer /1 edges /6 sides etch 1times				



5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So, please handle it very carefully, do not touch, push or rub the exposed polarizing with anything harder than an HB pencil lead (glass, tweezers, etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is 320±10°C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM.

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is $25^{\circ}\text{C} \pm 5^{\circ}\text{C}$ and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush, shake, or jolt the module.

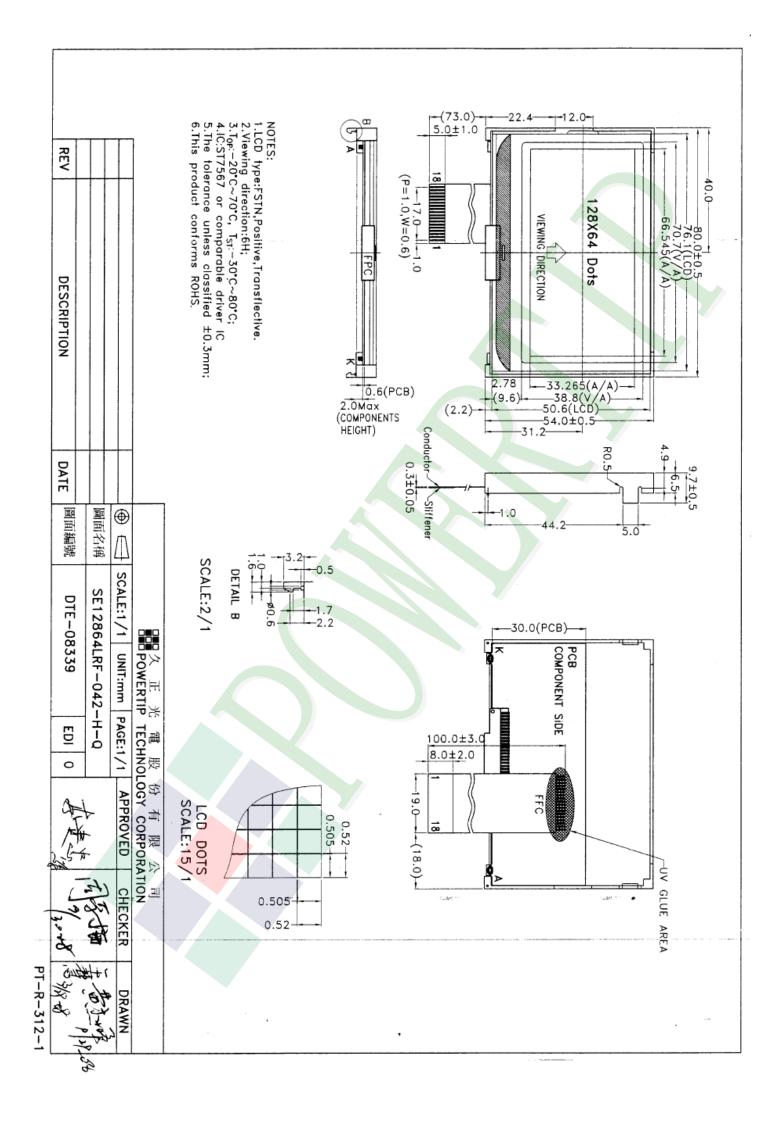
5.4 TERMS OF WARRANTY

5.4.1 Applicable warrant period

The period is within thirteen months since the date of shipping out under normal using and storage conditions.

5.4.2 Unaccepted responsibility

This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.



LCM Model	SE12864LRF-042-H-Q
Drawing NO.	DPK-08582

LCM包裝規格書 LCM Packaging Specifications

Approve	Check	Contact	
周愛梅	黄傑峰	王慧	
DATE	初版	版次Ver	
08'09'29	08'09'29	0	

1.包裝材料規格表 (Packaging Material): (per carton)

No.	Item	Model	Dimensions (mm)	Quantity
1	成品 LCM	SE12864LRF-042-H-Q	80.0*54.0*9.7	192
2	靜電袋 (1)BAG	BAG150100ARABA	150*120*0.05	192
3	氣泡墊(2)BAG	BAG240100AWBBA	240*100*5	192
4	氣泡墊(3)BAG	BAG290240BRBBA	240*290*5	16
5	刀卡A2(4)BX	BX29500072BZBA	295*72*3	104
6	刀卡B2(5)BX	BX24500072BZBA	245*72*3	24
7	C2內盒(6)Product Box	BX31025580AABA	310*255*86	8
8	外紙箱(7)Carton	BX52532536CCBA	525*325*360	1
9				

2. 單箱數量規格表 (Packaging Specifications and Quantity):

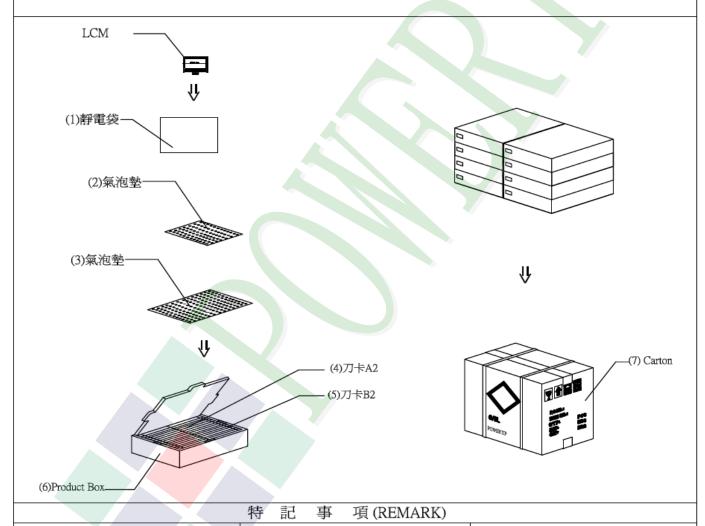
(1)LCM quantity per box : no. per box

x no. of box 12

2 24

(2)Total LCM quantity in carton: quantity per box

24 x no of boxes 192



1. Label Specifications: 前后空一格

MODEL: LOT NO: QUANTITY: CHECK: