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Analog Electronics ENEE2360

Project Report

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Abstract

The purpose of this project is to design a circuit (room thermostat) that is used to maintain the room temperature within predetermined temperatures T (max) and T (min). This circuit contains one sensor, 10 resistors with different values, one BJT pnp transistor, green LED, non-inverting amplifier, Schmitt trigger comparator and a potentiometer with a value 47k ohm. We used these components to build the circuit practically and show it to teaching assistant Eng. Shadi. Then we will use PSPICE software to do this project.

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Theory

Components

1- LM35DZ sensor

The LM35DZ is an integrated circuit analog sensor that can be used to measure temperature with an electrical output proportional to the temperature (this is why it is an analog sensor). It can measure temperature more accurately than a thermistor. The sensor circuitry is sealed and not subject to oxidation. The LM35DZ generates a higher output voltage than thermocouples and may not require that the output voltage be amplified. The LM35 has an output voltage that is proportional to the temperature. The scale factor is $10\text{mV}/^{\circ}\text{C}$ (Celcius). [1]

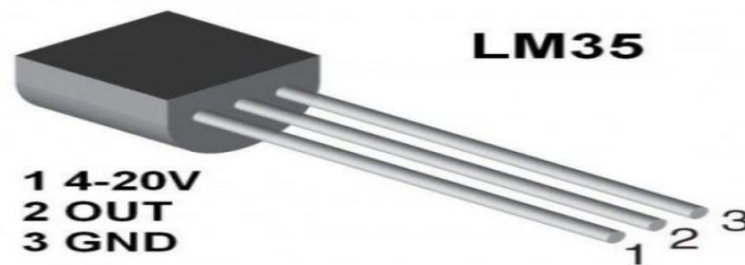


Figure 1: LM35DZ

2- LM324 op-amp

This IC is a non-inverting amplifier and it comprises four independent op-amps internally. This IC has designed with low-power, bandwidth and high stability for operating with single power supply over extensive voltage ranges. The range of operating voltages of this IC includes 3.0 V for low and 32 V for high. It consists of 14-pins. [2]

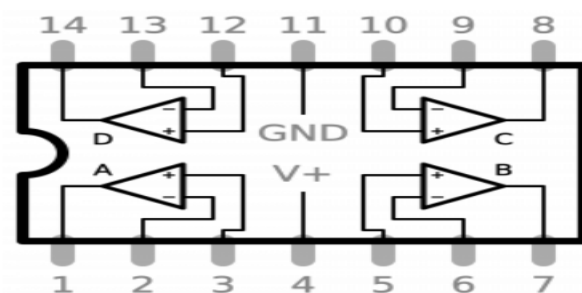


Figure 2: LM324

3- uA741 op-amp

uA741 is a monolithic op-amp with high-performance, and it is designed on an only Si (Silicon) chip. This IC is used in a broad variety of analog applications. The operating voltage of broad range as well as high-gain provides better performances within the applications like integrators, general feedback, and summing amplifier applications. The inner recompense network ensures constancy within closed-loop circuits. [3]

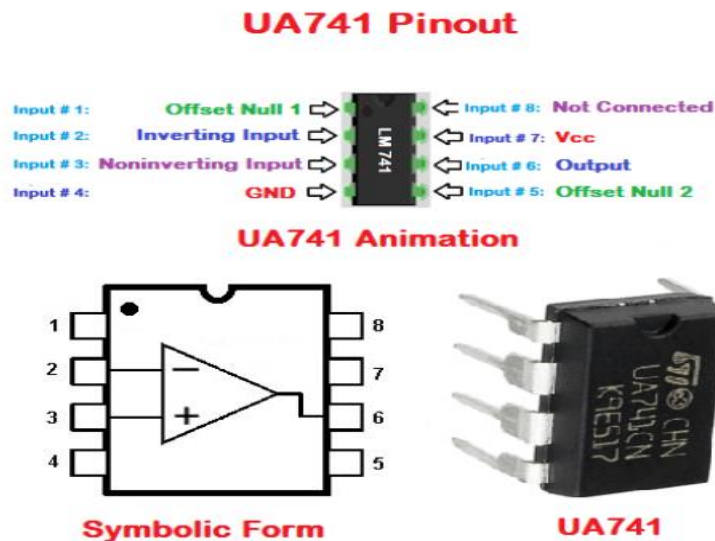


Figure 3: uA741

4- 2N3703 Transistor

The 2N3702 is a PNP Transistor with VCE of -25V and a collector current of 500mA. It can be used as a small signal switching transistor and any other small-signal application. It also has a low base voltage of -5V. [4]

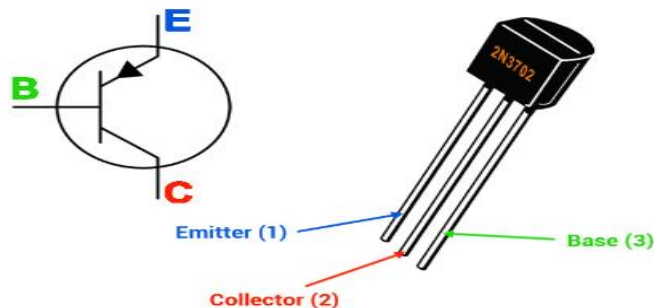


Figure 4: 2N3703 Transistor

5- Green LED

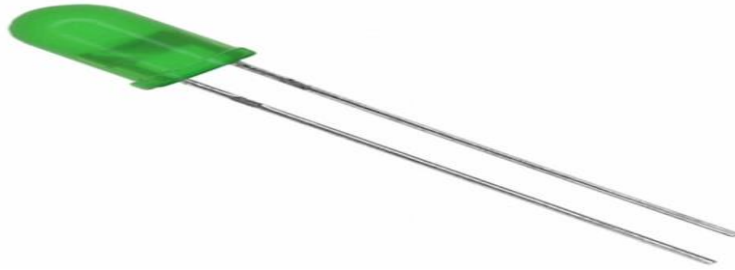


Figure 5: Green LED

In the simulation, we replace it with D1N4002.

6- D1N4002

It is a general-purpose diode from the 1N400x family. It is widely used in electronic appliances for rectification purposes and also for other purposes such as voltage blocking, voltage boosting, etc.

The 1N4002 can handle up to 100V reverse voltage and up to 1A of continuous current with a surge peak of 30A. The reverse current is 5uA which is negligible. And it can withstand reverse voltage peak up to 100V. [5]

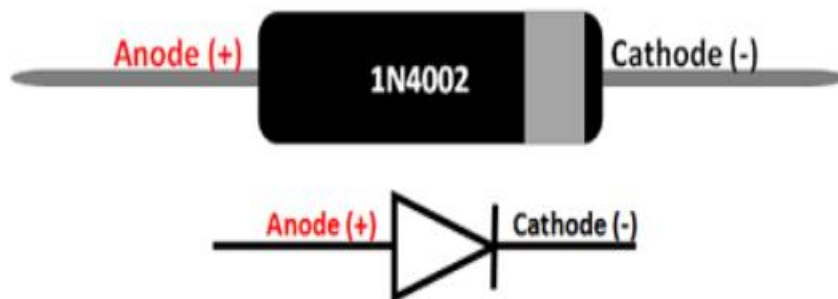


Figure 6: D1N4002

7- Resistors

The resistor is a passive electrical component that creates resistance in the flow of electric current. In almost all electrical networks and electronic circuits they can be found. The resistance is measured in ohms (Ω). [6]



Figure 7: a resistor

8- Potentiometer

A potentiometer is a manually adjustable variable resistor with 3 terminals. Two of the terminals are connected to the opposite ends of a resistive element, and the third terminal connects to a sliding contact, called a wiper, moving over the resistive element. The potentiometer essentially functions as a variable resistance divider. The resistive element can be seen as two resistors in series (the total potentiometer resistance), where the wiper position determines the resistance ratio of the first resistor to the second resistor. If a reference voltage is applied across the end terminals, the position of the wiper determines the output voltage of the potentiometer. [7]



Figure 8: Potentiometer

Procedure

Part1 (Practical)

We built the circuit correctly. We had a discussion with the TA. Shadi about it. We had a problem in operating the circuit and most of our colleagues had it, because of the source of the components is the same, there was a defect in the work of the sensor.

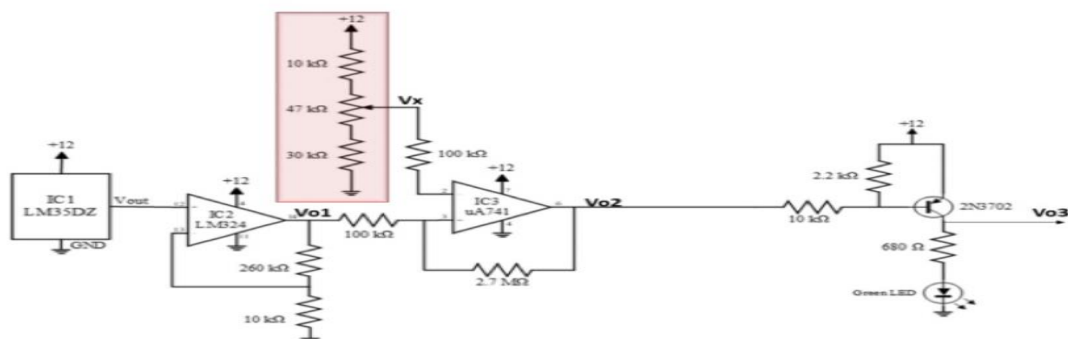
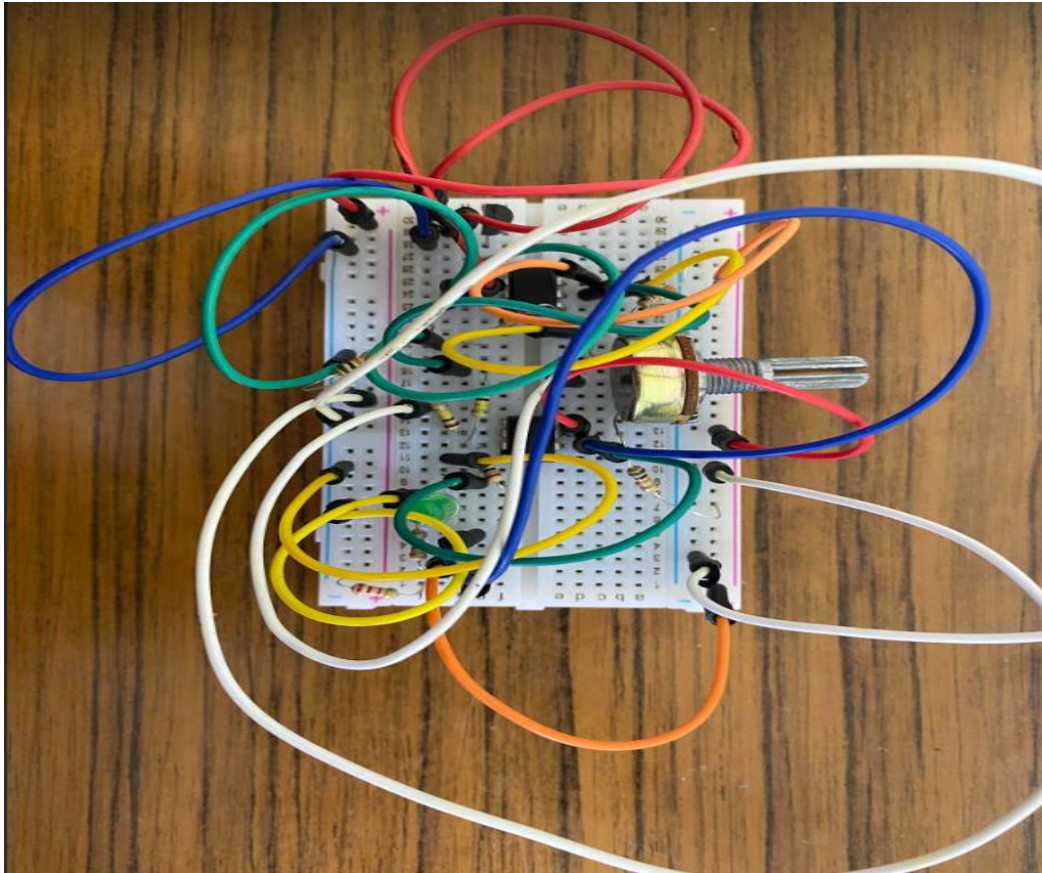


Figure 9: The circuit

Explanation of the function of the circuit of Fig. (9)

Firstly, the LM35DZ sensor gives us 10mv for every 1°C , it has three pins, one of them connected to the ground, and the second pin connected to the +12volt, while the third pin connected to the positive input of the non-inverting amplifier.

Secondly, the LM324 is a non-inverting amplifier, the positive input connected to the output of the sensor, also it has a negative feedback resistor, and it has the gain of 27. The output connected to the positive input of the Schmitt trigger comparator.

Thirdly, the uA741 is a Schmitt trigger comparator, the positive input connected to the output of the non-inverting amplifier, also it has a positive feedback resistor. **If $V (+) > V (-)$, then the output is $+V_{\text{sat}}$, otherwise, if $V (+) < V (-)$, then the output is $-V_{\text{sat}}$.**

Finally, we have a pnp transistor, it has a collector, base and emitter. Also we have a green LED. If the output of the Schmitt trigger comparator is $-V_{\text{sat}}$ then the transistor will be in saturation mode, and the green LED is on. While If the output of the Schmitt trigger comparator is $+V_{\text{sat}}$ then the transistor will be in cutoff mode, and the green LED is off.

Part2 (Simulation circuits and results)

- a- We replace the circuit to the left of V_x by a 6V battery.
- b- We replace the green LED with D1N4002 which I mentioned above.
- c- We replace the temperature sensor by a VPWL Voltage source as shown in Figure 10.

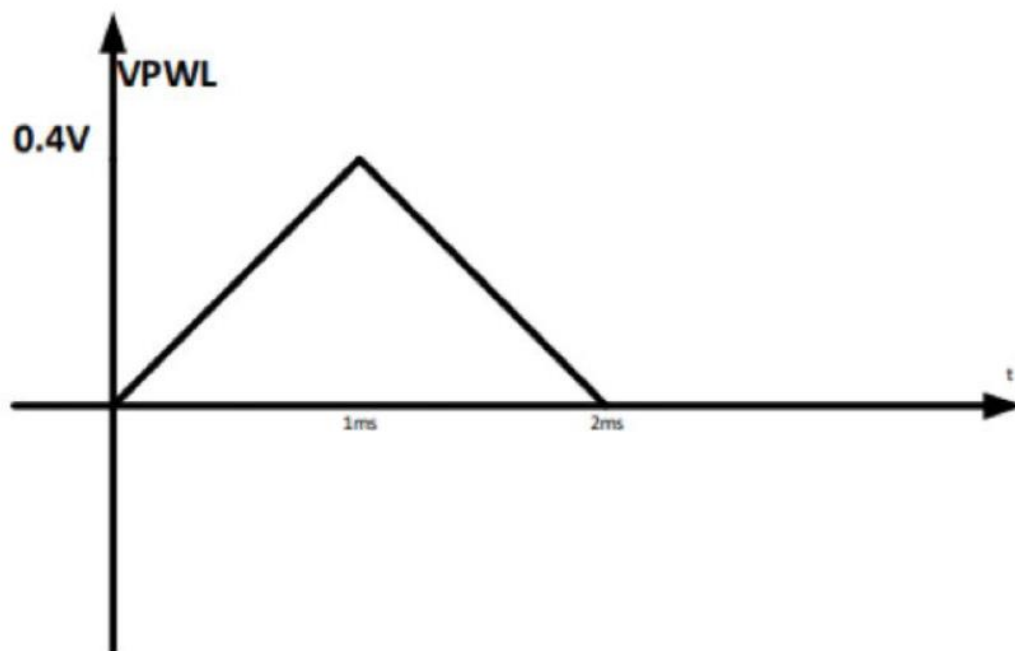


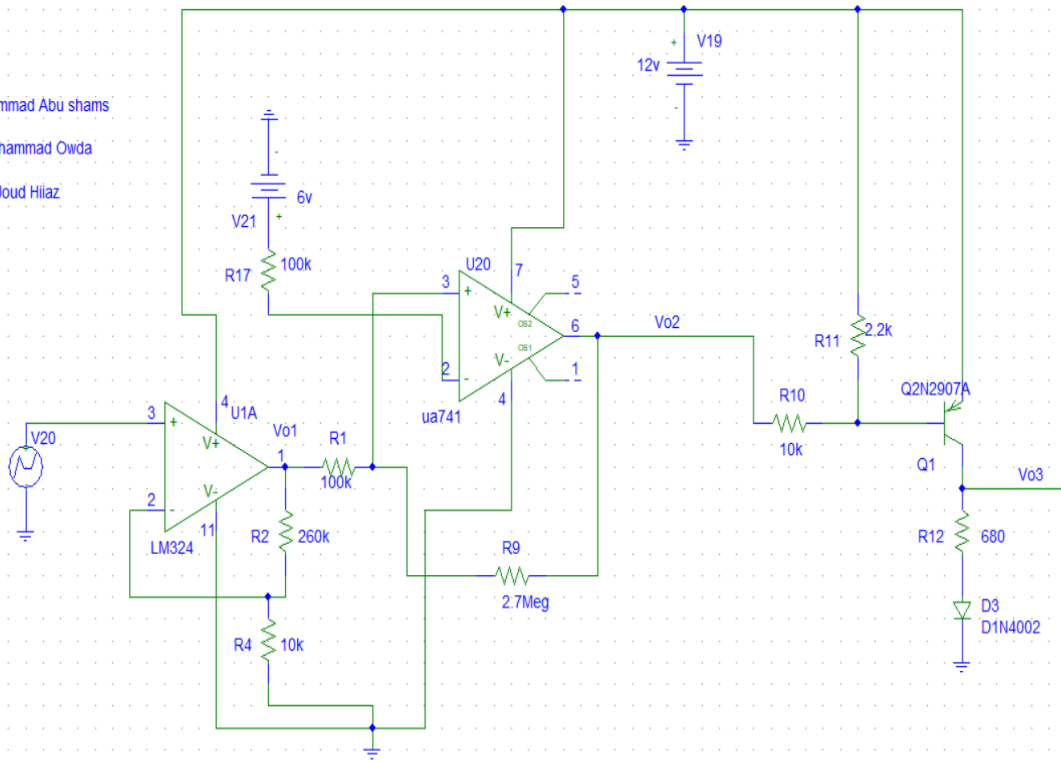
Figure 10: VPWL Voltage source

Then we built the circuit in the PSPICE:

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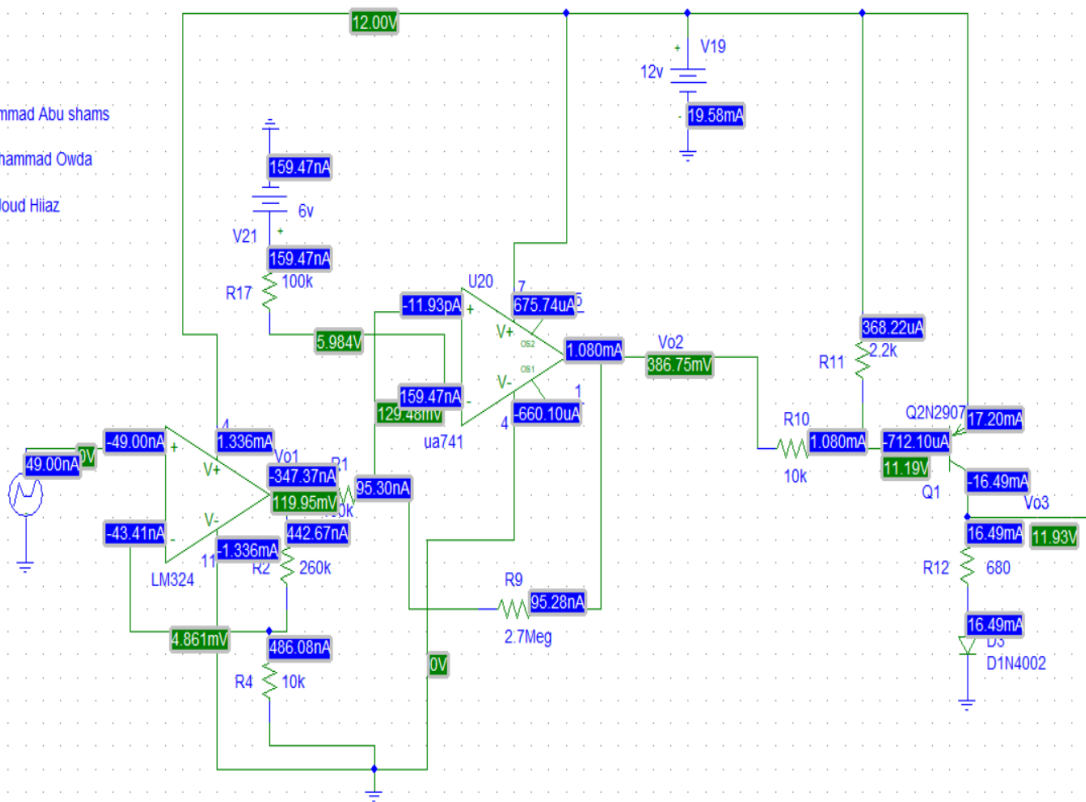


Figure 11: Simulation

We put the voltage marker on the output of the non-inverting amplifier and we plot Vo1:

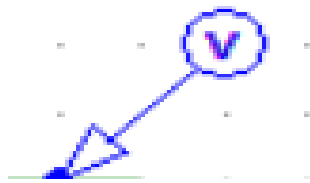


Figure 12: Voltage marker

Vo1:

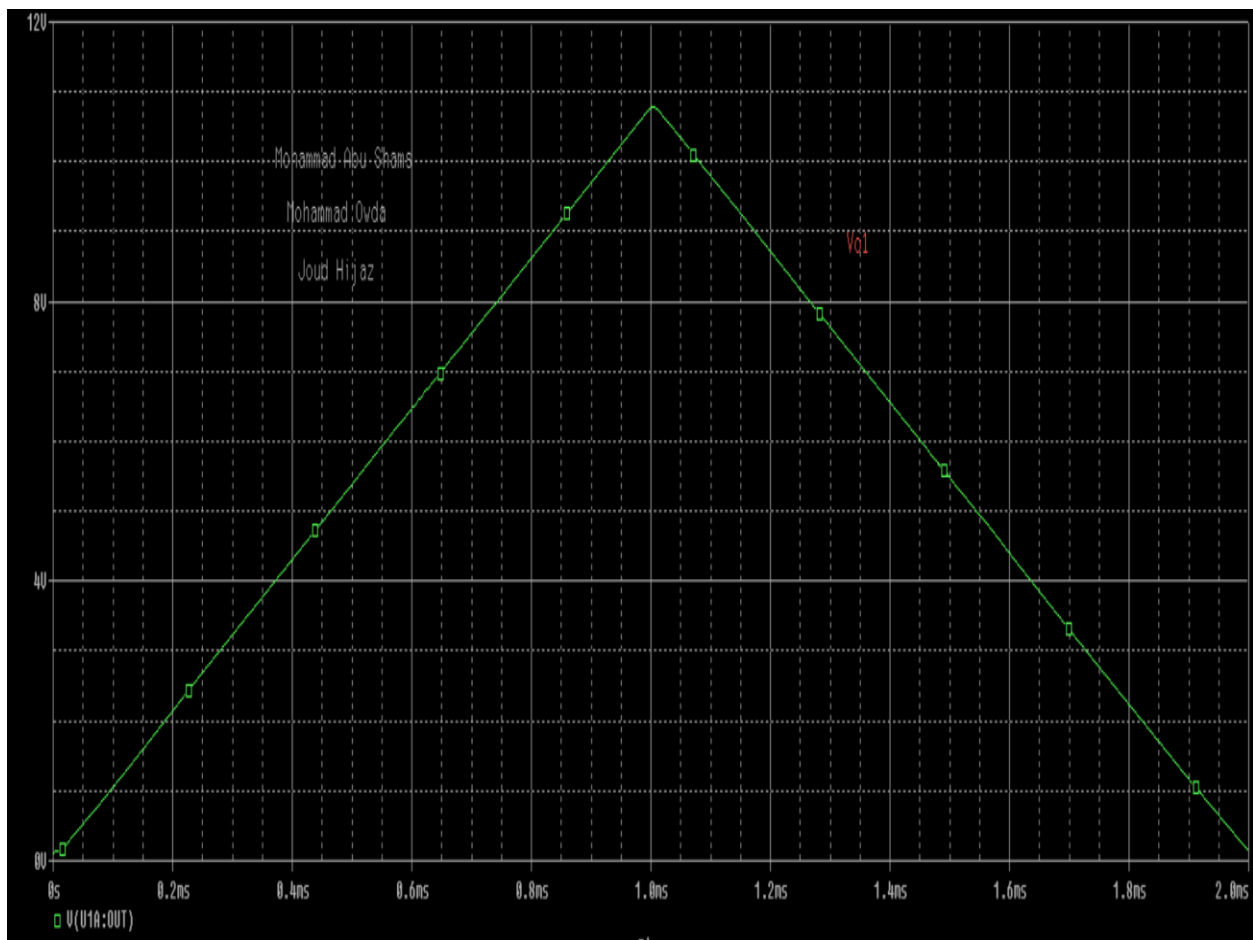


Figure 13: Plotting Vo1

Then we put the voltage marker on the output of the Schmitt trigger comparator and we plot Vo2:

Vo2:

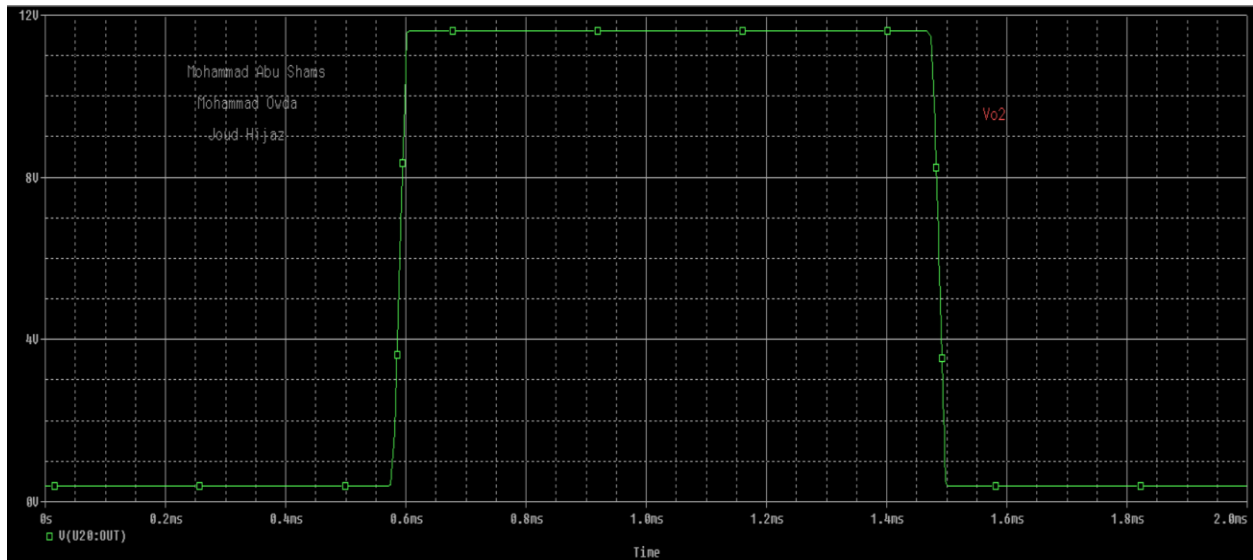


Figure 14: Plotting Vo2

Then we put the voltage marker on the collector of pnp transistor and we plot Vo3:

Vo3:

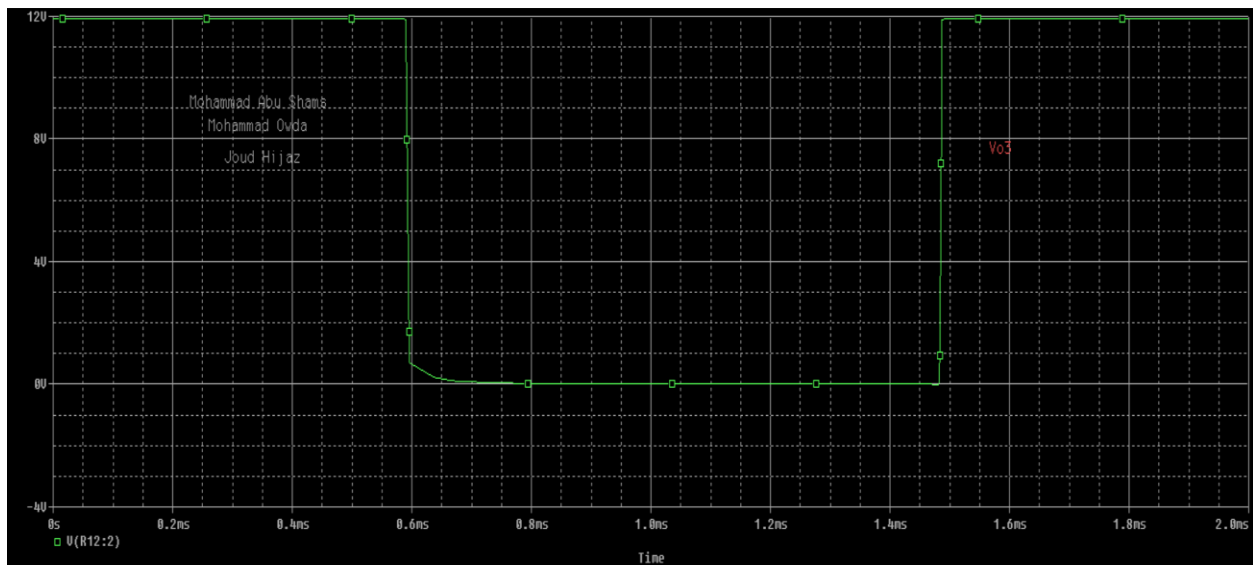
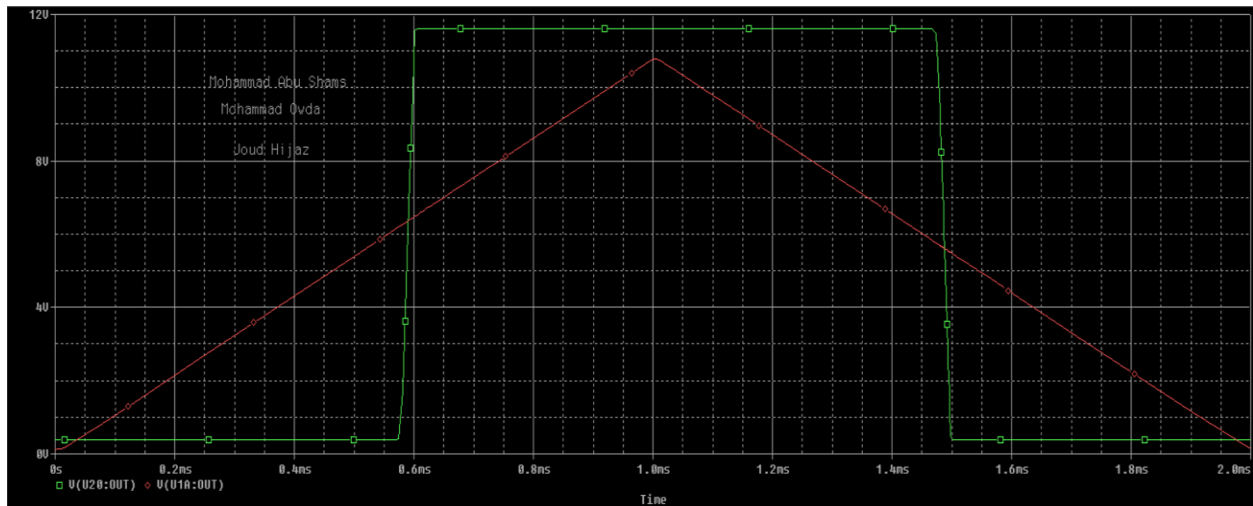


Figure 15: Plotting Vo3

d- Estimate the upper threshold and the lower threshold temperatures from Vo1 and Vo2(t) plots.



In PSpice:

① upper threshold temperature:

From PSpice: V_{o1} cuts V_{o2} approximately at $\frac{6.4 \text{ Volt}}{V_{UT}}$
and the gain is $(1 + \frac{R_F}{R_i}) = (1 + \frac{260k}{10k}) = 27$

So $\frac{6.4}{27} = 0.237037 \text{ Volt}$

without sensor

1°C	→	10mV
x	→	0.237037

$x = \frac{0.237037}{10 \times 10^{-3}} = 23.7037^\circ\text{C}$

② lower threshold temperature:

From PSpice V_{o1} cuts V_{o2} approximately at $\frac{5.7 \text{ Volt}}{V_{LT}}$
gain = 27

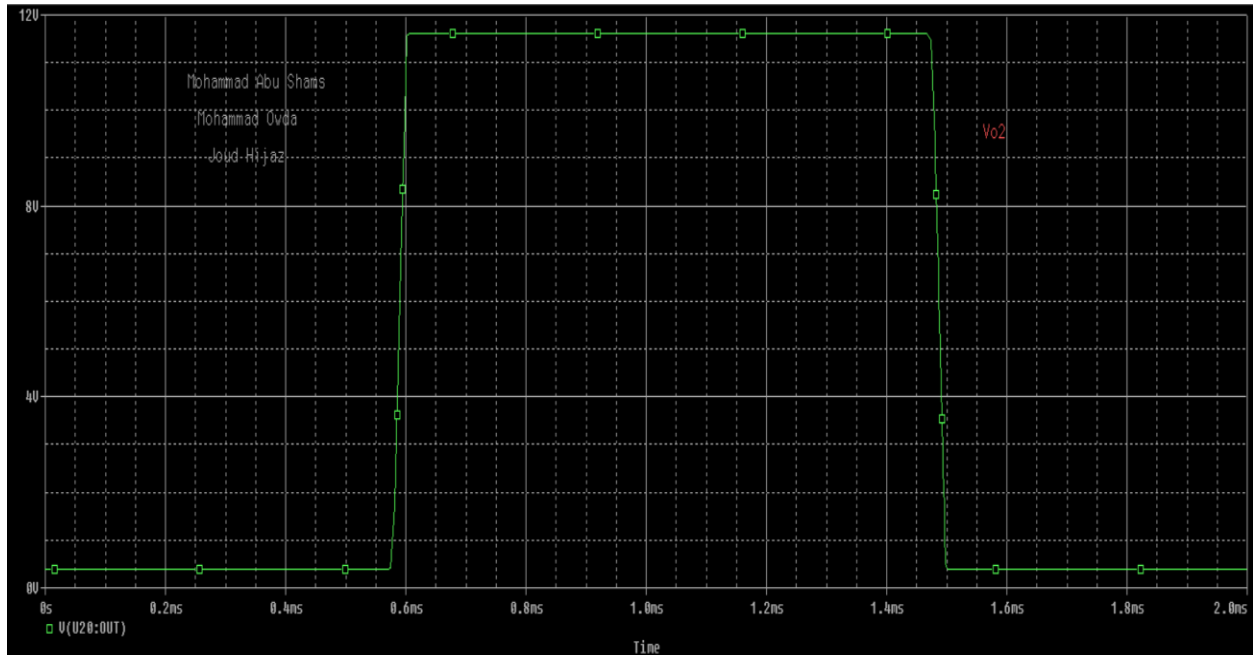
So $\frac{5.7}{27} = 0.21111 \text{ Volt}$

1°C	→	10mV
x	→	0.21111

$x = \frac{0.21111}{10 \times 10^{-3}} = 21.1111^\circ\text{C}$

e- Determine $+V_{sat}$ and $-V_{sat}$.

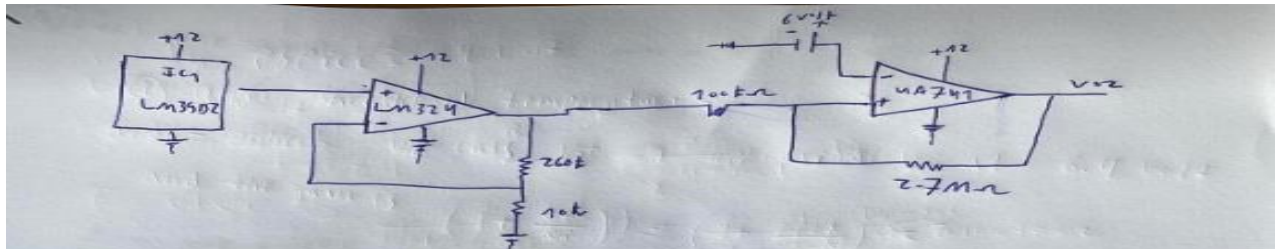
From the plotting of v_{o2} :



$+V_{sat}$ approximately equal 11.6 Volt

$-V_{sat}$ approximately equal 0.4 Volt

f- Using results of part e, calculate by hand the upper threshold and the lower threshold temperature.



From part e: we found that $+V_{sat} \approx 11.6$ volt and $-V_{sat} \approx 0.4$ volt

Now: ① let $V_{out} = +V_{sat} = +11.6$ volt

$V_d > 0$

$V(-) = 6$ volt

$V(+) = \frac{100k}{100k+2.7M} (+V_{sat}) + \frac{2.7M}{2.7M+100k} V_+$

For $V_d > 0$, $\Rightarrow \frac{100k}{100k+2.7M} (11.6) + \frac{2.7M}{2.7M+100k} V_+ > 6$

$V_e > V_c$

$0.414286 + 0.964286 V_+ > 6$

$V_+ > \frac{(6 - 0.414286)}{0.964286}$

$V_+ > 5.79259$ V

As long as $V_+ > 5.79259$, $V_o = +V_{sat}$

But when $V_+ < 5.79259$, V_o switch to $(-V_{sat})$

VLT

Now we find the gain of Non inverting Amplifier

Gain = $(1 + \frac{R_F}{R_i})$

$= 1 + \frac{260k}{10k}$

$= 27$

Now we divide $\frac{5.79259}{27} = 0.21454$ volt

Since in the sensor $= 1^\circ$ leads to $10mV$

$1^\circ \longrightarrow 10mV$

$x8 \longrightarrow 0.21454$

$X = \frac{0.21454 \times 1}{10 \times 10^{-3}} = 21.45^\circ C$

this is the lower threshold temperature.

② let $v_{o2} = -v_{sat} = 0.4 \text{ volt}$

$$v_d < 0$$

$$v(-) = 6 \text{ volt}$$

$$v(+) = \frac{100k}{100k + 2.7M} (-v_{sat}) + \frac{2.7M}{2.7M + 100k} v_t$$

$$\text{For } v_d < 0 \Rightarrow \frac{100k}{100k + 2.7M} (0.4) + \frac{2.7M}{2.7M + 100k} v_t < 6$$

$$0.014286 + 0.964286 v_t < 6$$

$$v_t < \frac{(6 - 0.04286)}{0.964286}$$

$$v_t < 6.2074 \text{ V}$$

As long as $v_t < 6.2074 \text{ V}$, $v_o = -v_{sat}$

But when $v_i > \boxed{6.2074}$, v_o switch to $(+v_{sat})$

the gain = 27

$$\text{So } \frac{6.2074}{27} = 0.2299 \text{ volt}$$

Since $1^\circ \text{C} \rightarrow 10 \text{ mV}$

$9^\circ \text{C} \rightarrow 0.2299 \text{ volt}$

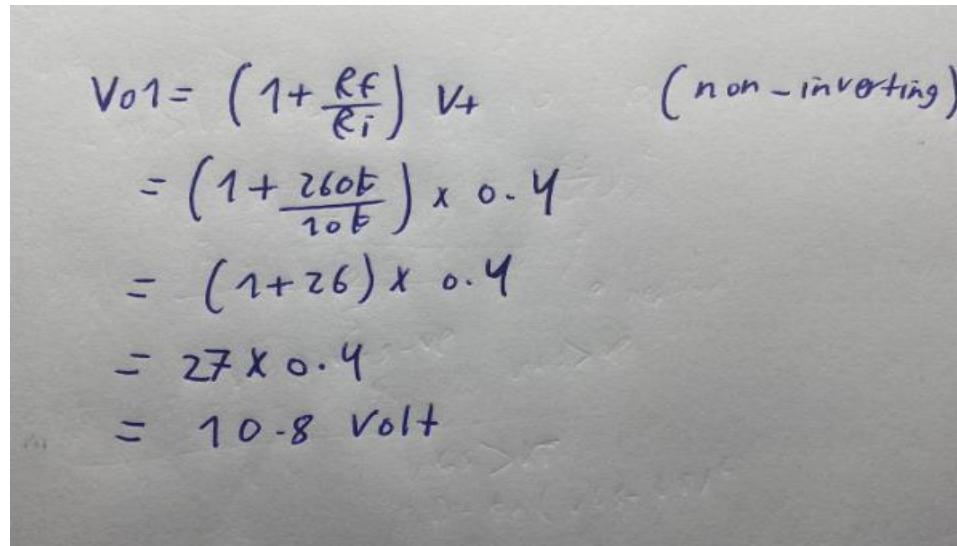
$$\boxed{y = \frac{0.2299}{20 \times 10^{-3}} = 22.99^\circ \text{C}}$$

this is upper threshold temperature.

Comparison of simulation results to hand calculation

1- Vo1 from PSPICE approximately equal 10.8 volt.

And from hand calculations it is the same:



Handwritten calculation for V_{o1} (non-inverting):

$$\begin{aligned} V_{o1} &= \left(1 + \frac{R_f}{R_i}\right) V_+ \quad (\text{non-inverting}) \\ &= \left(1 + \frac{260k}{20k}\right) \times 0.4 \\ &= (1 + 26) \times 0.4 \\ &= 27 \times 0.4 \\ &= 10.8 \text{ Volt} \end{aligned}$$

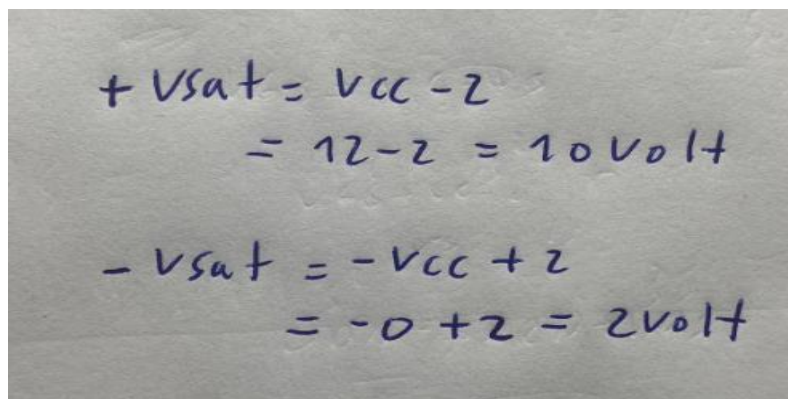
2- Vo2 from PSPICE:

+Vsat approximately equal 11.6 Volt.

−Vsat approximately equal 0.4 Volt.

When $V_d > 0$ ($V(+) > 6$) Volt the output is +Vsat ... and When $V_d < 0$ ($v(+) < 6$) Volt

The output is −Vsat.



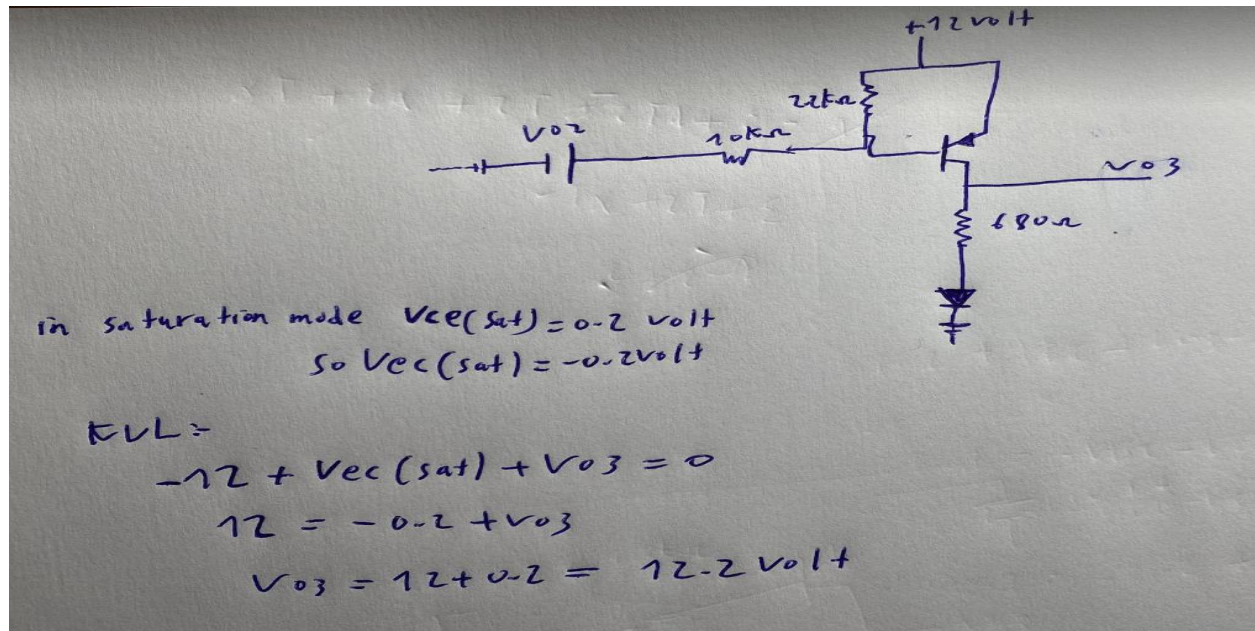
Handwritten calculation for V_{sat} :

$$\begin{aligned} +V_{sat} &= V_{CC} - 2 \\ &= 12 - 2 = 10 \text{ Volt} \\ -V_{sat} &= -V_{CC} + 2 \\ &= -0 + 2 = 2 \text{ Volt} \end{aligned}$$

And from hand calculations the different between them is 1.6 Volt.

3- V_{o3} from PSPICE approximately equal 11.9 when the V_{o2} is $-V_{sat}$, and approximately equal 0 when V_{o2} is $+V_{sat}$.

When the V_{o2} is $-V_{sat}$, then the transistor is in saturation mode and the V_{o3} equal 12.2 volt from hand calculations:



Also When the V_{o2} is $+V_{sat}$, then the transistor is in cutoff mode and the V_{o3} equal 0 volt because $I_c = 0$ in cutoff mode

$V_{o3} = V_c = I_c R_c = 0 \text{ Volt}$.

Conclusion

In conclusion, we learnt how to build a room thermostat circuit, and we know about circuit's components, and how each components work, and we know the datasheet of them. We learnt how to simulate the circuit in PSPICE. We became able to find V_{o1} and V_{o2} and V_{o3} , and find the upper and lower threshold temperature. The result in PSPICE is closer to the result by hand calculations, that's mean our results is true.

Feedback

This project is a nice one, we learnt some new ideas in Analog electronics and we became more familiar with PSPICE.

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