

Automata and Logic Engineering 1 (ALE1) Feb 2019

Assignments

1: parse + tree

Due Feb 21 at 12:45pm

2: truth table + hash code

Due Feb 28 at 12:45pm

3: simplify

Due Mar 14 at 12:45pm

4: normalize

Due Mar 21 at 12:45pm

5: nandify

Due Mar 31 at 11:59pm

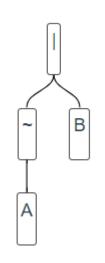
Final deadline



Last time

Normalization

Input: $|(\sim(A),B)|$



Α	В	Result
0	0	1
0	1	1
1	0	0
1	1	1

DNF: $|(|(&(\sim(A), \sim(B)), &(\sim(A), B)), &(A, B))|$



NAND

- NAND (negative-AND)
- Operator (logic gate) which produces an output which is false only if all its inputs are true

Α	В	A NAND B
0	0	1
0	1	1
1	0	1
1	1	0



NAND

- Created only with either the connectives
 - ~ NOT & AND

formula	becomes
A ⇒ B	¬A V B
A ⇔ B	(¬A ∧ ¬B) ∨(A ∧ B)
AVB	¬(¬A ∧ ¬B)



Nandify

formula	becomes
$A \Rightarrow B$	¬A V B
A ⇔ B	(¬A ∧ ¬B) ∨(A ∧ B)
AVB	¬(¬A ∧ ¬B)
~A	?
A & B	?



Nandify

formula	becomes	NAND
$A \Rightarrow B$	¬A V B	%(A,%(B,B))
A ⇔ B	(¬A ∧ ¬B) V(A ∧ B)	%(%(%(A,A),%(B,B)),%(A,B))
AVB	¬(¬A ∧ ¬B)	%(%(A,A),%(B,B))
~A	¬(A/\A)	%(A,A)
A & B	¬(¬(A∧B))	%(%(A,B),%(A,B))



ALE1 Deadline

- 31st March (Sunday) 23:59
 - Source code
 - Executable
 - Report
 - Don't forget to brag about your code! (all extra work you did in your project and advanced implementation that I might oversee)
- Ask for extension at least 1 day before



