In the name of God

Core-Based Embedded System Design

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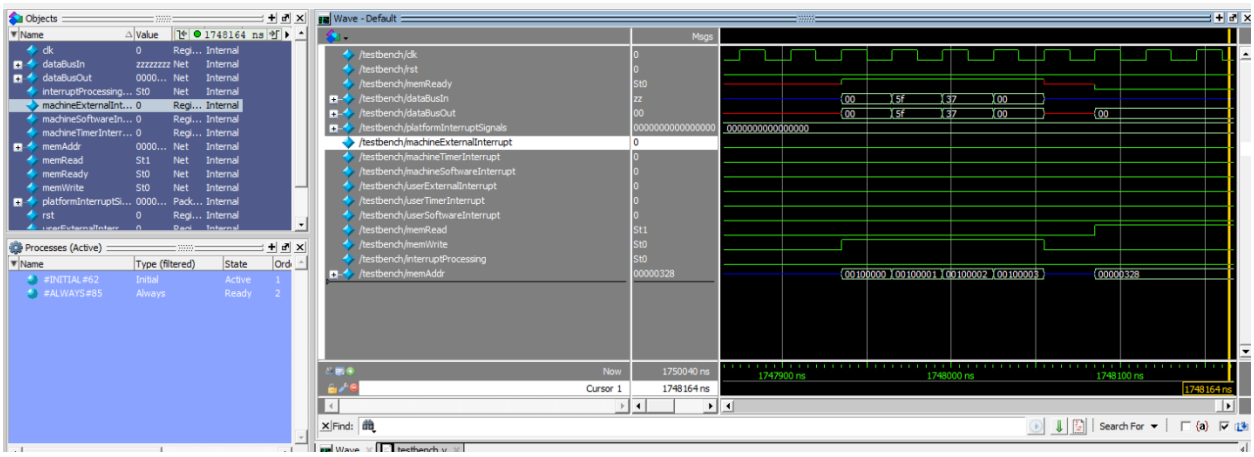
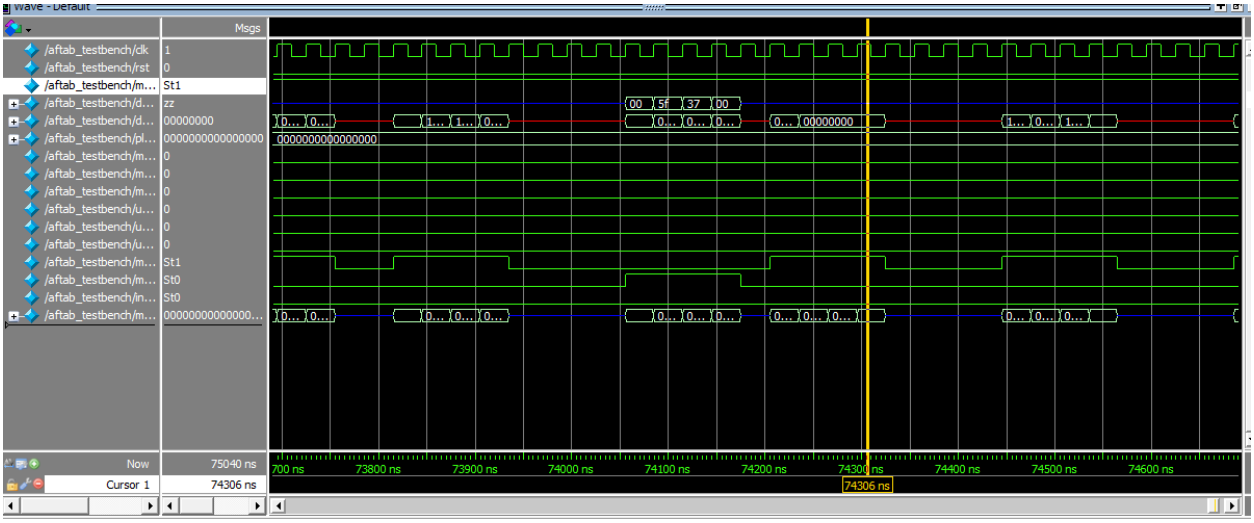
CA3

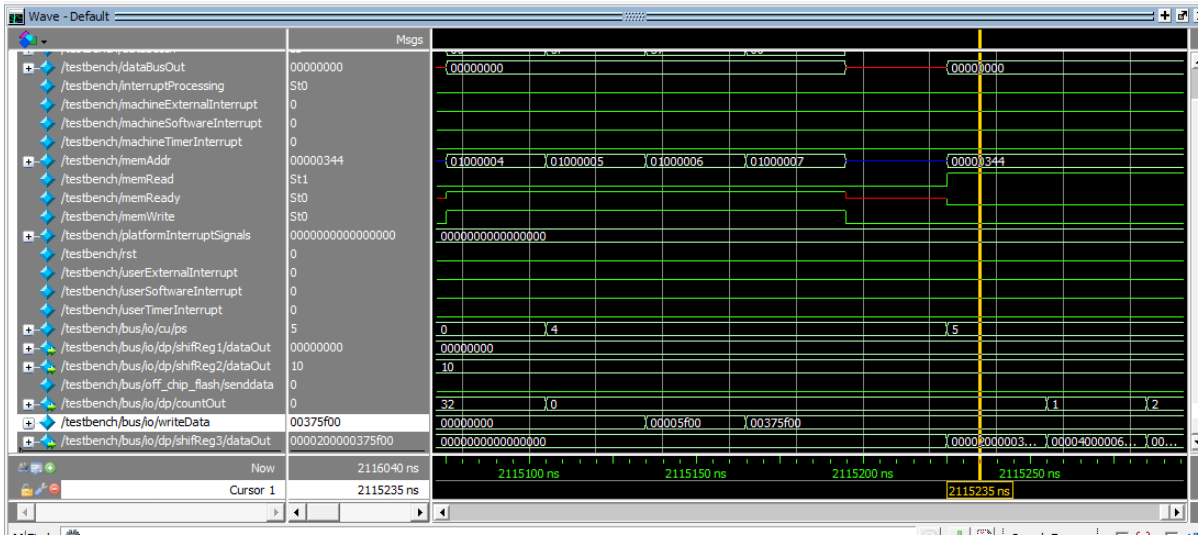
Part A

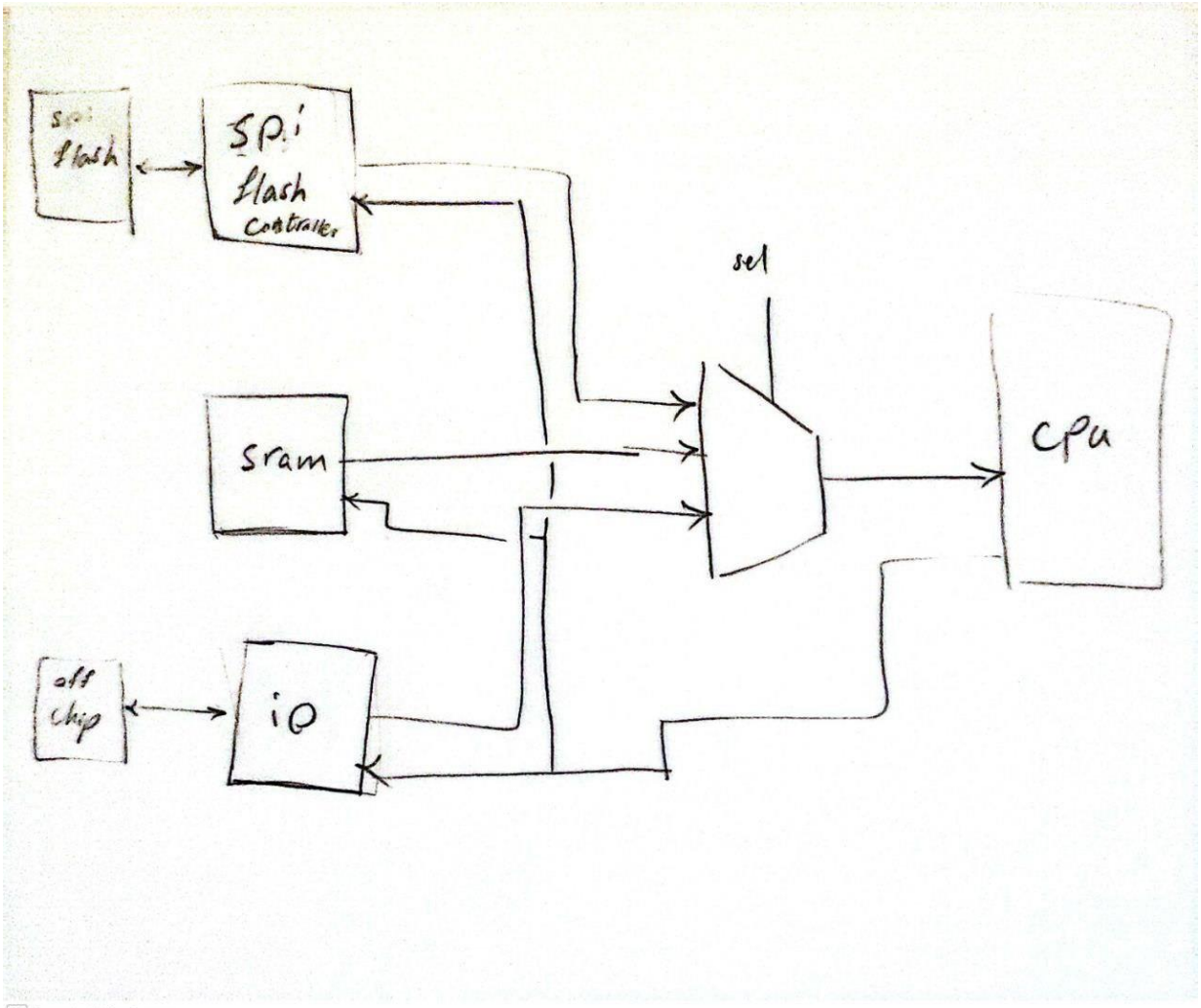

Objects
Name
Value
clk
dataBusIn
dataBusOut
interruptProcessing...
machineExternalInt...
machineSoftwareIn...
machineTimerInterr...
memAddr
memRead
memReady
memWrite
platformInterruptSi...
rst
Processes (Active)
Name
Type (filtered)
State
#INITIAL#62
Initial
Active
#ALWAYS#85
Always
Ready
Wave - Default
Msgs
/testbench/clk
/testbench/rst
/testbench/memReady
/testbench/dataBusIn
/testbench/dataBusOut
/testbench/platformInterruptSignals
/testbench/machineExternalInterrupt
/testbench/machineTimerInterrupt
/testbench/machineSoftwareInterrupt
/testbench/userExternalInterrupt
/testbench/userTimerInterrupt
/testbench/userSoftwareInterrupt
/testbench/memRead
/testbench/memWrite
/testbench/interruptProcessing
/testbench/memAddr
00000328
Now
1750040 ns
Cursor 1
1748164 ns
1747900 ns
1748000 ns
1748100 ns
1748164 ns
Find:
Search For

Part B


Wave - Default
Msgs
/aftab_testbench/clk
/aftab_testbench/rst
/aftab_testbench/m...
/aftab_testbench/d...
/aftab_testbench/d...
/aftab_testbench/pl...
/aftab_testbench/m...
/aftab_testbench/m...
/aftab_testbench/m...
/aftab_testbench/u...
/aftab_testbench/u...
/aftab_testbench/u...
/aftab_testbench/m...
/aftab_testbench/m...
/aftab_testbench/in...
/aftab_testbench/m...
St1
zz
00000000
0000000000000000
St0
St0
00000000000000...
00
5f
37
00
00000000
Now
75040 ns
Cursor 1
74306 ns
700 ns
73800 ns
73900 ns
74000 ns
74100 ns
74200 ns
74300 ns
74400 ns
74500 ns
74600 ns
74306 ns

Part C


Wave - Default
Msgs
/testbench/dataBusOut
/testbench/interruptProcessing
/testbench/machineExternalInterrupt
/testbench/machineSoftwareInterrupt
/testbench/machineTimerInterrupt
/testbench/memAddr
/testbench/memRead
/testbench/memReady
/testbench/memWrite
/testbench/platformInterruptSignals
/testbench/rst
/testbench/userExternalInterrupt
/testbench/userSoftwareInterrupt
/testbench/userTimerInterrupt
/testbench/bus/io/cu/ps
/testbench/bus/io/dp/shifReg1/dataOut
/testbench/bus/io/dp/shifReg2/dataOut
/testbench/bus/off_chip_flash/senddata
/testbench/bus/io/dp/countOut
/testbench/bus/io/writeData
/testbench/bus/io/dp/shifReg3/dataOut
00000000
St0
0
0
0
00000344
St1
St0
St0
0000000000000000
0
0
0
0
5
00000000
10
0
32
00375f00
0000020000375f00
01000004
01000005
01000006
01000007
00000344
0
4
5
10
1
2
00005f00
00375f00
Now
2116040 ns
Cursor 1
2115235 ns
2115100 ns
2115150 ns
2115200 ns
2115250 ns
2115235 ns



CS Scanned with CamScanner