* **Mini-Cordic Ip Core User Manual**

Developed By

Mitu Raj  
Roshan Raju

Mini-cordic   
IP core

* *A 16 Bit IP core for sine and cosine generation*

Mini

Cordic IP Core

* *Documentation*

***Revised on Nov 1 2016***

**Contents**

1. Introduction
2. Specifications
3. Description
4. Examples

**1.Introduction**

Most of the low cost microcontrollers and processors in the market lack the computability of DSP processors have. They don’t have dedicated multipliers and floating point units to compute trigonometric functions like sine , cosine etc. CORDIC ALGORITHM is a popular approach to achieve such computability in low end processors using simple shifters and adders. **MINI CORDIC IP CORE** is an IP core based on **CORDIC ALGORITHM** to find sine and cosine of an angle . It may be easily integrated with processor cores to build simple and low cost dedicated processors for trigonometric applications and DSP computations.

**2.Specifications**

* **Inputs**

1. **Angle** in degrees, ɸ - 16 bit signed vector.
2. **Clock**.
3. **Load** - single bit.
4. **Reset** – single bit.

* **Outputs**

1. **Sin ɸ** - 16 bit signed vector.
2. **Cos ɸ** - 16 bit signed vector.
3. **Done** - single bit.

* **Resolution**

1. **ɸ** - 0.01 degree.
2. **Sin ɸ , Cos ɸ** : 0.0003 with maximum error +/- 0.005.

* **Speed**

Maximum of 16 clock cycles to obtain the output.

Tested for clock frequency upto 100 Mhz on Xilinx Artix-7 FPGA.

**3.Description**

The angle **ɸ** is input as 16-bit signed vector and the Load signal is asserted high to start processing the input. Depending upon the input angle, Sin **ɸ** and Cos **ɸ** are calculated and the respective outputs are obtained as 16 bit signed vectors within 16 clock cycles. Done signal is set to indicate that the output is available. Load signal is initially pulled low, before inputting the angle and it is then asserted high after inputting angle. It has to be kept high till the output is available ; for the proper functioning of the core. All inputs and outputs are synchronous with Clock signal at the input.

**4.Examples**

Angles are fed as 16-bit signed vector. For positive angle values , MSB is ‘0’, and for negative angle values , MSB is ‘1’ and the magnitude is represented in 2’s compliment form is employed. Maximum angle which can be input is 360 degrees. It is represented as 215 in signed vector format. Then **ɸ** may be represented as the binary of (**ɸ** \* 215/360). If **ɸ** is negative , then 2’s compliment has to be taken in addition to the previous step. Some examples are given below for reference

|  |  |
| --- | --- |
| **Angle** **ɸ in degrees** | **16 bit signed representation** |
| 45 | 1000H |
| -45 | F000H |
| 60 | 1555H |
| 90 | 2000H |
| 180 | 4000H |
| 270 | 6000H |
| 360 | 7FFFH |

The outputs are also obtained as 16 bit signed vector. Positive values in the usual notation and the negative values in 2’s compliment form.

**Copyright Notice**

**Mini-Cordic IP Core © 2016**

All Rights Reserved to Mitu Raj and Roshan Raju.

**Contact :** iammituraj@gmail.com