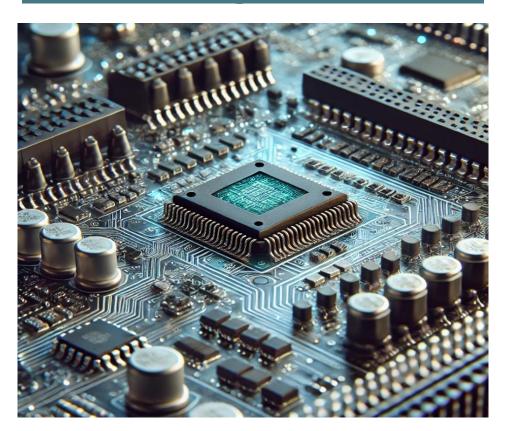


## **Digital System Design**

# **FPGA-Edge-Detection**



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