

DYNAMIC DESIGN

EGFWD ADVANCED EMBEDDED SYSTEMS
SCHOLARSHIP

EMBEDDED SW DESIGN PROJECT

Prepared by

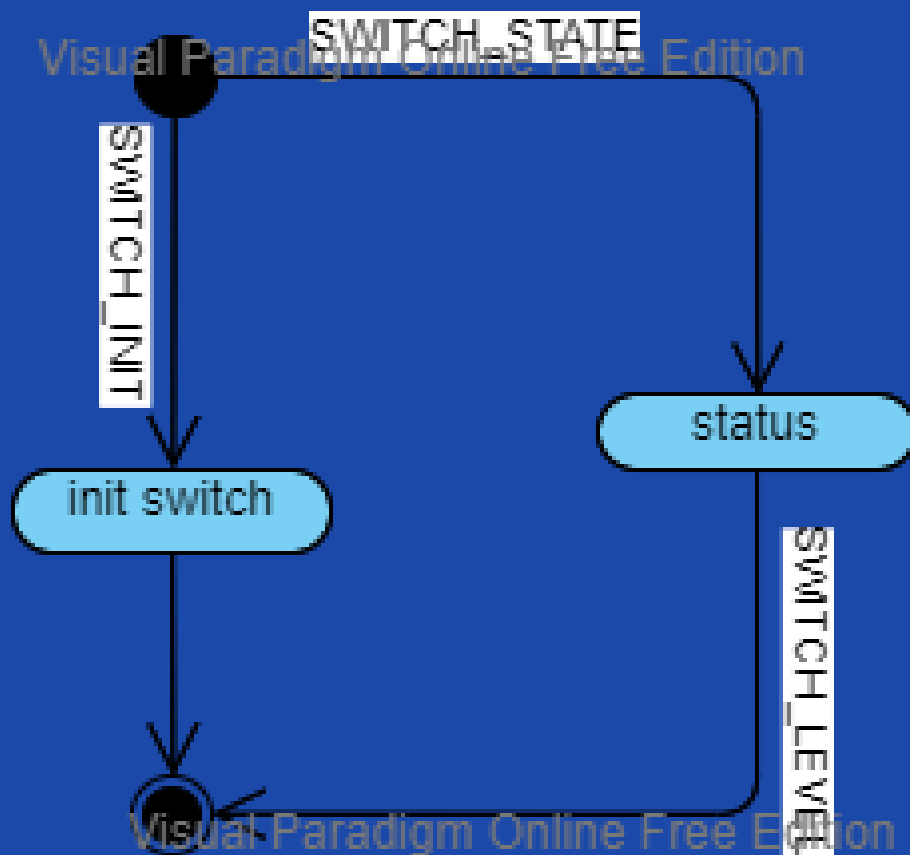
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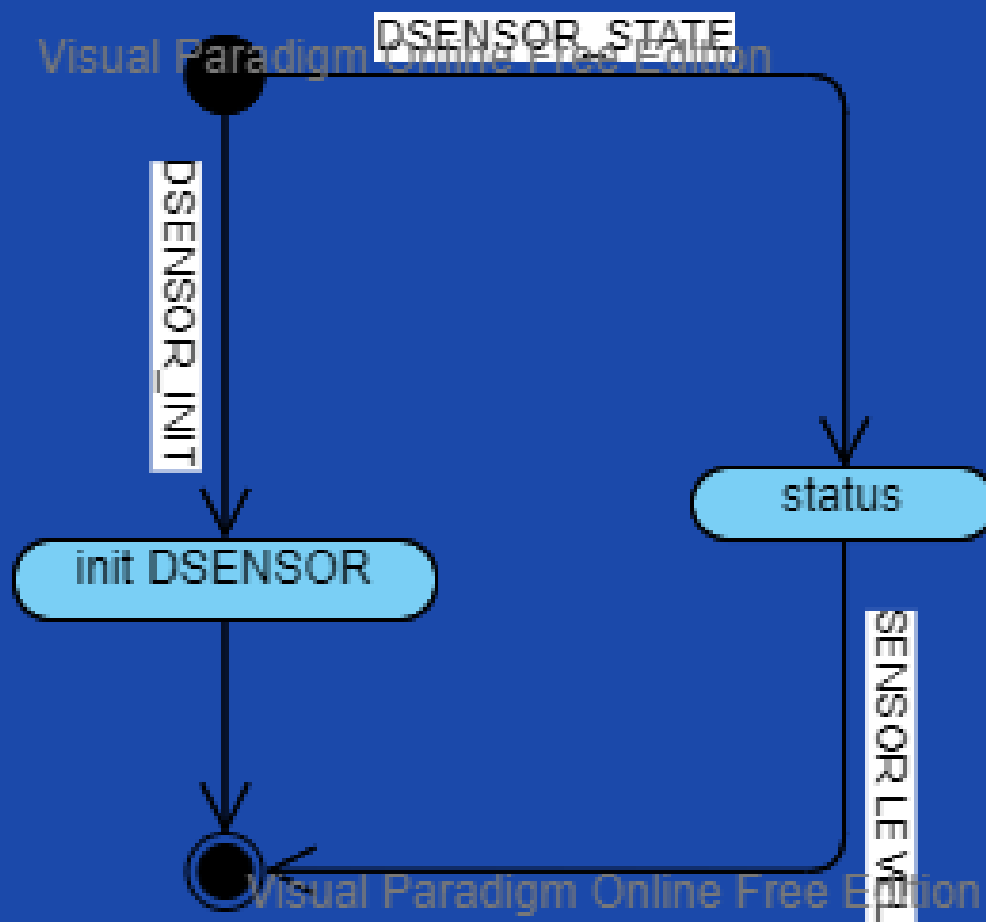


ECU1 COMPONENTS STATE MACHINE

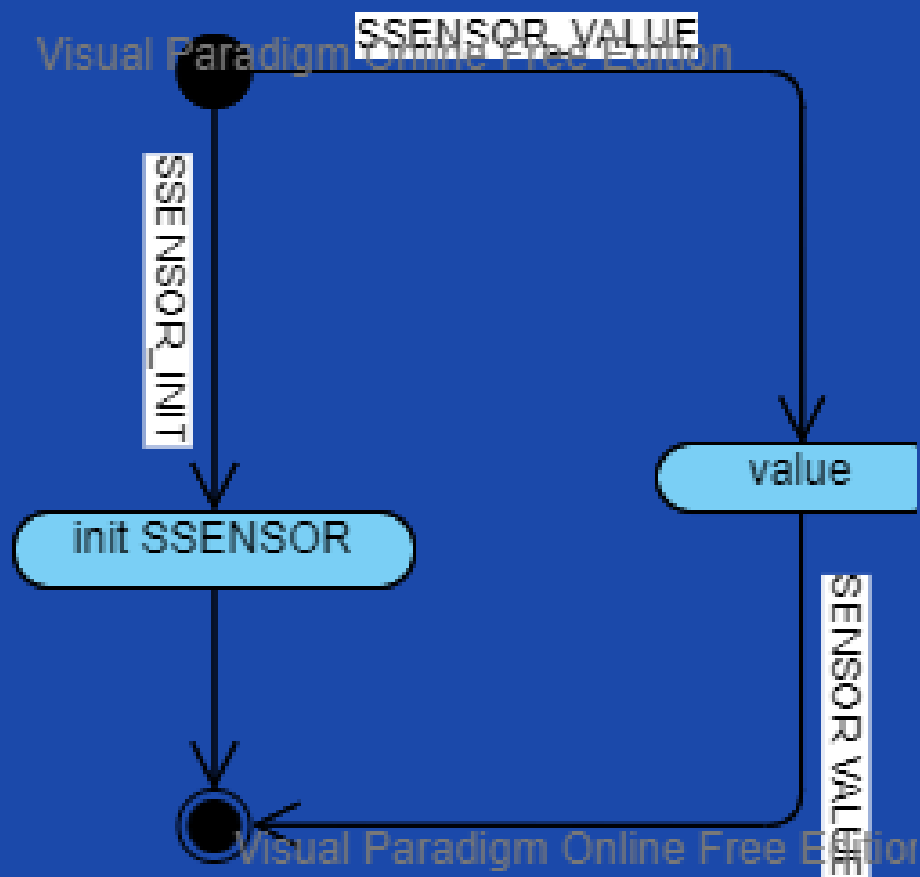
SWITCH



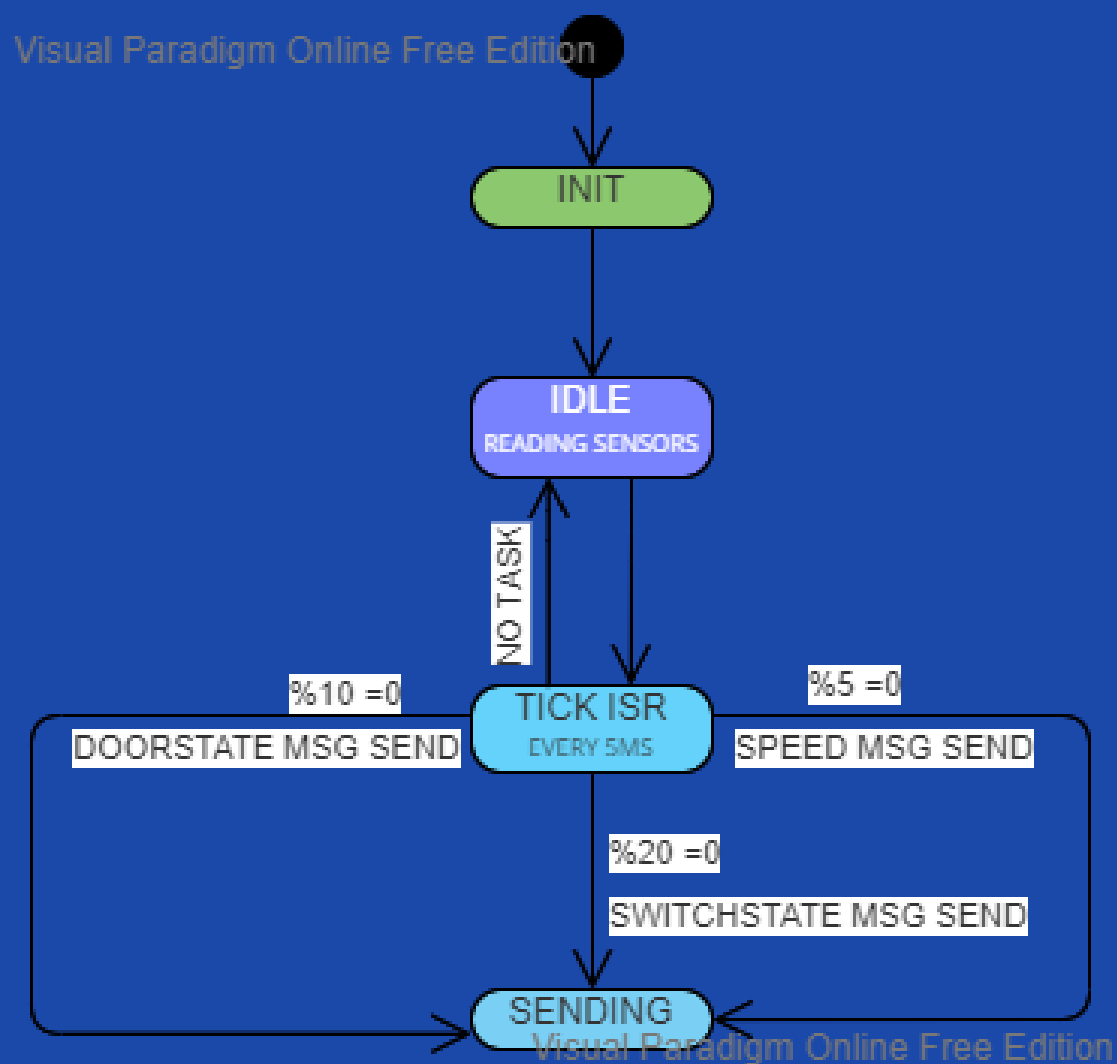
DOOR SENSOR



SPEED SENSOR



ECU1 STATE MACHINE

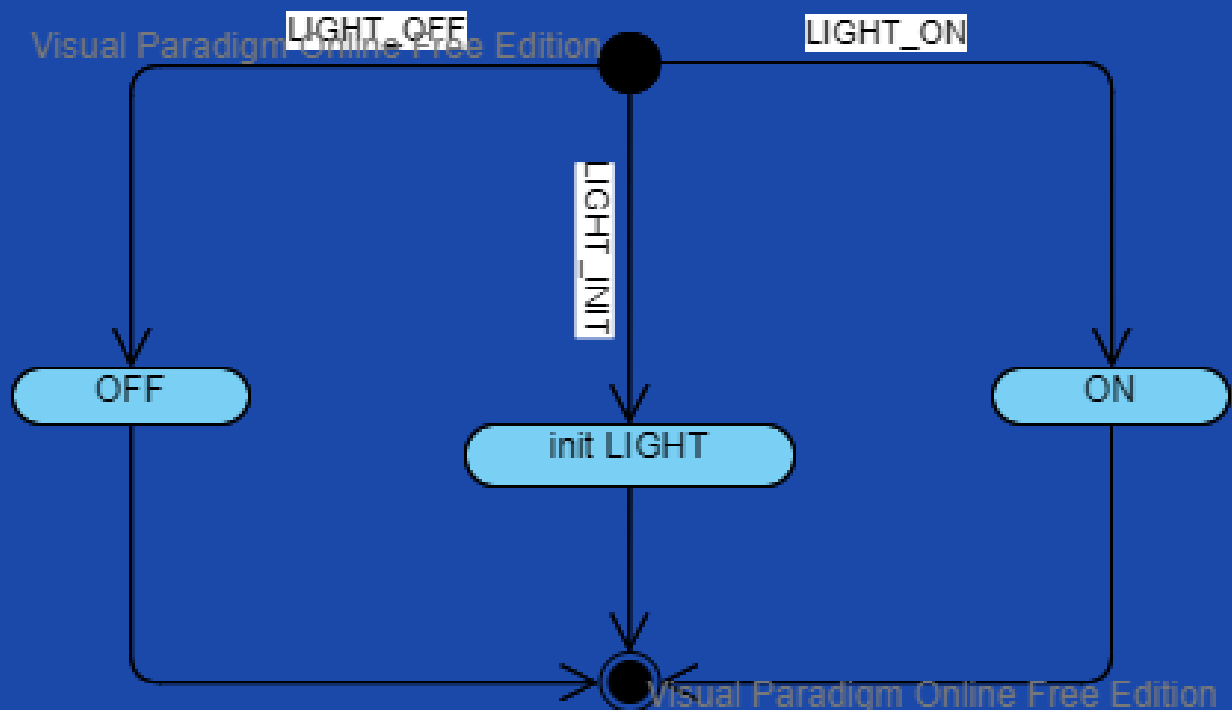


ECU1 SEQUENCE DIAGRAM

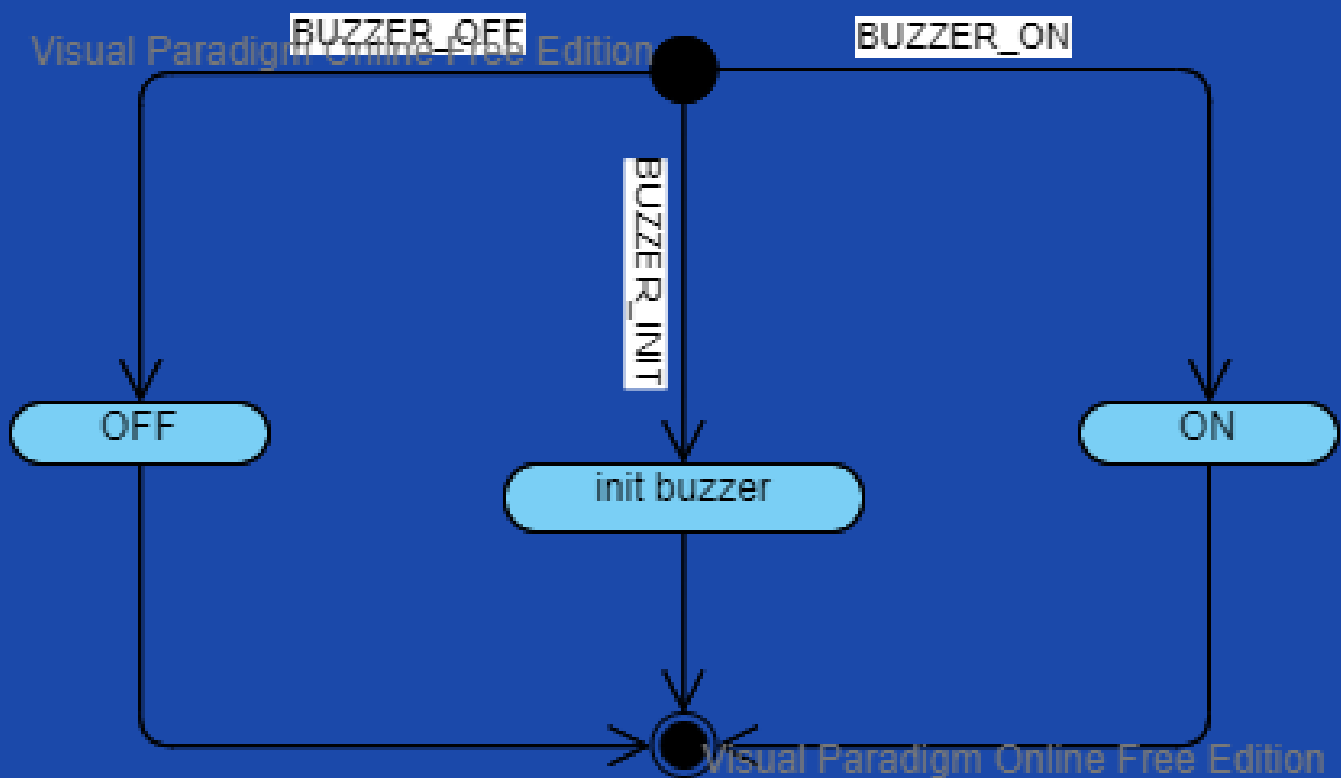


ECU2 COMPONENTS STATE MACHINE

LIGHT

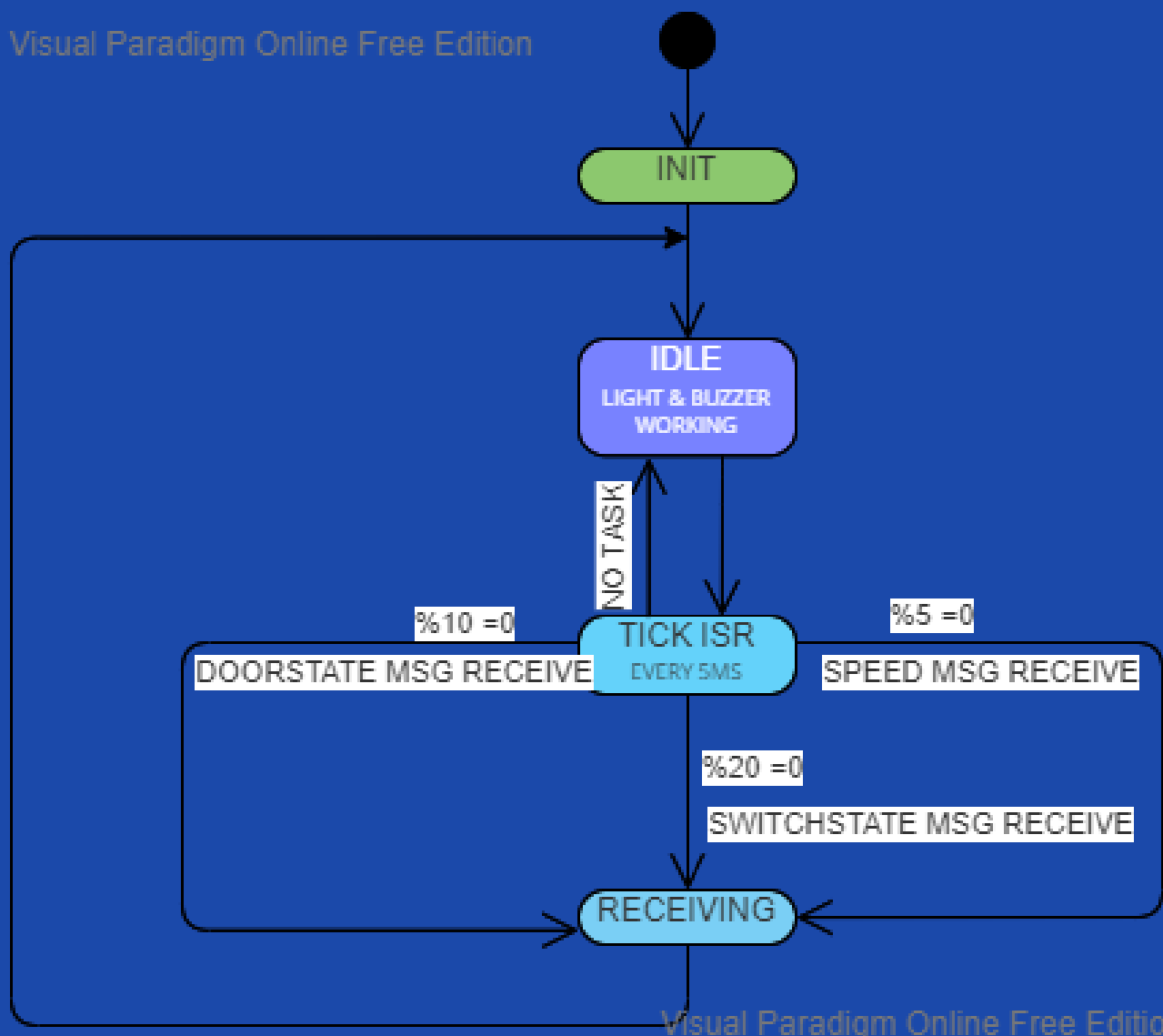


BUZZER



ECU2 STATE MACHINE

Visual Paradigm Online Free Edition



ECU2 SEQUENCE DIAGRAM



CPU LOAD

both of two CPUs are mainly loaded while tasks are processing

- speed sensor --> every 5 ms
- door sensor --> every 10 ms
- switch --> every 20 ms

hyperperiod = 20 ms

assuming all tasks have equal execution time which equals 1ms

then CPU load = $(4*1)+(2*1)+(1*1)/20 = 35\%$