

```

int main()
{
    ...

    RF_Front_end source();
    DDC ddc(CENTER_FREQUENCY, DECIMATION_RATE);
    FFT fft(SIZE);
    Display_Sink sink;

    SDR_Flow_Controller fc;

    fc.connect(&source, 0, ddc, 0);
    fc.connect(&ddc, 0, fft, 0);
    fc.connect(&fft, 0, sink, 0);

    fc.start();

    ...
}

```

```

template <> struct Traits<FFT>: public Traits<SDR_Processing_Block>
{
    static const bool on_hardware = true; //the FFT will be in hardware
};

```

