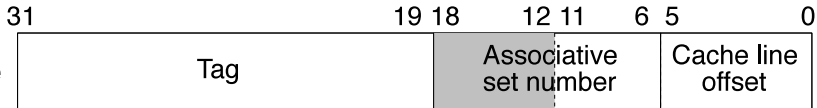
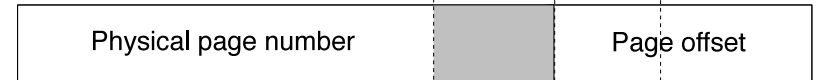


L3 cache



Physical Memory



OS-Controlled 7 bits HW-Controlled 6 bits