

# Siddhant Ray

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<https://github.com/Siddhant-Ray> (GitHub)

## Education

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- **MSc in Electrical Engineering and Information Technology** **ETH Zürich**  
Zürich, Switzerland 2020 – 2022
- **B.Tech in Electronics and Communication Engineering** **VIT Vellore**  
Vellore, Tamil Nadu, India 2016 – 2020
- **High School Certificate (CBSE)** **Delhi Public School Gurgaon**  
Gurgaon, Haryana, India 2014 – 2016

## Research Projects

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- **Advancing Packet-Level Traffic Predictions with Transformers** **ETH Zurich**  
*Master Thesis at the Networked Systems Group* Feb 2022 – Aug 2022
  - Developed a Transformer based neural network which can efficiently learning network dynamics and make smarter packet-level forwarding decisions.
  - Using features from both the packet and network state, we capture the underlying patterns in the traffic in a task-agnostic pre-training phase followed by task-specific fine-tuning phases to leverage this learnt behaviour, make quicker updates and make better decisions.
- **Towards a New Framework for Integration of Network Planes** **ETH Zurich**  
*Semester Thesis at the Networked Systems Group* Mar 2021 – Jun 2021
  - Created a prototype for a new forwarding node in programmable networks i.e. a Super-Node to have better and more dynamic control over forwarding decision in Layer-3 (L3) routers.
  - Our Super-Node consists of a traditional L3 router's control plane (CP) combined with a P4 programmable data plane (DP) for forwarding packets and we attempt accelerated forwarding and network convergence by leveraging the newly combined CP and DP.
- **Attentive Neural Networks for News Classification** **ETH Zurich**  
*Semester Thesis at the Chair for Mathematical Information Science* Feb 2021 – May 2021
  - Developed a Transformer based neural network to classify a multi-class hierarchical, context-overlapping news dataset.
  - Created a new statistical algorithm to detect context overlap in dataset classes, used it to reduce class label redundancy and demonstrated improvements of the classification model based on the algorithm's reduction
- **Machine Learning based Cell Association for 5G Communication Networks** **VIT Vellore**  
*International Journal of Mobile Network Design and Innovation* Dec 2019 – May 2020
  - Proposed a new cell association scheme to meet the ultra low latency, higher load and traffic needs of the 5G networks.
  - Proposed a Hidden Markov Model based learning algorithm followed by a Viterbi based decoding scheme, on the network's telemetry data, to learn network parameters and select the optimal eNodeB for cell association.

## Course Projects

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- **Deep Learning:** *Investigating Possible Inductive Biases in Local Sparse Attention ViT Architectures Against Traditional CNNs, ETH Zurich 2021*
- **Network Security:** *Automatic Certificate Management Environment framework, ETH Zurich 2021*
- **Advanced Computer Networks:** *Maximizing Cross Traffic Flows in a L2/L3 Network with Programmable Switches, ETH Zurich 2020*

## Professional Experience

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- **Law, Economics, and Data Science Group, ETH Zurich** **Zürich, Switzerland**  
*Graduate Research Assistant* *Oct 2021 –*
  - Research Assistant to Professor Dr. Elliott Ash and currently working on improving semantic labelling for text corpora using newer NLP models and sentence simplification and clustering for topic modelling.
  - Working on a paraphrase mining project to determine clusters of similar narratives in legal corpora and also using NLP models to capture underlying narratives in meat policy documents and analyse societal impacts and political discourse.
- **Capgemini Engineering** **Gurgaon, Haryana, India**  
*Networking Intern* *May 2019 – July 2019*
  - Developed a K-Shortest Path Searching algorithm for the ONOS platform in a Java environment, automated and deployed using Maven.
  - The algorithm was subject to dynamic constraints in terms of network resources such as required edges and vertices etc. and was used for path calculation for deployed in in Software Defined Layer 2 VPNs.
- **BlueStacks** **Gurgaon, Haryana, India**  
*Summer Intern* *May 2018 – July 2018*
  - Worked on a machine learning algorithm to predict the App Engine's appropriate display screen based on the customer's past experiences.
  - Developed an automation script for generating SVG cards for the App Engine's game front end and also worked on an address verification tool using the EasyPost API.

## Skills

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- **Programming:** Python, C++, Java, Bash, Rust, SQL, C
- **Tools:** Linux, Git, TeX, Docker, P4 switches, ONOS controller, Google Cloud, AWS, Maven, MATLAB, NetSim, Cadence
- **Frameworks:** Mininet, FRR suite, PyTorch, TensorFlow, Sklearn, NLTK, Flask, SciPy, Scapy, Spacy, BeautifulSoup, NS3
- **Natural Languages** – English(C1), Hindi, Bengali, Deutsch(B1)

## Relevant Courses

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- **CS & EE** - Advanced Computer Networks, System Security, Network Security, Distributed Computing, Discrete Event Systems, Computer Networks Seminar, Computer Networks, Operating Systems, Wireless Communication
- **Math & ML** - Introductory Machine Learning, Deep Learning, Learning and Classification Theory, Mathematics of Data Science, Neural Network Theory, Linear Algebra

## Honors and Awards

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- Winner at Datathon 2022, ETH Zurich (Microsoft Challenge) – (2022)
- Best Outgoing Student (SENSE department, VIT Vellore) – (2020)
- Runner-Up at VIT Hack 2019, VIT Vellore (Education Track) – (2019)
- Merit Scholarship for Academic Excellence, VIT Vellore – (2016-17, 2017-18, 2018-19)

## Leadership and Volunteering

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- Technical Advisor at IETE VIT – (2019-20)
- President at Anokha NGO – (2018-19)
- Organizer at TEDx VIT Vellore – (2018-19)
- Outreach Worker at Anokha NGO – (2017-18, 2018-19, 2019-20)