Assignment 1

Q1 Write down the specifications (ex. cores, memory, Architecture etc...) of your system with the screenshot of the specification and its explanation (what each term is representing). Explanation should be handwritten?

```
Byte Order:
 On-line CPU(s) list:
   Socket(s):
   CPU max MHz:
   CPU min MHz:
   BogoMIPS:
                        fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mc
                         a cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss
                         tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds cp
Virtualization features:
```

Ans 1: Architecture

The Architecture field shows what type of CPU you have.

In the example above, X86_64 represents 64bit extension of X86 instruction set. Consists of rules and methods or procedures which describe the implementation, functionality of the computer systems.

<u>CPU op-mode</u> — <u>displays operation mode</u>.

The CPU op-mode(s) option in the command output tells whether the given Linux is 32 or 64 bits. If it shows 32-bit or 64-bit then Linux is 64 bits as it supports both 32-and 64-bit memory.

Address sizes

The physical address of a CPU is 39-bit, the virtual address size gives you the size of the virtual address space that is how much memory a single program can address (48-bit).

Byte order

Displays endianness (order in which bytes are arranged in memory).

CPU (s)

Displays number of logical CPUs as seen by operating system. The list of on-line CPU (s) list is of: 0-7

Caches

CPU MHz — Clock speed of processor.

Processor performance is sometimes measured in IPC (Instructions per cycle) / CPI (Cycles per instructions). IPCs/CPIs can further be increased through various methods e.g., pipelining. However, higher CPU speed doesn't necessarily mean better performance. What matters is what is being done during CPU cycles. Often Times, CPU is less of bottleneck than memory access or disk access.

L1d cache: Size of L1d cache. (Data cache)

L1i cache: Size of L1i cache. (Instruction cache)

L2 cache: Size of L2 cache.

L3 cache: Size of L3 cache.

Q2. What is the speed of the processor of your system?

Ans: 3.60GHz

A computer's processor clock speed determines how quickly the central processing unit (CPU) can retrieve and interpret instructions.

Q3. what is the byte order of your system and what does it represent?

Ans: The byte order is little endian. It represents the order in which bytes are arranged in memory.

Q4. What is the range of numbers you can store in your system. (Minimum number after which underflow takes place and maximum number after which overflow take place) with the screenshot and explanation of the experiment?

Ans: Minimum value :- (2^63) =- $(9*10^19)$ Maximum value: $2^63-1=9*10^19$

Q5 Write a simple Hello world program?

Ans:

```
#include <stdio.h>
    int main () {
        printf("Hello world");
        return 0;
}
```

```
1 #include<stdio.h>
2 int main() {
3    printf("Hello world");
4    return 0;
5 }
```